

## M8216/M8226 **4-BIT PARALLEL BIDIRECTIONAL BUS DRIVER**

Military

- Not Recommended for New Designs
- Data Bus Buffer Driver for M8080A CPU
- Low Input Load Current: 0.25 mA Maximum
- High Output Drive Capability for Driving **System Data Bus**
- 16-Pin Dual-In-Line Package

- 3.40V Output High Voltage for Direct Interface to M8080A CPU
- 3-State Outputs
- Military Temperature Range: -55°C to + 125°C (Tc)
- ± 10% Power Supply Tolerance

The M8216/M8226 is a 4-bit directional bus driver/receiver. All inputs are low power TTL compatible. For driving MOS, the DO outputs provide a high 3.40V VOH, and for high capacitance terminated bus structures, the DB outputs provide a high 50 mA IOI capability. A non-inverting (M8216) and an inverting (M8226) are available to meet a wide variety of applications for buffering in microcomputer systems.

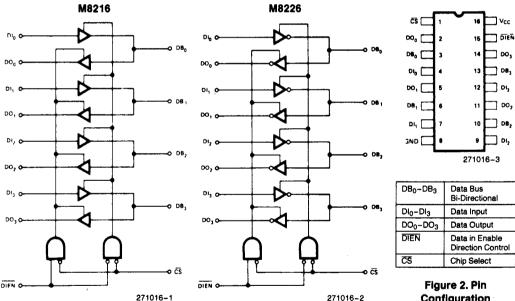


Figure 1. Logic Diagrams

Configuration



# A.C. CHARACTERISTICS $T_C = -55^{\circ}C$ to $+125^{\circ}C$ , $V_{CC} = +5V \pm 10\%$

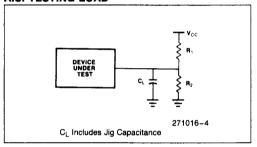
Symbol	Parameter	Limits			Unit	Conditions
		Min	Typ(1)	Max		
T <sub>PD1</sub>	Input to Output Delay DO Outputs		15	25	ns	(Note 2)
T <sub>PD2</sub>	Input to Output Delay DB Outputs M8216		19	33	ns	(Note 2)
T <sub>PD2</sub>	Input to Output Delay DB Outputs M8226		16	. 26	ns	(Note 2)
Τ <sub>E</sub>	Output Enable Time M8216		42	75	ns	(Note 2)
Τ <sub>E</sub>	Output Enable Time M8226		36	62	ns	(Note 2)
TD	Output Disable Time M8216		16	40	ns	(Note 2)
T <sub>D</sub>	Output Disable Time M8226		16	38	ns	(Note 2)

#### NOTES:

1. Typical values are for  $T_C = 25^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V}$ .

Test	CL	R <sub>1</sub>	R <sub>2</sub>
T <sub>PD1</sub>	30 pF	300Ω	600Ω
T <sub>PD2</sub>	300 pF	90Ω	180Ω
T <sub>E</sub> , (DO, ENABLE ↑)	30 pF	10 KΩ	1 ΚΩ
T <sub>E</sub> , (DO, ENABLE ↓)	30 pF	300Ω	600Ω
T <sub>E</sub> , (DB, ENABLE ↑)	300 pF	10 ΚΩ	1 ΚΩ
T <sub>E</sub> , (DB, ENABLE ↓)	300 pF	90Ω	180Ω
T <sub>D</sub> , (DO, DISABLE↑)	5 pF	300Ω	600Ω
T <sub>D</sub> , (DO, DISABLE↓)	5 pF	10 ΚΩ	1 ΚΩ
T <sub>D</sub> , (DB, DISABLE ↑)	5 pF	90 KΩ	180Ω
T <sub>D</sub> , (DB, DISABLE↓)	5 pF	10 KΩ	1 KΩ

### A.C. TESTING LOAD



#### **WAVEFORMS**

