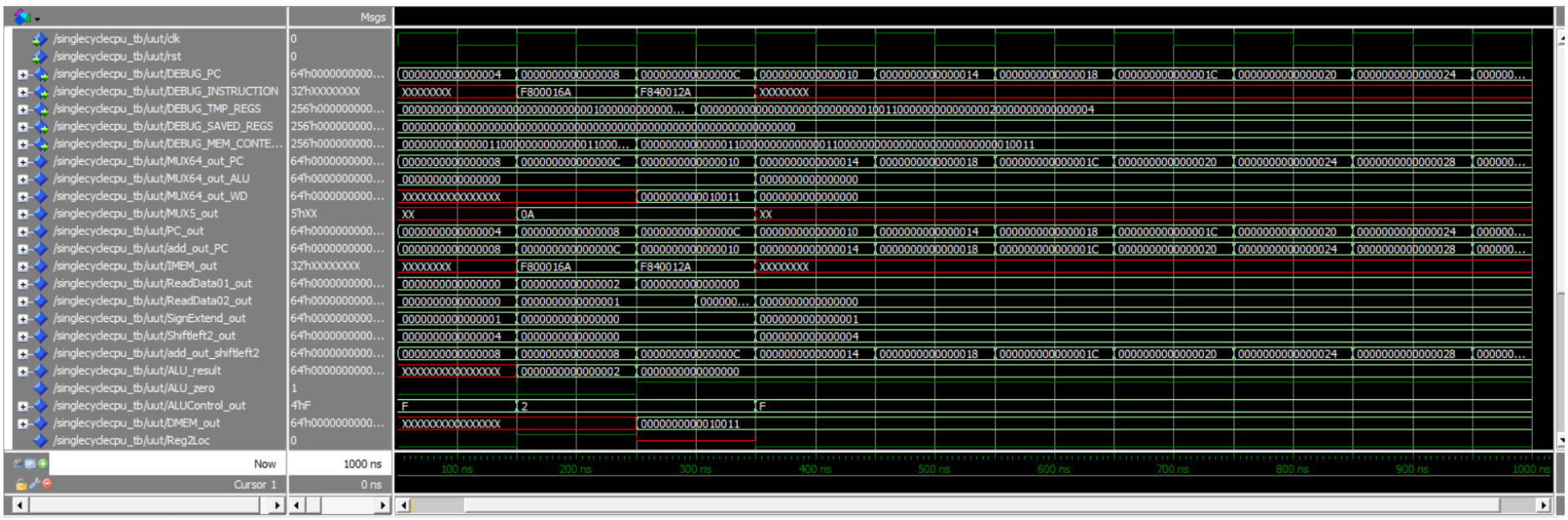
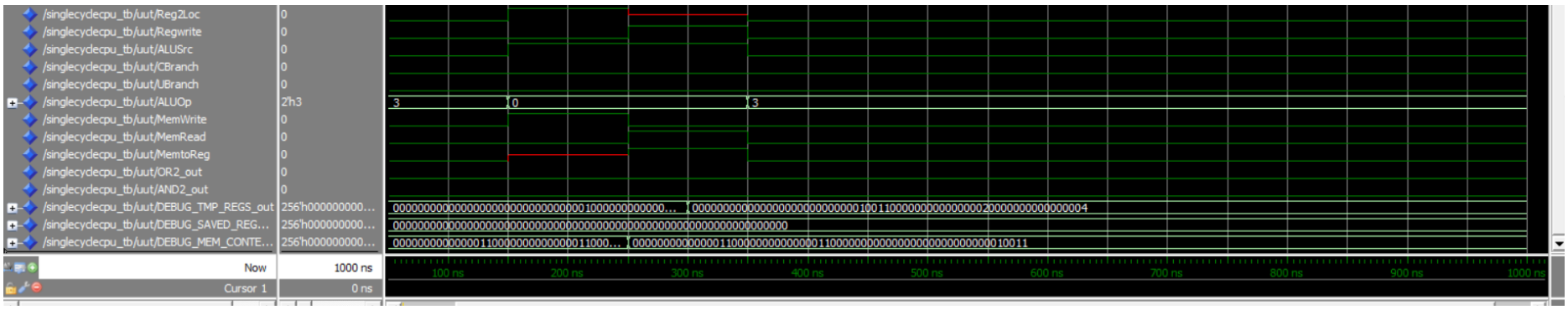

Msgs
/singlecycdecpu_tb/uut/clk
/singlecycdecpu_tb/uut/rst
/singlecycdecpu_tb/uut/DEBUG_PC
/singlecycdecpu_tb/uut/DEBUG_INSTRUCTION
/singlecycdecpu_tb/uut/DEBUG_TMP_REGS
/singlecycdecpu_tb/uut/DEBUG_SAVED_REGS
/singlecycdecpu_tb/uut/DEBUG_MEM_CONTE...
/singlecycdecpu_tb/uut/MUX64_out_PC
/singlecycdecpu_tb/uut/MUX64_out_ALU
/singlecycdecpu_tb/uut/MUX64_out_WD
/singlecycdecpu_tb/uut/MUX5_out
/singlecycdecpu_tb/uut/PC_out
/singlecycdecpu_tb/uut/add_out_PC
/singlecycdecpu_tb/uut/IMEM_out
/singlecycdecpu_tb/uut/ReadData01_out
/singlecycdecpu_tb/uut/ReadData02_out
/singlecycdecpu_tb/uut/SignExtend_out
/singlecycdecpu_tb/uut/Shiftleft2_out
/singlecycdecpu_tb/uut/add_out_shiftleft2
/singlecycdecpu_tb/uut/ALU_result
/singlecycdecpu_tb/uut/ALU_zero
/singlecycdecpu_tb/uut/ALUControl_out
/singlecycdecpu_tb/uut/DMEM_out
/singlecycdecpu_tb/uut/Reg2Loc
Now
1000 ns
Cursor 1
0 ns
100 ns
200 ns
300 ns
400 ns
500 ns
600 ns
700 ns
800 ns
900 ns
1000 ns


/singlecycdecpu_tb/uut/Reg2Loc
/singlecycdecpu_tb/uut/Regwrite
/singlecycdecpu_tb/uut/ALUSrc
/singlecycdecpu_tb/uut/CBranch
/singlecycdecpu_tb/uut/UBranch
/singlecycdecpu_tb/uut/ALUOp
/singlecycdecpu_tb/uut/MemWrite
/singlecycdecpu_tb/uut/MemRead
/singlecycdecpu_tb/uut/MemtoReg
/singlecycdecpu_tb/uut/OR2_out
/singlecycdecpu_tb/uut/AND2_out
/singlecycdecpu_tb/uut/DEBUG_TMP_REGS_out
/singlecycdecpu_tb/uut/DEBUG_SAVED_REG...
/singlecycdecpu_tb/uut/DEBUG_MEM_CONTE...
Now
1000 ns
Cursor 1
0 ns
100 ns
200 ns
300 ns
400 ns
500 ns
600 ns
700 ns
800 ns
900 ns
1000 ns