| 1 • | Msgs | | | | | | | | | |
|--|--------------------|------------|---|---|-------------|-------------|-----------|-----------|--|-----------|
| /singlecyclecpu_tb/uut/clk | 0 | | | | | | | | | |
| /singlecyclecpu_tb/uut/rst | 0 | | | | | | | | | |
| =-🔩 /singlecydecpu_tb/uut/DEBUG_PC | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 000000000 | 800000 | 000000000 | 00000C | 000000000 | 0000010 | 000000 |
| | 32'hXXXXXXXX | 9100056A | 9100096A | 91000529 | | D1000529 | | 8B0B012A | | XXXXXXXXX |
| //singlecydecpu_tb/uut/DEBUG_TMP_REGS | 256'h0000000000 | 000000000 | 0000000 \$0000 | 00000ф000000 | 000000000 | 0000001 | 000000000 | 0000000 | 0000000000 | 0000000 |
| | 256'h0000000000 | 000000000 | 00000000000000000000000000000000000000 | 000000000000000000000000000000000000000 | 0000000000 | 00000000000 | 000 | | | |
| 🛂 🔩 /singlecydecpu_tb/uut/DEBUG_MEM_CONT | 256'h0000000000 | 000000000 | 0000011000000000 | 00000 1100000000 | 00000001100 | 0000000000 | 011 | | | |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 8 000000000 | 00000C | 000000000 | 0000010 | 000000000 | 0000014 | 000000 |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 2 000000000 | 0000001 | | | 000000000 | 0000002 | 000000 |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 4 000000 | 000000 | 000000 | FFFFFF | 000000000 | 0000002 | |
| | 5'hXX | 00 | | | | | | OB | | XX |
| | 64'h00000000000 | 000000 | X 000000000000000000000000000000000000 | 4 000000000 | 800000 | 000000000 | 00000C | 000000000 | 0000010 | 000000 |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 8 000000000 | 00000C | 000000000 | 0000010 | 000000000 | 0000014 | 000000 |
| | 32'hXXXXXXXXX | 9100056A | 9100096A | 91000529 | | D1000529 | | 8B0B012A | | XXXXXXXX |
| | 64'h00000000000 | 000000000 | 0000002 | 000000 | 000000000 | 0000001 | 000000000 | 0000000 | | |
| | 64'h00000000000 | 000000000 | 0000000 | | | | | 000000000 | 0000002 | 000000 |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 2 000000000 | 0000001 | | | | | |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 8 000000000 | 0000004 | | | | | |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | C 000000000 | 00000C | 000000000 | 0000010 | 000000000 | 0000014 | 000000 |
| | 64'h00000000000 | 000000 | 000000000000000000000000000000000000000 | 4 000000 | 000000 | 000000 | FFFFFF | 000000000 | 0000002 | |
| /singlecydecpu_tb/uut/ALU_zero | 0 | | | | | | | | | |
| | 4'hF | 2 | | | | 6 | | 2 | | F |
| | 64'hXXXXXXXXXXXXXX | XXXXXXXXXX | XXXXXXX | | | | | | | |
| /singlecyclecpu_tb/uut/Reg2Loc | 0 | | | | | | | | | |
| ■ ● Now | 500 ns | 111111111 | 100 ns | 20 | 0 ns | 300 | ns) | 400 | liiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiiii | 500 |

