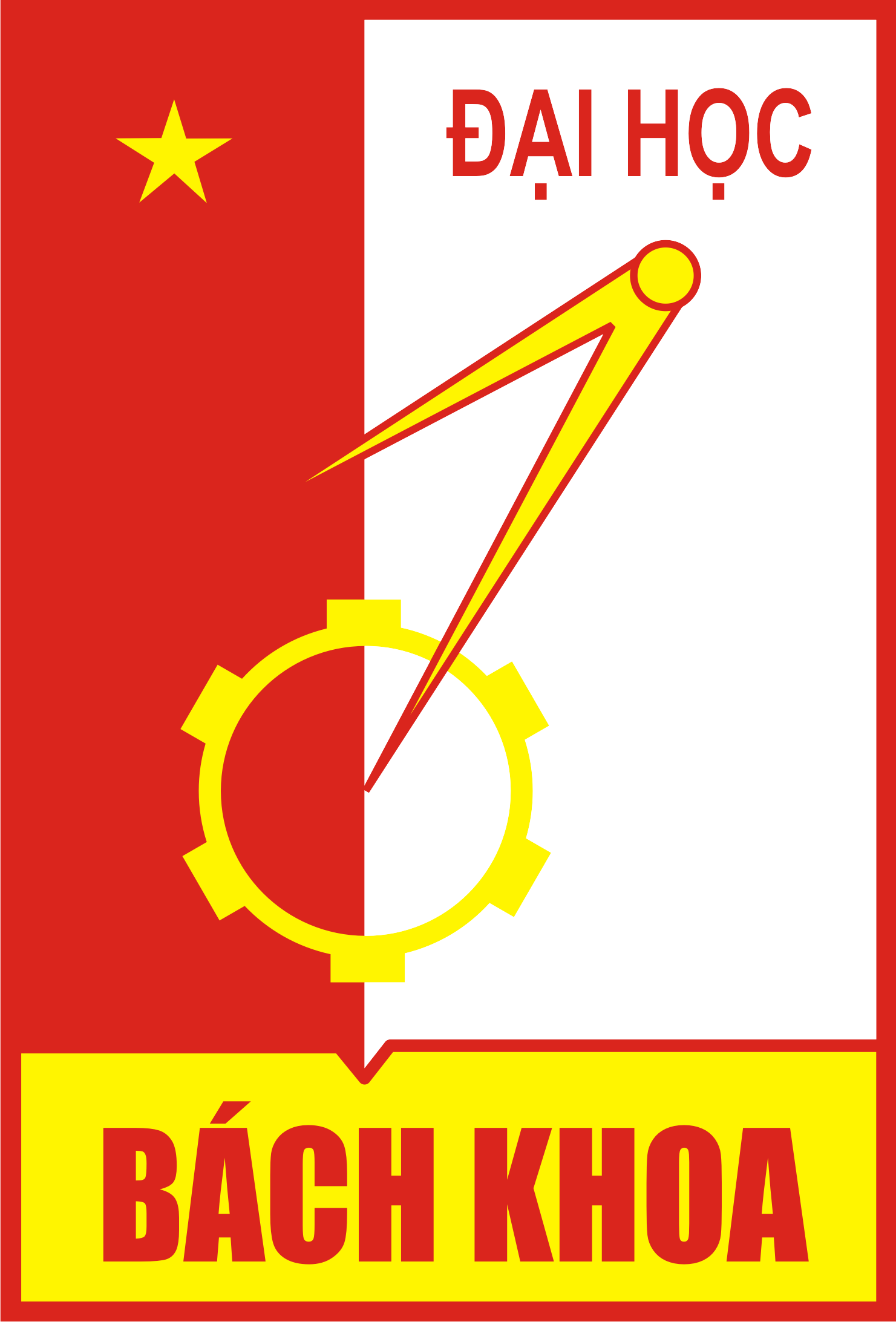
Hanoi University of Science and Technology  
School of Information and Communication Technology



**Final Project Report**

Assembly Language and Computer Architecture Laboratory

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| **LECTURER:** | **Prof. Nguyen Thi Thanh Nga** |
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| **GROUP:** | **1** |

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*Hanoi, 6/2020*

***Topic 3* : Typing speed and accuracy checking**

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1. Project description
   1. **Topic**

Check user input against a given string and output the number of correct characters to 7-leds.

* 1. **Requirement**

Number of correct characters is updated on the 7-leds every interval

of time counter.

* 1. **Advanced idea**

Input is refreshed everytime user press *Enter.*

1. Variables and Constants
   1. **Constants**

- MASK\_CAUSE\_COUNTER: mask to identify if counter is the cause of interupt.

- MASK\_CAUSE\_KEYBOARD: mask to identify if pressed keyboard is the cause of interupt.

- KEY\_READY: address which hold value showing if any key is pressed: 1 for true, 0 otherwise.

- KEY\_CODE: address which hold ASCIIZ value of key pressed.

- DISPLAY\_READY: address which hold value showing if screen is ready to display: 1 for true, 0 otherwise.

- KEY\_CODE: address which hold ASCIIZ value to be displayed.

- SEVENSEG\_LEFT: load value to display left 7-led.

- SEVENSEG\_RIGHT: load value to display right 7-led.

- COUNTER: 1 to active counter, 0 to disable.

* 1. **Variables**

- target\_string: string used to compare correct characters.

- length: length of target\_string.

- input\_string: string which stores value which the user inputs.

- max\_input\_length: max length of input that input\_string will hold.

- display\_num: value to be displayed on 7-leds.

- last\_display: value currently displayed on 7-leds.

1. Functions
   1. **Main**
   2. **Used registers**

- $k0: store KEY\_CODE value.

- $k1: store KEY\_READY value.

- $s0: store DISPLAY\_CODE value.

- $s1: store DISPLAY\_READY value.

- $t1: hold pressed key value.

* 1. **Logic**

Infinite loop waiting for interrupt(s).

* 1. **Interrupt Handler**
  2. **Used registers**

- $t1: hold pressed key value.

- $t2, $t3, $t4, $a0, $a1, $t8, $t9: hold temporary data, condition results.

* 1. **Logic**

- Find the cause of interruption and navigate to the apppropriate handler

- If the counter is interrupted: re-display 7-leds value if changed

- If the keyboard is interrupted:

+ If the key pressed has ASCIIZ value of 10 (*Enter*): refresh display, input\_string.

+ Else: append to input\_string, compare with target\_string and update display\_num.

* 1. **Push**
  2. **Used registers**
  3. **Logic**

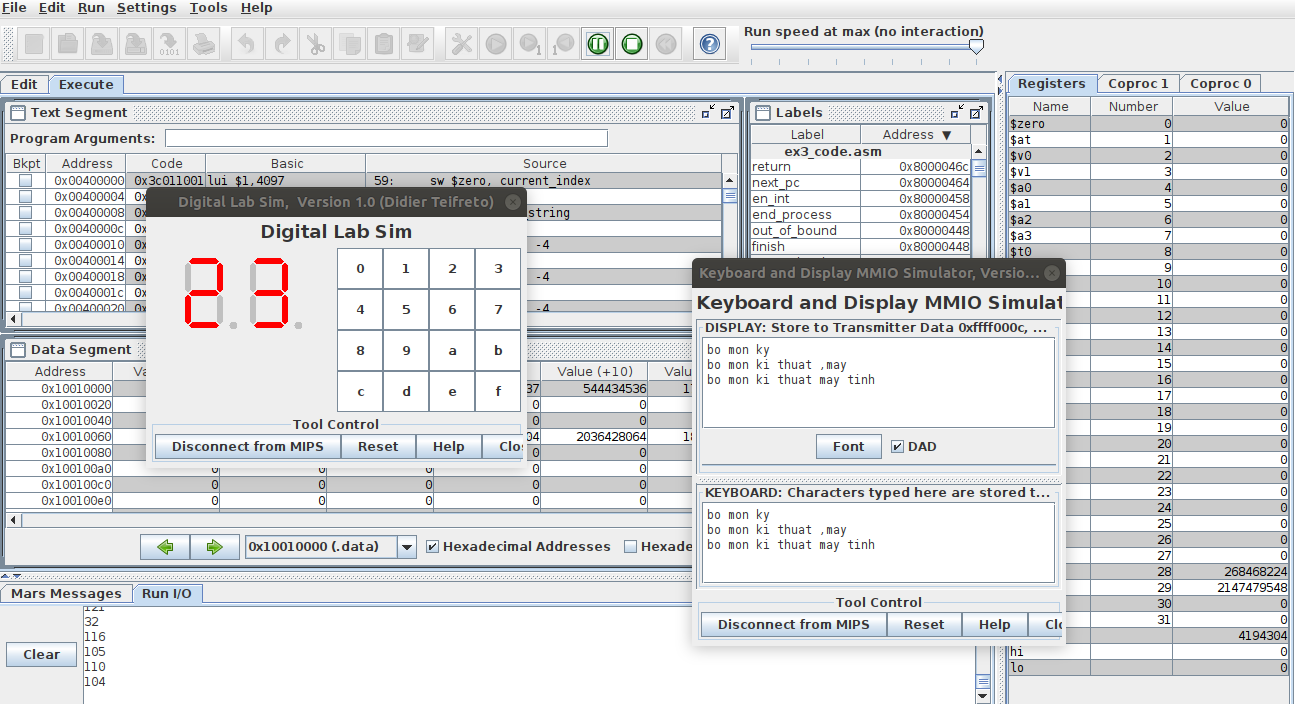
- Push value in register to stack.

* 1. **Pop**
  2. **Used registers**
  3. **Logic**

- Pop value from stack to register.

* 1. **get\_length\_reg**
  2. **Used registers**
  3. **Logic**

- Get the length of an input string, whose address is stored in a register.

1. Simulation Sample Result

***Topic 7* : MIPS basic instruction syntax checking**

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# Project description

* 1. **Topic**

A program to simulate a specific task of a MIPS compiler, which is opcode syntax checking (basic instructions only).

* 1. **Requirements**
* User enters a mips instruction.

Ex : beq $t1, $t2, label

* Check if input opcode exists.
* If input opcode exists, continue to check its operands’ validation behind.
* If input mips instruction is valid, print out the cycles in which it is executed.

# Algorithms

* 1. **Main**
     1. **Registers used**
* $a0: input string, opcode cycles
* $a1: classified group of opcodes
* $a2: opcode
* $a3: string of operands
* $v0: flag -> 0/1
  1. **If-else check**

*Note: Opcode and the number of cycles is found by using “check\_substring\_appearance” defined in Utils below.*

|  |  |
| --- | --- |
| **Step 1** | Read input string from user |
| **Step 2** | Split string into 2 parts: *Opcode* & *Operands* |
| **Step 3** | Use branch instructions to find a possible *Opcode* in every classified group |
| **Step 4** | If *Opcode* is not found, then branch to the next label to find it. (brute force search)  If *Opcode* is not found in any groups, then print an invalid message to console |
| **Step 5** | If *Opcode* is found, start checking the *Operands* part by using the global variables and validation-checking functions defined in resource file |
| **Step 6** | If operands part is valid, print valid valid message console, followed by the its number of cycles, which could be found by using the global variables and functions defined in resource and utils file (*reg\_char\_first\_pos*) |

* 1. **Utils**
  2. **Purpose**

To define subprograms, which will be supportively used for the miniprojects.

*Note: In this miniproject, I use macros to define every subprogram for better understanding.*

* 1. **Important functions**

1. ***check\_in\_reg\_range(%int\_register, %lower\_bound, %upper\_bound)***

* Purpose: to check if an integer stored in a register is in range [lower\_bound, upper\_bound]
* Return: $v0 = 1 if true, 0 if false

1. ***check\_label(%string\_reg)***

* Purpose: to check if a label stored in a register is valid or not
* Return: $v0 = 1 if true, 0 if false

1. ***check\_substring\_appearance(%string\_reg, %substring\_reg)***

* Purpose: to check if a substring belongs to a given string in which words are split by spaces

Ex: substring “c” in string “a b c d e”

* Return: $v0 = 1 if found, 0 if not found

1. ***convert\_num\_hex(%register\_string)***

* Purpose: to convert a valid string of 16-bit hexadecimal to decimal
* Return: $v0 = num

1. ***is\_hexa(%register\_string)***

* Purpose: to check if a string stored in a register is a valid 16-bit hexadecimal or not

Ex: 0xAF14, 0x0032E6, 0x12345678

* Return: $v1 = 1, $v0 = convert\_num\_hex(%register\_string) if valid

$v1 = 0, $v0 = 0 if invalid

1. ***is\_num(%register\_string)***

* Purpose: to check if a string stored in a register is a valid integer or not
* Return: $v1 = 0, $v0 = 0 if invalid

$v1 = 1, $v0 = num(decimal) if valid

1. ***reg\_char\_first\_pos(%char\_reg, %string\_reg)***

* Purpose: to find the first position of a character in a string, both stored in registers
* Return: $v0 = position if found, -1 if not found

1. ***split\_by\_literal\_separator(%string\_reg, %char)***

* Purpose: to split a string stored in a register into 2 substrings, using separator character
* Return: if %char is found:

$a2 = the first half of the string split by this function

$a3 = the address of the second half of the string

(remove redundant spaces as many as possible)

else: $a2 = %string\_reg

$a3 = null

Ex: $t0 = “add $t1 , $t2, $t3 “

split\_by\_literal\_separator($t0, “ “)

* $a2 = “add”
* $a3 = “$t1 , $t2, $t3”

**3. Resource**

* 1. **Global variables**

*Note: In this miniproject, I classify:*

*+ every opcode stored in a specific string named based on operands and number of cycles*

*+ every register based on type*

*Ex: cee : condition flag, even-float-register, even-float-register*

*es16\_i : even-float-register, signed\_16\_bit\_integer(integer\_register)*

* Abbreviation of operands list:

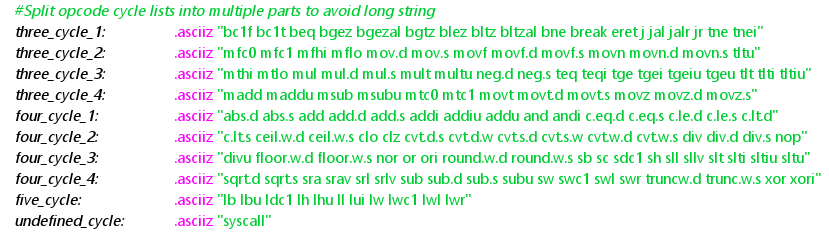
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| --- | --- |
| **Abbreviation** | **Meaning** |
| c | Condition flag (0-7) |
| e | Even float register ($f0, $f2, …, $f30) |
| f | Float register ($f0, $f1, …, $f31) |
| i | Integer Register ($zero, $at, …, $ra, $0, $1, …, $31) |
| 8 | Coprocessor 0 ($8, $12, $13, $14) |
| l | Label |
| s16 | Signed 16-bit integer |
| u16 | Unsigned 16-bit integer |
| u5 | Unsigned 5-bit integer |
| none | No operands |

* Classified groups of opcodes based on operands:

A screenshot of a cell phone

Description automatically generated

* Classified groups of opcodes based on the number of cycles:



* Classified groups of registers based on type:

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* 1. **Operands validation-checking functions**
* **Function Format: *valid\_[group](%string\_of\_operands)***
* **Return: $v0 = 1 if valid, 0 if invalid**

*Note: For each classified groups of opcodes based on operands, I define a specific operand validation-checking function.*

As most of operand validation-checking functions share similarities, I only make a general description of algorithm based on the number of operands.

1. *3 operands (Ex: add $t0, $t1, $t2)*

|  |  |
| --- | --- |
| **Step 1** | Load the suitable group of opcodes depending on its name to $t0  Ex: Function “valid\_cee” needs Group “cee” |
| **Step 2** | * Split the string of operands into 2 parts with separator ‘,’ (comma), using function split\_by\_literal\_separator defined in Utils, then we have $a2 = the first operand,   $a3 = the rest of $t0  Ex: “$t0, $t1, $t2” -> split\_by\_literal\_separator($t0, ‘,’)   * $a2 = “$t0” * $a3 = “$t1, $t2” * Based on the operand’s type, check if that operand exists, by using:   + “check\_substring\_appearance” if operand is a register  + “is\_num”/”is\_hexa” && ”check\_int\_reg\_range” if operand is a number/offset  + “check\_label” if operand if a label   * If valid, go to the next step, else invalid |
| **Step 3** | Similar to Step 2, split the string of operands in $a3 to get the second operand by split\_by\_literal\_separator($a3, ‘,’), then check the validation of it  Ex: “$t1, $t2” -> split\_by\_literal\_separator($a3, ‘,’)   * $a2 = “$t1” * $a3 = “$t2”   If valid, go to the next step, else invalid |
| **Step 4** | Repeat Step 3 for the third operand  Ex: “$t2” -> split\_by\_literal\_separator($a3, ‘,’)   * $a2 = “$t2” * $a3 = null |
| **Step 5** | Check if the fourth operand exists by getting the string length of $a3. If length > 0 return invalid, else valid |

1. *2 operands (Ex: bc1f 1, label)*

|  |  |
| --- | --- |
| **Step 1** | Load the suitable group of opcodes depending on its name to $t0  Ex: Function “valid\_ee” needs Group “ee” |
| **Step 2** | * Split the string of operands into 2 parts with separator ‘,’ (comma), using function split\_by\_literal\_separator defined in Utils, then we have $a2 = the first operand,   $a3 = the rest of $t0  Ex: “1, label” -> split\_by\_literal\_separator($t0, ‘,’)   * $a2 = “1” * $a3 = “label” * Based on the operand’s type, check if that operand exists, by using:   + “check\_substring\_appearance” if operand is a register  + “is\_num”/”is\_hexa” && ”check\_int\_reg\_range” if operand is a number/offset  + “check\_label” if operand if a label   * If valid, go to the next step , else invalid |
| **Step 3** | Similar to Step 2, split the string of operands in $a3 to get the second operand by split\_by\_literal\_separator($a3, ‘,’), then check the validation of it  Ex: “label” -> split\_by\_literal\_separator($t0, ‘,’)   * $a2 = “$t1” * $a3 = “$t2”   If valid, go to next step, else invalid |
| **Step 4** | Check if the third operand exists by getting the string length of $a3.  If length > 0 return invalid, else valid |

1. *1 operand (Ex: mfhi $t1)*

|  |  |
| --- | --- |
| **Step 1** | Load the suitable group of opcodes depending on its name to $t0  Ex: Function “valid\_i” needs Group “i” |
| **Step 2** | * Split the string of operands into 2 parts with separator ‘,’ (comma), using function split\_by\_literal\_separator defined in Utils, then we have $a2 = the first operand,   $a3 = the rest of $t0  Ex: “$t1” -> split\_by\_literal\_separator($t0, ‘,’)   * $a2 = “$t1” * $a3 = null * Based on the operand’s type, check if that operand exists, by using:   + “check\_substring\_appearance” if operand is a register  + “is\_num”/”is\_hexa” && ”check\_int\_reg\_range” if operand is a number/offset  + “check\_label” if operand if a label   * If valid, go to the next step , else invalid |
| **Step 3** | Check if the second operand exists by getting the string length of $a3. If length > 0 return invalid, else valid |

1. *0 operand (Ex: syscall)*

|  |  |
| --- | --- |
| **Step 1** | Load the suitable group of opcodes depending on its name to $t0  Ex: Function “valid\_none” needs Group “none” |
| **Step 2** | Check if the first operand exists by getting the string length of $t0.  If length > 0 return invalid, else valid |

# Simulation Sample Result

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