

## WORK EXPERIENCE

Software Engineer    **MediaTek** | 2021 ~ 2024

Implemented and verified **DisplayPort (DP)** specifications, ensuring compliance with industry standards.

**Developed and maintained** production software, optimizing performance and debugging critical issues.

**Collaborated** with hardware engineers to streamline verification processes, reducing time-to-market by 30%.

Writing testing procedures, **improving efficiency** and reliability in IC verification.

Software Engineer    **St. Joseph's Hospital** | 2016 ~ 2018

Designed and maintained **Hospital Information System (HIS)** applications, improving medical data management.

Developed **Oracle PL/SQL** database solutions, optimizing medical record retrieval and reducing query time.

Worked closely with **doctors and medical staff** to enhance system usability and efficiency.

Implemented data security measures to protect sensitive patient information.

## EDUCATION

Master's Degree in Computer Science

**National ChiaoTung University** | 2019 ~ 2021

Researched and developed **learning-based style transfer methods**, reducing dependence on reference images and improving image synthesis quality.

Published research in computer vision and artificial intelligence.

Bachelor's Degree in Computer Science

**National ChungHsing University** | 2011 ~ 2015

Developed an **Android-based** automatic roll call system, utilizing face recognition to streamline attendance tracking.



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## PROFILE

- Personality: easy-going, optimistic, passionate, hard-working, willing to learn.
- Graduated from National Chiao Tung University with a strong academic background in CS and valuable experience as a software engineer at MediaTek.
- Quickly learn and master new technology, excelling in both team oriented and independent environments. Proficient in a wide range of computer systems, programming languages, tools and machine learning techniques.

## SKILLS

programming: Python, C, C++, Delphi

Database: SQL

Image processing: OpenCV, OpenGL, PIL

Machine learning: Pytorch, Keras

Development Tools: Visual studio, Vscod, Github

# 自傳

## 〔關於我〕

畢業於交通大學資工所（碩士）及中興大學資工系（學士）。我個性細心且善於傾聽，擅長站在他人角度思考，與人相處融洽，能有效避免衝突。在團體活動中，適時表達自己的意見，並與團隊密切合作完成任務。

## 〔工作經歷〕

在聯發科工作兩年期間，負責以下任務：

- 使用影像處理技術實作 monitor DP port 規範，並參與兩款聯發科新型 monitor IC 的開發。
- 參與 IC FPGA 驗證，累積實戰經驗。
- 出差至客戶端（如 Intel、Dell 等 monitor source 廠商）進行 debug，解決產品問題。

## 〔碩士AI論文研究〕

碩士期間專注於深度學習與風格轉換的研究。傳統方法雖在模擬特定風格時表現較好，但無法隨意轉換風格；而現代 learning-based 方法則具備靈活性，卻在特定風格模擬中效果不佳。我的研究結合了兩者的優點，成功開發出能模擬任意畫家風格並實現卓越效果的轉換方法。

## 〔碩士學習與實踐〕

- **實驗計畫：**碩一時參與台北榮總合作專案，使用 3D Slicer 對乳房 MRI 影像進行體積估算。開發的方法最終被採用，並投稿至醫學期刊。
- **助教經歷：**碩二期間擔任演算法課程助教，負責輔助教學與學生指導。
- **自我學習：**積極自學新技術，參加 CGW，並透過線上課程（如 Udemy 和開放課程）提升專業能力。

Good day! I am Yong-Lun Wang, a graduate of National Chiao Tung University with a Master's degree in Computer Science. I worked at MediaTek, where I am responsible for IC PHY verification. I also successfully introduced and implemented new DP port specification margining for the company.

My short-term goal is to secure a position where I can apply my knowledge and strengths, contributing to the growth and success of the organization I join.

In the long term, I aim to continuously improve my programming skills and stay updated with the latest advancements in Computer Science, as I believe learning new skills is essential in this ever-evolving field.

My previous work experience has given me exposure to various areas of Computer Science. I bring not only the ideas and insights I have gained from my prior roles but also a deep passion for the field. This enthusiasm drives me to contribute meaningfully to your company, and that's why I am excited about this opportunity.

Thank you for your time and consideration.