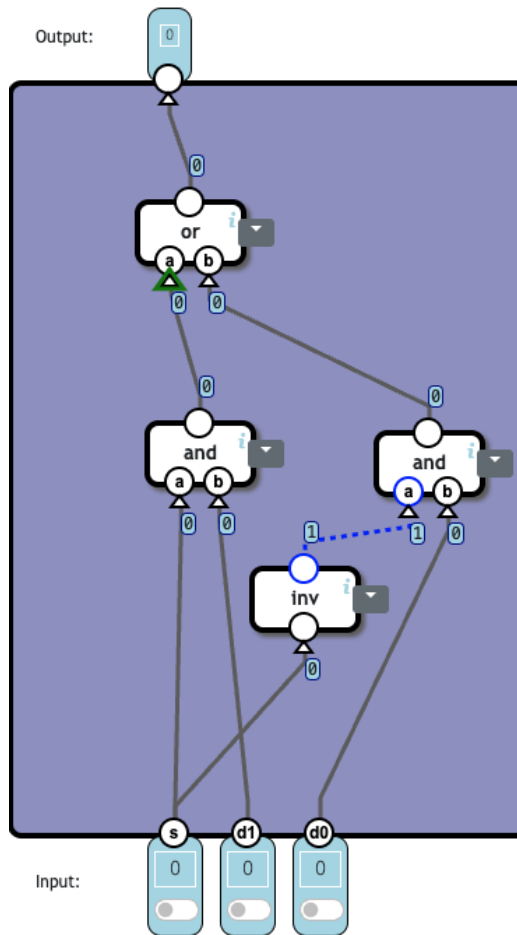


1. Selector

S determines which bit is the output

The AND gates (d0's having an inverse S input) output the value of the bit if it is selected

The OR gate outputs zero unless one of the AND gates outputs a 1



2. Switch

Same concept, but without the OR gate, rather just routing it out to one channel or the other

