

# Board/EC5LP/struct

**Package List**  
 LIBRARY ieee;  
 USE ieee.std\_logic\_1164.all;  
 USE ieee.numeric\_std.all;  
  
 LIBRARY gates;  
 USE gates.gates.all;

**Declarations**  
**Ports:**  
 IGCK\_clk : std\_ulogic  
 IRST\_rst\_n : std\_ulogic  
 i\_acq\_pretrig : std\_ulogic  
 i\_acq\_trig : std\_ulogic  
 i\_adc\_sdo : std\_ulogic  
 i\_fpga\_m : std\_ulogic  
 i\_fpga\_sck : std\_ulogic  
 i\_fpga\_mosi : std\_ulogic  
 i\_clk\_en : std\_ulogic  
 SIGNAL logic\_1 : std\_ulogic  
 SIGNAL lsig\_resetsynch\_n : std\_ulogic  
 SIGNAL rst : std\_ulogic

**Pre User:**

**Diagram Signals:**  
 o\_acq\_done : std\_ulogic  
 o\_adc\_sclk : std\_ulogic  
 o\_adc\_nsc : std\_ulogic  
 o\_fpga\_miso : std\_ulogic  
 o\_fram\_sclk : std\_ulogic  
 o\_fram\_sdi : std\_ulogic  
 o\_fram\_ncs : std\_ulogic  
 o\_sclk\_meas : std\_ulogic  
 o\_meas\_1mhz : std\_ulogic  
 o\_cal : std\_ulogic  
 io\_fpga\_miso : std\_ulogic

