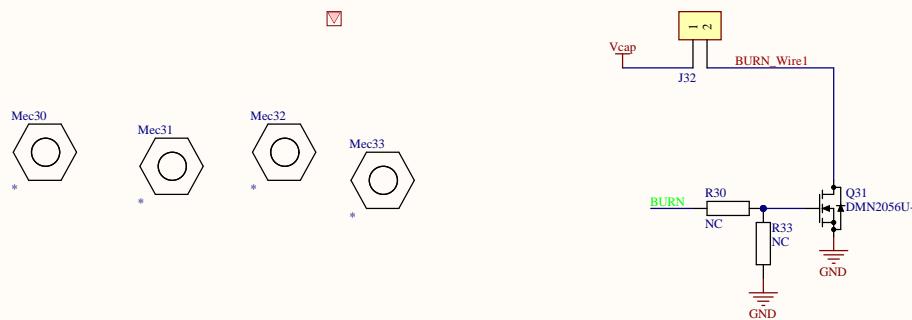


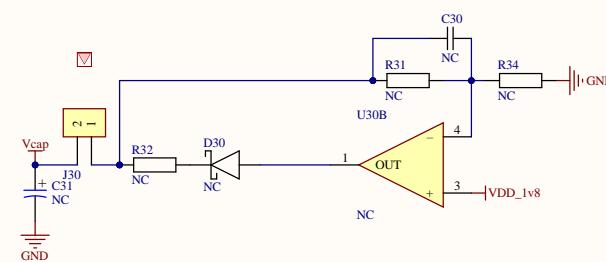
104

A



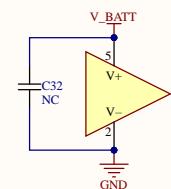
104

B



104

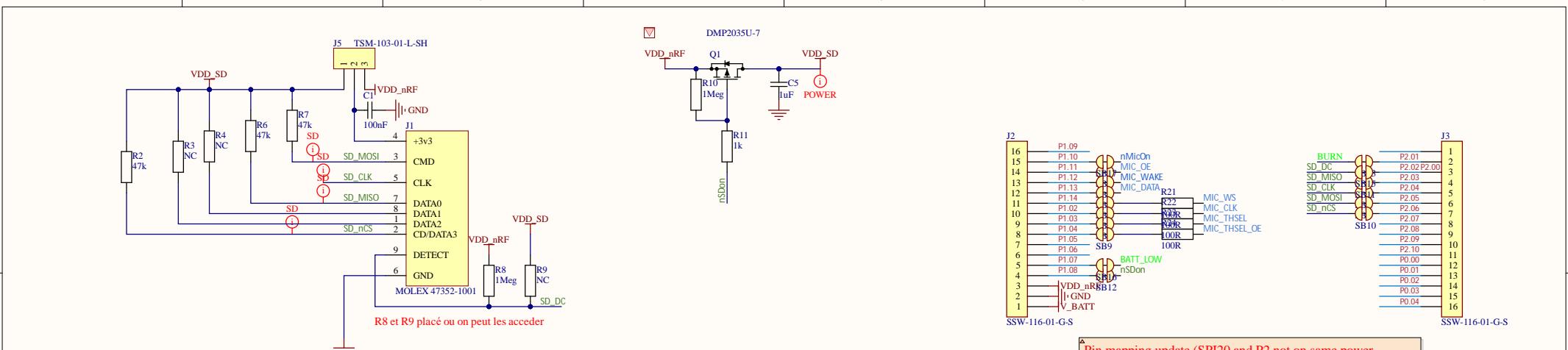
6



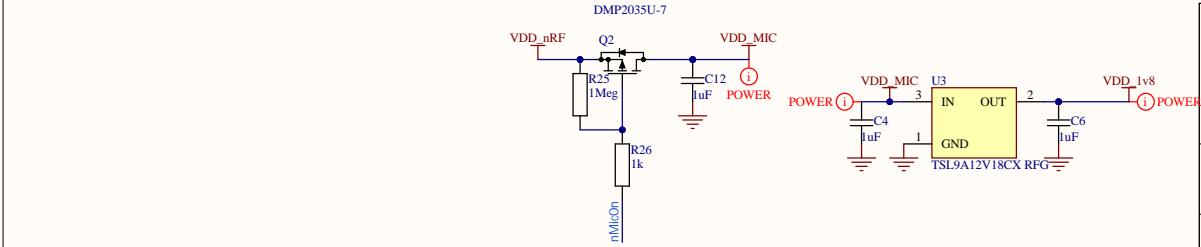
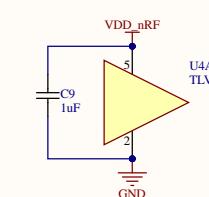
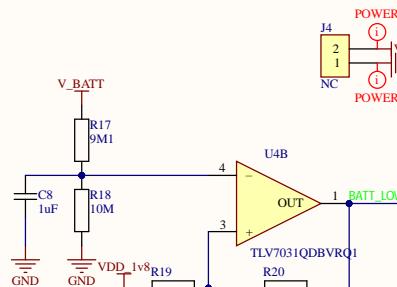
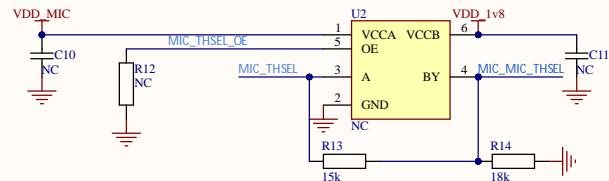
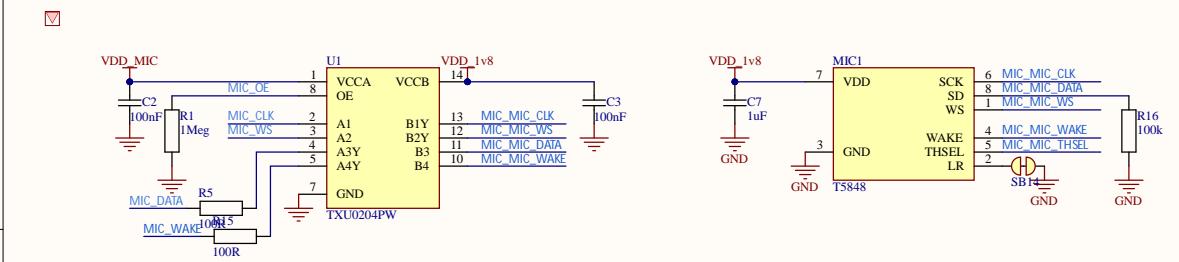
104

P

SpeakNoEvil4_micSD_v2.PrjPcb	Hes-SO // VALAIS WALLIS
SpeakNoEvil4_Burn_proto_v2.SchDoc	Date : 2025-10-31
Revision : 1.0	Sheet 1 of 2
	Design by : ANX



Pin mapping update (SPI20 and P2 not on same power block):
SD_MOSI: P2.05 -> P1.09
SD_CLK: P2.04 -> P1.03
SD_MISO: P2.03 -> P1.04
SD_DC: P2.02 -> P1.05
SD_nCS: P2.06 -> P1.06
MIC_THSEL: P1.03 -> P2.00
MIC_THSEL_OE: P1.04 -> P2.02



SpeakNoEvil4_micSD_v2.PrbPcb		Hes-SO // VALAIS WALLIS	
SpeakNoEvil4_micSD_v2.SchDoc		Date : 2025-10-31	π
Revision : 1.0	Sheet 2 of 2	Design by : ANX	
Q:\ECS\MonkeyBusiness\PCB\TM-AdrienRey\SpeakNoEvil4_micSD_v2\SpeakNoEvil4_micSD_v2.SchDoc			

