1. Description

1.1. Project

Project Name	Processor
Generated with:	STM32CubeMX 4.3.0
Date	11/16/2014

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RCTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration
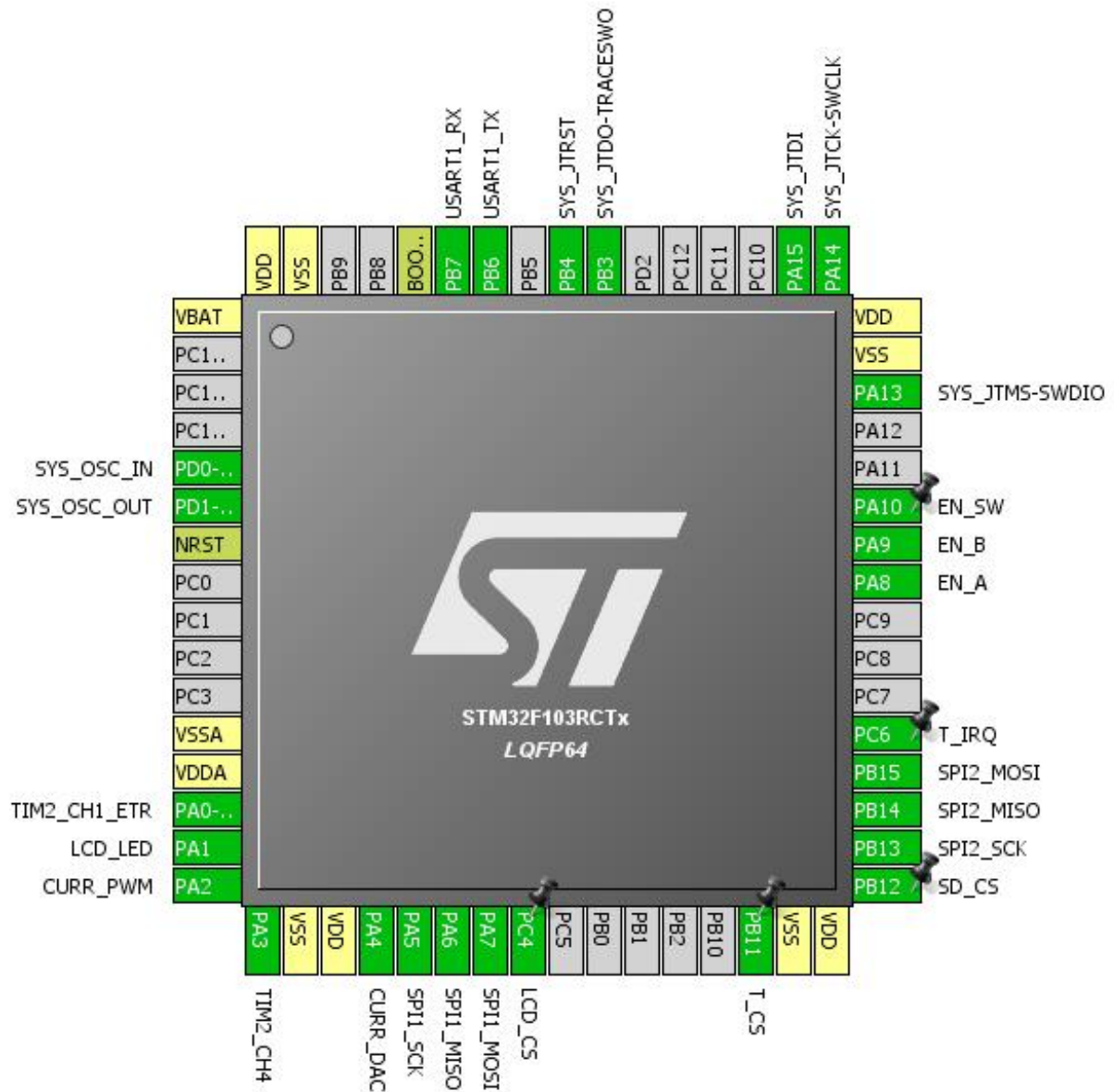
3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
DAC	OUT1	DAC_OUT1	PA4
SPI1	Mode: Full-Duplex-Master	SPI1_MISO	PA6
		SPI1_MOSI	PA7
		SPI1_SCK	PA5
SPI2	Mode: Full-Duplex-Master	SPI2_MISO	PB14
		SPI2_MOSI	PB15
		SPI2_SCK	PB13
SYS	Debug: JTAG(5-pin)	SYS_JTCK-SWCLK	PA14
		SYS_JTDI	PA15
		SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13
		SYS_JTRST	PB4
	OSC: HSE-External-Oscillator	SYS_OSC_IN	PD0-OSC_IN
		SYS_OSC_OUT	PD1-OSC_OUT
TIM1	Mode: Encoder-Interface	TIM1_CH1	PA8
		TIM1_CH2	PA9
TIM2	Mode: PWM-Generation	TIM2_CH1_ETR	PA0-WKUP
		TIM2_CH2	PA1
		TIM2_CH3	PA2
		TIM2_CH4	PA3
USART1	Mode: Asynchronous	USART1_RX	PB7
		USART1_TX	PB6

4. Pins Configuration

Pin	Pos	Function(s)	Label
PD0-OSC_IN	5	SYS_OSC_IN	
PD1-OSC_OUT	6	SYS_OSC_OUT	
PA0-WKUP	14	TIM2_CH1_ETR	
PA1	15	TIM2_CH2	LCD_LED
PA2	16	TIM2_CH3	CURR_PWM
PA3	17	TIM2_CH4	
PA4	20	DAC_OUT1	CURR_DAC
PA5	21	SPI1_SCK	
PA6	22	SPI1_MISO	
PA7	23	SPI1_MOSI	
PC4 *	24	GPIO_Output	LCD_CS
PB11 *	30	GPIO_Output	T_CS
PB12 *	33	GPIO_Output	SD_CS
PB13	34	SPI2_SCK	
PB14	35	SPI2_MISO	
PB15	36	SPI2_MOSI	
PC6 *	37	GPIO_Input	T_IRQ
PA8	41	TIM1_CH1	EN_A
PA9	42	TIM1_CH2	EN_B
PA10 *	43	GPIO_Input	EN_SW
PA13	46	SYS_JTMS-SWDIO	
PA14	49	SYS_JTCK-SWCLK	
PA15	50	SYS_JTDI	
PB3	55	SYS_JTDO-TRACESWO	
PB4	56	SYS_JTRST	
PB6	58	USART1_TX	
PB7	59	USART1_RX	

 * The pin is affected with an I/O function