P1:

A.

Destination address link interface

H3 3

B.

Can’t.

P4:

The minimal number of time slots needed is 3.First we transfer the x of the first input port and the y of the second input port;Second we transfer the y of the third port and the x of the second port;Finally we transfer the z of the third port.

The largest number is also the 3.

P6:

a.

2:t=2,3:t=3,4:t=4,5:t=6,6:t=5,7:t=7,8:t=8,9:t=9,10:t=10,11:t=11,12:t=12.

delay:1,2,2,3,3,3,4,3,4,3,3,4

average of delay is 2.917

b.

2:t=3,3:t=2,4:t=7,5:t=4,6:t=8,7:t=5,8:t=10,9:t=6,10:t=11,11:t=9,12:t=12.

delay:1,3,1,6,1,6,2,5,1,4,1,4

average of delay is 2.917

c.

2:t=3,3:t=5,4:t=2,5:t=4,6:t=7,7:t=6,8:t=8,9:t=10,10:t=12,11:t=9,12:t=11.

delay:1,3,4,1,1,5,3,3,5,5,1,3

average of delay is 2.917

d.

2:t=3,3:t=2,4:t=6,5:t=4,6:t=8,7:t=5,8:t=10,9:t=7,10:t=11,11:t=9,12:t=12.

delay:1,3,1,5,1,6,2,5,2,4,1,4

average of delay is 2.917

e.

All the average delays are equal.

P9:

destination address range link interface number

0000 0000~0011 1111 0 26=64

0100 0000~0101 1111 1 25=32

0110 0000~0111 1111 2 27-26-25=32

1000 0000~1011 1111 2 26=64

1100 0000~1111 1111 3 28-27-26=64