

Compilation and Program Analysis (#10) : Parallelism

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- 1 Generalities: Parallelism and semantics
 - The different forms of parallelism
 - Semantics of parallelism
- 2 Two case studies from the literature: Message passing and futures
- 3 Adding parallelism to Mini-while
- 4 Parallel programming languages in practice
- 5 MiniC with future: the lab session

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Why parallelism?

- ❶ To go faster
Massive amount of computation, sometimes massively parallel, sometimes with complex parallelisation patterns
- ❷ To handle large amount of data: big data-bases, consistency problems, synchronisation is crucial
- ❸ To handle problems that are by nature parallel, from system interruption to online applications with several users/distributed data or decisions

Different forms of parallelism

Shared memory

principle: processes can write and read data in common memory

spaces example: threads in most languages generally you need a form of locking to be able to write things correctly or something similar (can be basic mutex or more complex locking like Java serialize)

Message passing

principle: communication between thread by sending/receiving messages several communication patterns exist

synchronous/asynchronous/different send and receive primitives, etc.

High-level programming models

Can mix shared data and message passing or simply provide a high-level view on one of them, generally provides richer and safer way to compose computations

Example: parallel skeletons like map-reduce, actors, ...

Parallel, concurrent, or distributed?

Objectives of this course

- Study and write semantics for parallel programs.
- Compare different forms of parallelism.
- See a few possible ways to write the semantics of parallel programs.
- Study one particular construct, futures, that we will implement in the practical session
- A bit of typing
- Several semantics with focus on one: mini-while and futures (in the second half of the course).

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Semantics of parallelism: what exists?

Shared memory No standard semantics with deep studies, see one solution later

In fact additional constraints / logics / analysis is necessary to prove interesting properties.

Exception: weak memory models! (see next slide)

Message passing some classical calculi like CCS, and π -calculus highly studied (also automata-based formalism)

High-level models Many formal studies but no single calculus minimal and highly studied (because different models enforce different programming paradigms).

"cultural" digression 1: weak memory models

Question: What are the possible results (value of c) for running these two threads in parallel (initially a=b=c=0)?

```
a=1;                                b=1;
if (b==0) then                      ||  if (a==0) then
    c++;                            c++;
```

Answer: It depends ... a lot, there are many ways to interpret the program

from now on, we will forget about weak memory models ...

What semantics for parallel programs

No big step semantics for parallelism **Why?**

Denotational semantics difficult too because somehow big-step (not impossible but out of our scope / current research topics)

Consequence: do small step semantics with interleaving between small steps

if P does $P \rightarrow P_1 \rightarrow P_2 \rightarrow \dots \rightarrow P_n$ and Q does

$Q_1 \rightarrow Q_1 \rightarrow Q_2 \rightarrow \dots \rightarrow Q_n$ then $P||Q$ does the combination of the two. This is expressed by reordering processes ($P||Q \equiv Q||P$) and a simple rule:

$$\frac{P \rightarrow P'}{P||Q \rightarrow P'||Q}$$

This is most of the time sufficient but sometimes not enough, e.g. not directly adapted to weak memory models, no “true concurrency”.

Cultural digression 2: True concurrency semantics

It “simply” means there is one rule of the form:

$$\frac{P \rightarrow P' \quad Q \rightarrow Q'}{P || Q \rightarrow P' || Q'}$$

Which changes a bit the behaviour, generally in a non-observable manner.

[Discussion] About denotational semantics for parallelism

Recall: denotational semantics transforms a “program” into a “mathematical structure”

It looks like big-step but it depends on the structure generated.

Generating something like a trace is possible too. Trace semantics exist but is not exactly denotational. However there are ways to get something more denotational:

- LAGC semantics with Reiner Hahnle, Einar Broch Nohnsen et al.: kind of small-step denotational by “concretizing” the semantics at some points
- itrees / ctrees with Yannick Zakowski (and others): Generate a tree (coinductive) structure similar to a trace to take into account inputs ... and non-determinism.

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CCS syntax

- Channel names: a, b, c, \dots
- Co-names: $\bar{a}, \bar{b}, \bar{c}, \dots$ (complementary « $\bar{\bar{a}} = a$ »)
- Silent action (unobservable): τ
- Actions: $\mu ::= a \mid \bar{a} \mid \tau$
- Processes:

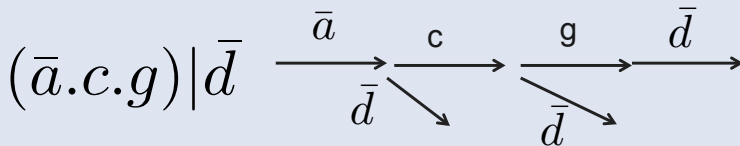
P, Q	$::=$	0	inaction
	$ $	$\mu.P$	prefix
	$ $	$P \mid Q$	parallel
	$ $	$P + Q$	(external) choice
	$ $	$(\nu a)P$	restriction
	$ $	$\text{rec}_K P$	process P with definition $K = P$
	$ $	K	(defined) process name

Intuitive Semantics from an “action” point of view

- $a.P$ offers action a and then becomes P
- $a.P + b.Q$ may offer either a and become P or b and become Q
- $(\nu a)P$ may offer any action of P , except a
- $P \mid Q$ may offer an action of P or of Q , but also if P offers a and Q offers \bar{a} , they may synchronise (into a τ action)

We will use a labelled transition semantics
for CCS processes.

A Micro-example



A tiny example

$rec_{C1}(Tick.C1)$

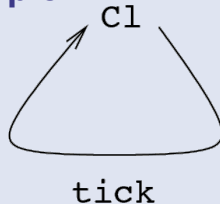


Figure: The transition graph for $C1$

Labelled graph

- vertices: process expressions
- labelled edges: transitions
- Each derivable transition of a vertex is depicted
- Abstract from the derivations of transitions

Exercise:

What are the possible traces (output sequences) of $C1$?

CCS : behavioural semantics (1)

Operators and rules

- Action prefix:

$$\overline{\mu.P \xrightarrow{\mu} P}$$

- Communication:

$$\frac{P \xrightarrow{a} P' \quad Q \xrightarrow{\bar{a}} Q'}{P|Q \xrightarrow{\tau} P'|Q'}$$

- Parallelism

$$\frac{P \xrightarrow{\mu} P'}{P|Q \xrightarrow{\mu} P'|Q}$$

$$\frac{Q \xrightarrow{\mu} Q'}{P|Q \xrightarrow{\mu} P|Q'}$$

CCS : behavioural semantics (2)

Operators and rules

- Non-deterministic choice

$$\frac{Q \xrightarrow{\mu} Q'}{P + Q \xrightarrow{\mu} Q'}$$

$$\frac{P \xrightarrow{\mu} P'}{P + Q \xrightarrow{\mu} P'}$$

- Scope restriction

$$\frac{P \xrightarrow{\mu} P' \quad \mu \neq a, \bar{a}}{(\nu a)P \xrightarrow{\mu} (\nu a)P'}$$

- Recursive definition

$$\frac{P[\text{rec}_K P / K] \xrightarrow{\mu} P'}{\text{rec}_K P \xrightarrow{\mu} P'}$$

Example

Apply semantic rules to infer one possible behaviour of:

$$\nu a.(a.b|(\bar{a}.c + \bar{a}.\bar{b}))$$

Are the following traces acceptable:

- $\tau.c.b$
- $a.\bar{a}.\tau$
- $\tau.\bar{b}.b$
- $\tau.\tau$

?

Notes on CCS and process algebras

- Different syntax exist, and plenty of variants, among them:
 - Different recursion: define process names
 - Guarded choice / guarded recursion
 - Passing data on channels ($\bar{a}(5) / a(x)$)
- More work on π -calculus (a follow-up) than CCS. π -calculus is more or less CCS+sending channel names over channels. Scopes and restrictions become more complex. You need

something like: $(\nu x)(P|Q) \equiv (\nu x)P|Q$

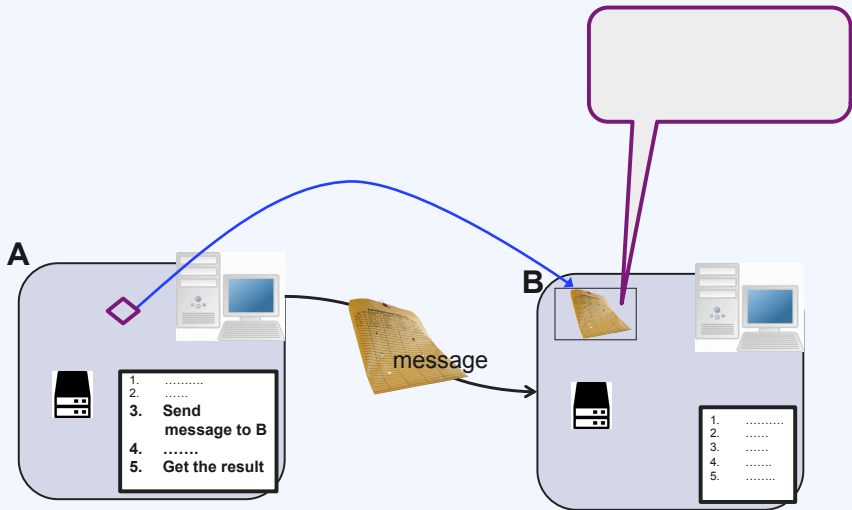
Why is name extrusion complex?

- many research works on equivalence relations between CCS/pi-calculus programs: Bisimulation and other equivalences.

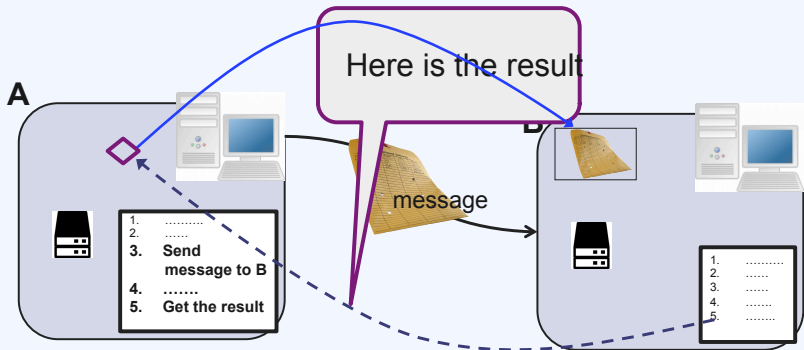
Overall a vast topic

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Requests and replies



Requests and replies



A simple λ -calculus with futures: Syntax

Terms: λ -calculus + futures:

$$e ::= (e \ e') \mid \lambda x. e \mid x \mid \text{get } e \mid f \mid \text{async}(e)$$

f appears during execution.

v is a value (fully evaluated term), i.e. $v ::= f \mid \lambda x. e$.

We could add other values, e.g. `int`.

A configuration consists of

- futures: $fut(f)$ (unresolved) or $fut(f \ v)$ (resolved with a value)
- and tasks ($tasks(fe)$).

References:

- A more complete lambda calculus with futures can be found in: *Joachim Niehren, Jan Schwinghammer, Gert Smolka. A Concurrent Lambda Calculus with Futures. Theoretical Computer Science, 2006,*
- simple lambda calculus with futures has been used in *Fernandez-Reyes, K., Clarke, D., Castegren, E., Vo, H-P. Forward to a Promising Future. Coordination 2018*

A simple λ -calculus with futures: Semantics

RED-LAMBDA

$$task(g \ E[(\lambda x.e) \ v]) \Rightarrow task(g \ E[e\{v/x\}])$$

RED-ASYNC

$$\frac{fresh \ f}{task(g \ E[async(e)]) \Rightarrow fut(f) \ task(f \ e) \ task(g \ E[f])}$$

CONTEXT

$$\frac{cn \Rightarrow cn'}{cn \ cn'' \Rightarrow cn' \ cn''}$$

END-TASK

$$fut(f) \ task(f \ v) \Rightarrow fut(f \ v)$$

RED-GET

$$\frac{task(f \ E[getf]) \ fut(f \ v)}{task(f \ E[v]) \ fut(f \ v)}$$

Note: configurations identified modulo reordering of tasks / futures,

What is E?

Evaluation contexts

Evaluation contexts (sometimes called reduction contexts) used to focus on part of the configuration and reduce it. Compared to context rules they are more versatile: you can better choose what is in/out of the context.

For lambda-calculus with futures:

$$E ::= E \ e \mid v \ E \mid \bullet \mid \text{get } E$$

This ensures call-by-value.

$$E[e] = E\{\bullet \leftarrow e\}$$

Reduction context

$$(\lambda x.x) \ ((\lambda y.y) \ ((\lambda z.z) \ T))$$

Reduced term

$E = (\lambda x.x) \ ((\lambda y.y) \ (\bullet))$ and RED-LAMBDA can be applied.

Example of lambda-fut evaluation

What is the initial configuration?

→ a task containing the program to be evaluated: $task(f\ e)$ where e is the program and f is a future that will never be used.

What is the behaviour of $(\lambda x. (\lambda z. z)(async((\lambda y. y + y)x)))\ 3$?

Suppose we have a print operation in the language of the form $print\ "A"; e$. Write a simple program that can print either first "A" then "B" or first "B" then "A". add get to the program so that only one output is possible. (Use some e_A of the form $e_A = print\ "A"; 1$ and call it asynchronously)

Questions and discussions

Typing: What about typing lambda-fut? what properties to ensure?

Deadlocks What statement introduces synchronisation? Write a configuration that would be blocked in a deadlock. Can you create a program that creates this configuration? Or a program that deadlocks? Is it possible by extending the language?

Stateful actors and futures – A complex semantics

CONTEXT

$$\frac{cn \rightarrow cn'}{cn \ cn'' \rightarrow cn' \ cn''}$$

ASSIGN

$$\frac{\llbracket e \rrbracket_{a+\ell} = w \quad (a+\ell)[x \mapsto w] = a' + \ell'}{\alpha(a, q : \{\ell|x = e; s\}, \overline{q'}) \rightarrow \alpha(a', q : \{\ell'|s\}, \overline{q'})}$$

NEW

$$\frac{\llbracket \overline{v} \rrbracket_{a+\ell} = \overline{w} \quad \beta \text{ fresh} \quad \overline{y} = \text{fieldsAct}}{\alpha(a, q : \{\ell|x = \text{newAct}\overline{v}; s\}, \overline{q'}) \rightarrow \alpha(a, q : \{\ell|x = \beta; s\}, \overline{q'}) \quad \beta([\overline{y} \mapsto \overline{w}], \emptyset, \emptyset)}$$

INVK

$$\frac{\llbracket v \rrbracket_{a+\ell} = \beta \quad \llbracket \overline{v} \rrbracket_{a+\ell} = \overline{w} \quad \beta \neq \alpha \quad f \text{ fresh}}{\alpha(a, q : \{\ell|x = v.\mathbf{m}(\overline{v}); s\}, \overline{q'}) \beta(a', p, \overline{q\beta}) \rightarrow \alpha(a, q : \{\ell|x = f; s\}, \overline{q'}) \beta(a', p, \overline{q\beta}\#(f, m, \overline{w})) f(\perp)}$$

RETURN

$$\frac{\llbracket v \rrbracket_{a+\ell} = w}{\alpha(a, (f, m, \overline{w}) : \{\ell|\text{return } v\}, \overline{q}) f(\perp) \rightarrow \alpha(a, \emptyset, \overline{q}) f(w)}$$

SERVE

$$\frac{\text{bind}(\alpha, q) = \{l|s\}}{\alpha(a, \emptyset, q\#\overline{q'}) \rightarrow \alpha(a, q : \{l|s\}, \overline{q'})}$$

GET

$$\frac{\llbracket w \rrbracket_{a+\ell} = f}{\alpha(a, q : \{\ell|y = \text{get } w; s\}, \overline{q'}) f(w') \rightarrow \alpha(a, q : \{\ell|y = w'; s\}, \overline{q'}) f(w')}$$

States + communication + requests makes the semantics a bit too complex to be studied here.

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Mini-While Syntax (OLD) 1/2

Expressions:

$$e ::= c \mid e + e \mid e \times e \mid \dots$$

$$\mid x \quad \text{variable}$$

Statements:

$S(Smt) ::=$	$x := expr$	assign
	$x := f(e_1, \dots, e_n)$	simple function call
	$skip$	do nothing
	$S_1; S_2$	sequence
	$\text{if } b \text{ then } S_1 \text{ else } S_2$	test
	$\text{while } b \text{ do } S \text{ done}$	loop

Mini-While Syntax (OLD) 2/2

Programs with function definitions and global variables

$Prog$	$::= D \text{ FunDef } Body$	Program
$Body$	$::= D; S$	Function/main body
D	$::= \text{var } x : \tau D; D$	Variable declaration
$FunDef$	$::= \tau f(x_1 : \tau_1, \dots, x_n : \tau_n) \text{ Body; return } e$ $ \text{ FunDef FunDef}$	Function def

Structural Op. Semantics (SOS = small step) for mini-while (OLD – no fun)

$$(x := a, \sigma) \Rightarrow \sigma[x \mapsto Val(a, \sigma)]$$

$$(\text{skip}, \sigma) \Rightarrow \sigma$$

$$\frac{(S_1, \sigma) \Rightarrow \sigma'}{((S_1; S_2), \sigma) \Rightarrow (S_2, \sigma')} \quad \frac{(S_1, \sigma) \Rightarrow (S'_1, \sigma')}{((S_1; S_2), \sigma) \Rightarrow (S'_1; S_2, \sigma')}$$

$$\frac{Val(b, \sigma) = tt}{(\text{if } b \text{ then } S_1 \text{ else } S_2, \sigma) \Rightarrow (S_1, \sigma)}$$

$$\frac{Val(b, \sigma) = ff}{(\text{if } b \text{ then } S_1 \text{ else } S_2, \sigma) \Rightarrow (S_2, \sigma)}$$

Mini-while + shared memory

Add parallel composition to statements

$$S ::= \dots | S || S'$$

And 2 reduction rules for parallelism:

PARALLEL1

$$\frac{(S_1, \sigma) \Rightarrow (S'_1, \sigma')}{(S_1 || S_2, \sigma) \Rightarrow (S'_1 || S_2, \sigma')}$$

PARALLEL2

$$\frac{(S_2, \sigma) \Rightarrow (S'_2, \sigma')}{(S_1 || S_2, \sigma) \Rightarrow (S_1 || S'_2, \sigma')}$$

And 2 special cases when a parallel task finishes:

ENDTASK1

$$\frac{(S_1, \sigma) \Rightarrow \sigma'}{(S_1 || S_2, \sigma) \Rightarrow (S_2, \sigma')}$$

ENDTASK2

$$\frac{(S_2, \sigma) \Rightarrow \sigma'}{(S_1 || S_2, \sigma) \Rightarrow (S_1, \sigma')}$$

An alternative syntax: Spawn

$||$ is not often practical in the syntax

Solution: add a `spawn(S)` statement and have a run-time syntax different from the static syntax. Programmer cannot use $S_1 || S_2$ but $||$ appears when the term is evaluated.

In this case we have the additional rule:

SPAWN

$$(\text{spawn}(S_1); S_2, \sigma) \Rightarrow (S_1 || S_2, \sigma')$$

is it really the same? (\rightarrow)

Example mini-while shared memory

Compute the semantics of:

- $x := 0; (x := 2; x := x + 1 || x := 3)$
or equivalently $x := 0; \text{spawn}(x := 2; x := x + 1); x := 3)$
- $x := 0; (x := 2 || \text{while } x < 3 \text{ do } x := x + 1 \text{ done})$

What is the semantics of

$x := 0; \text{while } (\text{true}) \text{ do } \text{spawn}(x := x + 1) \text{ done}; x := 3$

Is there an equivalent program with $||$?

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OLD SOS with functions (1/2)

Runtime configuration

(Optional-Statement, Call-Stack, Stack, Store):

$$cn ::= (S, Ctx, \Sigma, sto) \mid (Ctx, \Sigma, sto)$$

$$(x := e, Ctx, \Sigma, sto) \Rightarrow (Ctx, \Sigma, sto[\Sigma(x) \mapsto Val(e, sto \circ \Sigma)])$$

$$\frac{(S_1, Ctx, \Sigma, sto) \Rightarrow (Ctx, \Sigma', sto')}{((S_1; S_2), Ctx, \Sigma, sto) \Rightarrow (S_2, Ctx, \Sigma', sto')}$$

$$\frac{(S_1, Ctx, \Sigma, sto) \Rightarrow (S'_1, Ctx, \Sigma', sto')}{((S_1; S_2), Ctx, \Sigma, sto) \Rightarrow (S'_1; S_2, Ctx, \Sigma', sto')}$$

+ rules for if, skip, and while

OLD SOS with functions (2/2)

CALL

$$\frac{bind_3(f, e_1..e_n, \Sigma, sto) = (S', \Sigma', sto')}{(x := f(e_1, \dots, e_n); S, Ctx, \Sigma, sto) \Rightarrow (S', (\Sigma, x := R(f); S) :: Ctx, \Sigma', sto')}$$

Ctx is a list of $(Stack, Stm)$. $x := f(e_1, \dots, e_n); S$ must be the whole current statement (imposed by the rule SEQ). $R(f)$ is a marker that remembers the name of the function called

$bind_3(f, e_1..e_n, \Sigma, sto) = (S_f, \Sigma', sto[\ell_1 \mapsto v_1.. \ell_n \mapsto v_n])$ if $body(f) = D_f; S_f$,
 $params(f) = [x_1..x_n]$, $Vars(D_f) = \{y_1..y_k\}$

$\ell_1.. \ell_n$ fresh, $\ell'_1.. \ell'_k$ fresh $\forall i \in [1..n]. Val(e_i, sto \circ \Sigma) = v_i$,

$\Sigma' = \Sigma[x_1 \mapsto \ell_1.. x_n \mapsto \ell_n][y_1 \mapsto \ell'_1.. y_k \mapsto \ell'_k]$.

$$\frac{v = Val(ret(f), sto \circ \Sigma')}{((\Sigma, x := R(f); S) :: Ctx, \Sigma', sto) \Rightarrow (S, Ctx, \Sigma, sto[\Sigma(x) \mapsto v])}$$

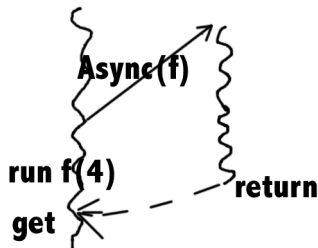
Futures: syntax and principles

Statements:

$S(Smt) ::= x := expr$	assign
$x := f(e_1, .., e_n)$	simple function call
$x := \textbf{Async}(f(e_1, .., e_n))$	Asynchronous function call
$x := get(e)$	future access (synchronisation)
$skip$	do nothing
$S_1; S_2$	sequence
if b then S_1 else S_2	test
while b do S done	loop

Example (informally)

```
int f (int x) (  
  int z;  
  z:=x+x  
) return z  
  
(  
  int x,y;  
  fut<int> t;  
  t:=Async(f(3));  
  y:=f(4);  
  x:=get(t)  
)
```



Design choice: no global state

We have the choice between

- 1 Have a global state and allow race-condition between tasks. Versatile and looks like C threads but non-deterministic behaviour of programs.
- 2 Forget the global state that we had in function evaluation to have a more predictable semantics: no races between two tasks writing on the same memory.

The second case corresponds to MiniC where we have no global variable.

We specify the semantics for the second solution. To implement the first solution a global memory should be added to the configuration.

Future semantics for mini-while (1/3)

- Syntax: F, G range over future identifiers. Values (v) can be future identifiers.
- Configurations:

$$cn ::= (S, Ctx, \Sigma, sto)_F \mid (Ctx, \Sigma, sto)_F \mid fut(F, v) \mid cn \ cn'$$

Configurations are identified modulo reordering of tasks.

Note: compared to λ -calculus+fut we do not put unresolved futures in the configuration.

- Sequential reduction rules adapted straightforwardly:

$$\frac{(S, Ctx, \Sigma, sto) \Rightarrow (S', Ctx', \Sigma', sto')}{(S, Ctx, \Sigma, sto)_F \ cn \Rightarrow (S', Ctx', \Sigma', sto')_F \ cn}$$

$$\frac{(S, Ctx, \Sigma, sto) \Rightarrow (Ctx', \Sigma', sto')}{(S, Ctx, \Sigma, sto)_F \ cn \Rightarrow (Ctx', \Sigma', sto')_F \ cn}$$

Future semantics for mini-while (2/3)

A naive solution for asynchronous function call:

ASYNC-CALL (BAD)

$$\frac{\text{bind}_3(f, e_1..e_n, \Sigma, sto) = (S', \Sigma', sto') \quad G \text{ fresh future}}{(x := \text{Async}(f(e_1, .., e_n)); S, Ctx, \Sigma, sto)_F \text{ cn} \Rightarrow (x := G; S, Ctx, \Sigma, sto)_F (S', \emptyset, \Sigma', sto')_G \text{ cn}}$$

Problem: we have lost the return expression that was stored by $R(f)$ in the synchronous call. We do not know what to fill the future G with.

Future semantics for mini-while (3/3)

A possible solution: add $return(e)$ to the valid Ctx and store the returned expression in the call-stack:

Ctx is a list of $(Stack, Stm)$ possibly with $return(e)$ as the last element of the list.

ASYNC-CALL

$$\frac{bind_3(f, e_1..e_n, \Sigma, sto) = (S', \Sigma', sto') \quad G \text{ fresh future}}{(x := \mathbf{Async}(f(e_1, \dots, e_n)); S, Ctx, \Sigma, sto)_F \mathbf{cn} \Rightarrow (x := G; S, Ctx, \Sigma, sto)_F (S', return(ret(f)), \Sigma', sto')_G \mathbf{cn}}$$

End of function execution and future access:

FUT-RESOLVE

$$\frac{v = Val(e, sto \circ \Sigma)}{(return(e), \Sigma, sto)_F \mathbf{cn} \Rightarrow fut(F, v) \mathbf{cn}}$$

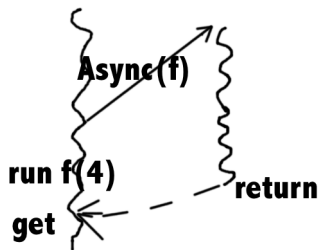
GET

$$\frac{G = Val(e, sto \circ \Sigma)}{(x := get(e); S, Ctx, \Sigma, sto)_F fut(G, v) \mathbf{cn} \Rightarrow (x := v; S, Ctx, \Sigma, sto)_F fut(G, v) \mathbf{cn}}$$

Example (semantics)

use the semantics to evaluate the previous example

```
int f (int x) (  
  int z;  
  z:=x+x  
) return z  
  
(  
  int x,y;  
  fut<int> t;  
  t:=Async(f(3));  
  y:=f(4);  
  x:=get(t)  
)
```



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Base Type System (OLD)

From declarations we infer $\Gamma : Var \rightarrow Basetype$ with a judgment \rightarrow_d . From program we infer a function table $\Gamma_f : FuncName \rightarrow (\tau_1.. \tau_n \rightarrow \tau)$ with a judgment \rightarrow_f . Then a typing judgment for expressions is $\Gamma \vdash e : \tau \in Basetype$. Typing of statements has the form : $\Gamma, \Gamma_f \vdash S$.

$$\frac{\Gamma \vdash e_1 : \mathbf{int} \quad \Gamma \vdash e_2 : \mathbf{int}}{\Gamma \vdash e_1 + e_2 : \mathbf{int}} \qquad \frac{}{\Gamma \vdash x : \Gamma(x)}$$

$$\frac{\Gamma, \Gamma_f \vdash S_1 \quad \Gamma, \Gamma_f \vdash S_2}{\Gamma, \Gamma_f \vdash S_1; S_2}$$

$$\frac{\Gamma \vdash x : \tau \quad \Gamma \vdash e : \tau}{\Gamma, \Gamma_f \vdash x := e}$$

$$\frac{\Gamma \vdash b : \mathbf{bool} \quad \Gamma, \Gamma_f \vdash S_1 \quad \Gamma, \Gamma_f \vdash S_2}{\Gamma, \Gamma_f \vdash \mathbf{if } b \mathbf{ then } S_1 \mathbf{ else } S_2}$$

$$\frac{\Gamma \vdash b : \mathbf{bool} \quad \Gamma, \Gamma_f \vdash S}{\Gamma, \Gamma_f \vdash \mathbf{while } b \mathbf{ do } S \mathbf{ done}}$$

Type function calls and typing program

To type a program we type all method bodies:

$$\begin{array}{c}
 D \rightarrow_d \Gamma_g \quad Fundef \rightarrow_f \Gamma_f \\
 \forall (\tau \ f(x_1 : \tau_1, \dots, x_n : \tau_n) \ D_f; S_f; \text{return } e \in Fundef). \\
 \Gamma_g + \Gamma_l \vdash e : \tau \wedge \Gamma_g + \Gamma_l, \Gamma_f \vdash S_f \text{ with } x_1 : \tau_1; \dots; x_n : \tau_n; D_f \rightarrow_d \Gamma_l \\
 D_m \rightarrow_d \Gamma_m \quad \Gamma_g + \Gamma_m, \Gamma_f \vdash S \\
 \hline
 \vdash D \ Fundef \ D_m; S
 \end{array}$$

CALL

$$\frac{\Gamma_f(f) = \tau_1.. \tau_n \rightarrow \tau \quad \forall i \in [1..n]. \Gamma \vdash e_i : \tau_i \quad \Gamma \vdash x : \tau}{\Gamma, \Gamma_f \vdash x := f(e_1, \dots, e_n)}$$

+ merges and overwrite variable declarations, for overriding variables (local over global).

Adding future types

Previous type syntax:

$$\tau ::= int \mid bool$$

New types can be futures:

$$\tau ::= int \mid bool \mid fut < \tau >$$

Future types can be declared: `fut<int> x,y`

We check structural type equivalence.

Typing rules for futures

ASYNC

$$\frac{\Gamma_f(f) = \tau_1.. \tau_n \rightarrow \tau \quad \forall i \in [1..n]. \Gamma \vdash e_i : \tau_i \quad \Gamma \vdash x : fut < \tau >}{\Gamma, \Gamma_f \vdash x := \mathbf{Async}(f(e_1, .., e_n))}$$

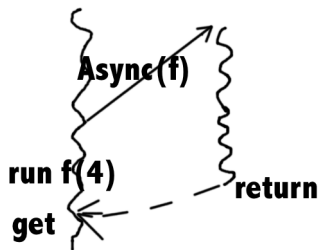
GET

$$\frac{\Gamma \vdash e : fut < \tau > \quad \Gamma \vdash x : \tau}{\Gamma, \Gamma_f \vdash x := \mathbf{get}(e)}$$

Example (type)

Type the previous example (skip the typing of f)

```
int f (int x) (  
  int z;  
  z:=x+x  
) return z  
  
(  
  int x,y;  
  fut<int> t;  
  t:=Async(f(3));  
  y:=f(4);  
  x:=get(t)  
)
```



A more complex example (C-like syntax)

```
main () {  
  fut<int> z ; int y  
  fut<fut<int>> x;  
  x=async(foo(2));  
  y=get(get(x));  
  y=y+1;  
  fut<int> z=get(x);  
}  
fut<int> foo(int x) {  
  fut<int> r  
  r=async(bar(x+1));  
  return r;  
}  
int bar(int x) { return x*x; }
```

Is this program well-typed?

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Preservation

Definition:

consider a well typed program $Prog$, a configuration cn reachable by executing $Prog$, we have

$$cn \Rightarrow cn' \wedge \Gamma_f, \mathbf{\Gamma}_{fut} \vdash cn \implies \exists \Gamma'_{fut}, \Gamma_f, \mathbf{\Gamma}'_{fut} \vdash cn'$$

What is a well-typed configuration? i.e. define the assertion

$\Gamma_f, \mathbf{\Gamma}_{fut} \vdash cn$ What is $\mathbf{\Gamma}_{fut}$? How to type function bodies?

Definition (Configuration typing (very OLD))

$$\Gamma \vdash (S, \sigma) \iff (\Gamma \vdash S \wedge \forall x. \emptyset \vdash \sigma(x) : \tau \iff \Gamma(x) = \tau)$$

Now we have:

$$cn ::= (S, Ctx, \Sigma, sto)_F \mid (Ctx, \Sigma, sto)_F \mid fut(F, v) \mid cn \ cn'$$

With Ctx of the form $(\Sigma, S) :: \dots :: (\Sigma_n, S_n) :: return(e)$ (no $return(e)$ for the main task).

Well-typed configuration

Suppose $\Gamma, \Gamma_f \vdash (S, \sigma)$ defined similarly to before (Γ_f added). We can define:

$$\frac{\begin{array}{c} \mathbf{\Gamma}, \Gamma_f \vdash (S, sto \circ \Sigma) \quad Ctx = (\Sigma_0, S_0) :: \dots :: (\Sigma_n, S_n) :: return(e) \\ \forall i \in [1..n]. \mathbf{\Gamma}_i, \Gamma_f \vdash (S_i, sto \circ \Sigma_i) \quad \mathbf{\Gamma}_n \vdash e : \Gamma_{fut}(F) \end{array}}{\Gamma_f, \Gamma_{fut} \vdash (S, Ctx, \Sigma, sto)_F}$$

$$\frac{\emptyset \vdash v : \Gamma_{fut}}{\Gamma_f, \Gamma_{fut} \vdash fut(F, v)} \qquad \frac{\Gamma_f, \Gamma_{fut} \vdash cn \quad \Gamma_f, \Gamma_{fut} \vdash cn'}{\Gamma_f, \Gamma_{fut} \vdash cn \quad cn'}$$

Problem: $\mathbf{\Gamma}$ is undefined.

It is the environment that types the considered statement, i.e. the typing environment of the function that contains the considered statement.

How to define properly? for example: annotate configurations with the name of the function that is currently evaluated and somehow recover the typing environment.

Now: How to prove that small step semantics preserves well-typed configurations?

Summary: what have we seen up to now?

- Overview of concepts for parallelism in programming languages and in semantics.
- Example of semantics for communications and futures.
- Designed a semantics for MiniWhile with thread and concurrent memory accesses.
- Designed a semantics for MiniWhile with futures and no concurrent memory access.
- A type system for futures

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Different approaches to implement languages

Classical compilation: as seen in course

Source-to-source compilation: compiling to assembler is tedious and restricted to one architecture. Many source-to-source compilers, e.g. language-to-C / language-to-Java

Libraries / DSL: No translation at all. Relies on software engineering expertise to implement rich libraries DSLs while staying in the restriction of the host language

Next: 2 examples.

ProActive:

A Java **API + Tools** for Parallel, Distributed Computing

A uniform framework: **An Active Object pattern**

A formal model behind: **Determinism (POPL'04)**

- **Programming Model (Active Objects):**
- **Asynchronous Remote Invocations, Wait-By-Necessity**
- **Groups, Mobility, Components, Security, Fault-tolerance, Load balancing**
- **Environment:**
 - **XML Deployment Descriptors, File Transfers**
 - **Interfaced with: rsh, ssh, LSF, PBS, Globus, Jini, SUN Grid Engine**



Creating active objects

An object created with `A a = new A (obj, 7);`
can be turned into an active and remote object:

- **Object-based:**

```
a = (A) ProActive.turnActive (a, node);
```

- **Instantiation-based:**

```
A a = (A) ProActive.newActive («A», param, node);
```

The "node" is the AO container.

Remaining of the code unchanged → "Transparency"

Example 2: The Encore language approach

A language with objects, futures, actors, etc. with a rich type system to optimise data access while preventing data-races. Advanced features: Ownership types, parallel futures, forwarding, ...

Compiled into C (source-to-source compilation)

Then relies on a specific C library, and an existing Actor library in C (pony) with a dedicated runtime (PonyRT) -> Final compilation and execution

Tiny code example (observe the dedicated syntax):

```
defrun() : void {
    let fut = service.provide()
    client =new Client()
    in {
        client.send(get fut);
    }
    ...
}
```

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Structure and approach

Approach restricted to future of integers, typed as a new type `futint`. Corresponds to `fut<int>`. Functions that can be called asynchronously all have a single parameter of type `int` and return an `int`. For other functions it is sufficient if your type-checker only takes into account functions with one single parameter of any type.

- An extended syntax (get and async and `futint` type) **Provided**
- A source-to-source transformation **Provided**
- A typing visitor: type get and async **To do**
- A dedicated library using C threads to implement Async and Get **To do**

Source-to-source transformation

Done by MiniCPPListener.py

- add pointers, especially pointer-to-function
- Import the right library (futurelib.h) and add a cleanup phase

A practical example:

```
int main(){
    futint fval;
    int val;
    fval = Async(funci,123)
        ;
    println_int(0);
    return 0;
}
```

→

```
int main(){
    futint fval;
    int val;
    fval = Async(&funci
        ,123);
    println_int(0);
    freeAllFutures();
    return 0;
}
```

Typing futures

To do: add typing of `async` and `Get` in your type-checker: Your new typing visitor should now have:

```
def visitAsyncFuncCall(self, ctx):  
    ...
```

Also type the instruction `Get`

```
def visitGetCall(self, ctx):  
  
    ...
```

What are the typing rules for `Async` and `Get` in our particular case?

Note: this is relatively simple and independent from the library, you can/should do it last!

The futurelib library: futurelib.h and futurelib.c

```
typedef struct {
    int Id;
    int Value;
    int resolved;
    pthread_t tid;
} FutureInt;

typedef FutureInt* futint;
FutureInt *fresh_future();
void print_futureInt(FutureInt *fut);
void free_future(FutureInt *fut);
void resolve_future(FutureInt *fut, int val);
int Get(FutureInt *fut);
FutureInt *Async(int (*fun)(int), int p);
void freeAllFutures();
```

On board: explain how the library works

We have a naive future dictionary (a large array of references)

Threads in C – Illustrated on board

```
int pthread_create(pthread_t *thread, const pthread_attr_t *  
    attr, void *(*start_routine) (void *), void *arg);
```

If attr is NULL, then the thread is created with default attributes.

A possible call:

```
int err = pthread_create(&tid, NULL, &runTask, (args));
```

Notice the function pointer (explanation on board). tid can be associated with a future **why?**

```
int pthread_join(pthread_t thread, void **value_ptr);
```

with a non-NULL value_ptr argument, the value passed to pthread_exit() by the terminating thread shall be made available in the location referenced by value_ptr.

usage:

```
pthread_join(tid, NULL);
```

Conclusion

What we have seen (2 courses)

- Different forms of parallelism (many exist)
- A few ways to describe semantics of parallel programs
- Link with type-systems and more structured programming
- Different ways to implement languages (beyond pure compilation solution)
- Illustrated by the extension of our MiniC language with futures
- A bit of practice on proofs with types and semantics

What we have not seen:

- Weak memory models
- Rich message passing semantics (only CCS)
- More advanced type system for futures, like data-flow explicit futures