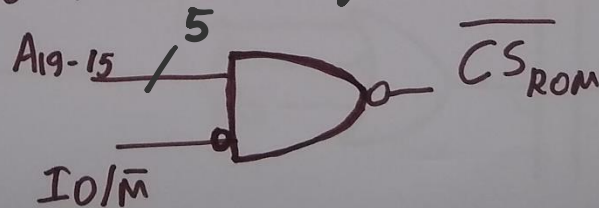
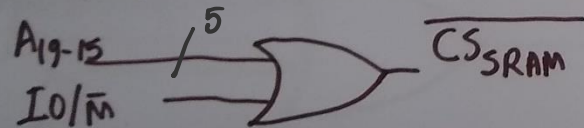


Implementar, a partir do μP 8088, as portas utilizadas no laboratório. Implemente também um sistema mínimo com 32k de SRAM (1 chip) e 32k de ROM (1 chip).

| | A_{19} | A_{18} | A_{17} | A_{16} | A_{15} | A_{14} | A_{13} | A_{12} | A_{11} | A_{10} | A_9 | A_8 | A_7 | A_6 | A_5 | A_4 | A_3 | A_2 | A_1 | A_0 |
|--------|----------|----------|----------|----------|----------|----------|----------|----------|----------|----------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| SRAM | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| ROM | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| PORTAS | { 318H | | | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| | { 319H | | | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 1 |

$$\overline{CS}_{SRAM} = A_{19} + A_{18} + A_{17} + A_{16} + A_{15} + \overline{IO/\overline{M}}$$

$$\overline{CS}_{ROM} = (A_{19} \cdot A_{18} \cdot A_{17} \cdot A_{16} \cdot A_{15} \cdot \overline{IO/\overline{M}})$$



Faixa Endereço de memória $A_{19}-A_0$

Faixa de endereço de I/O $A_{15}-A_0$

