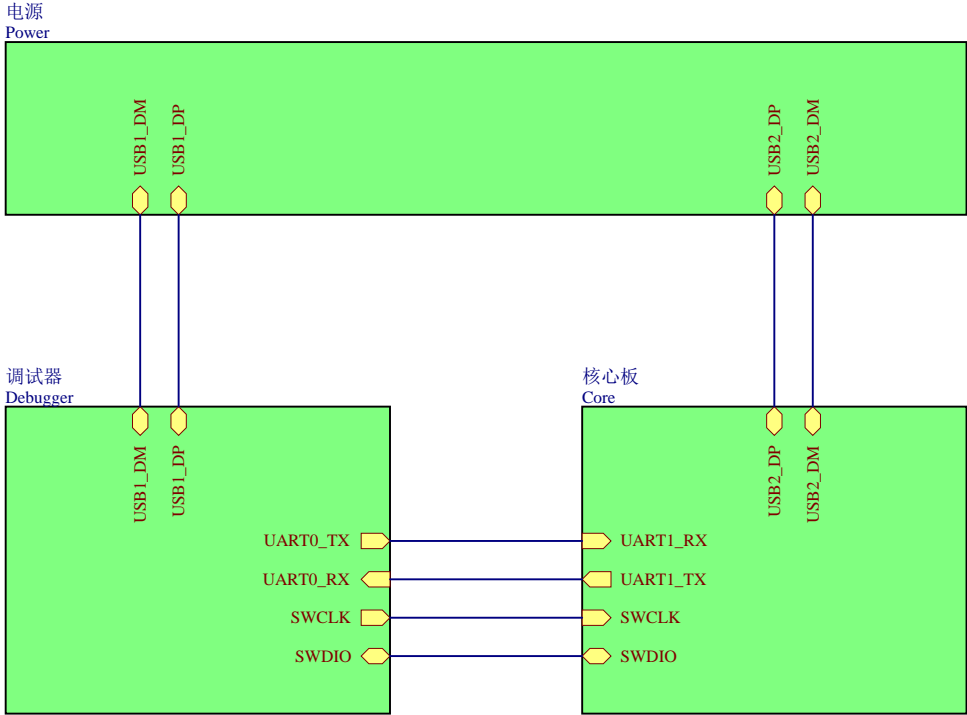


Pitaya Lite Board Rev2.0

系统框图(Block Diagram)



目录(Contents)

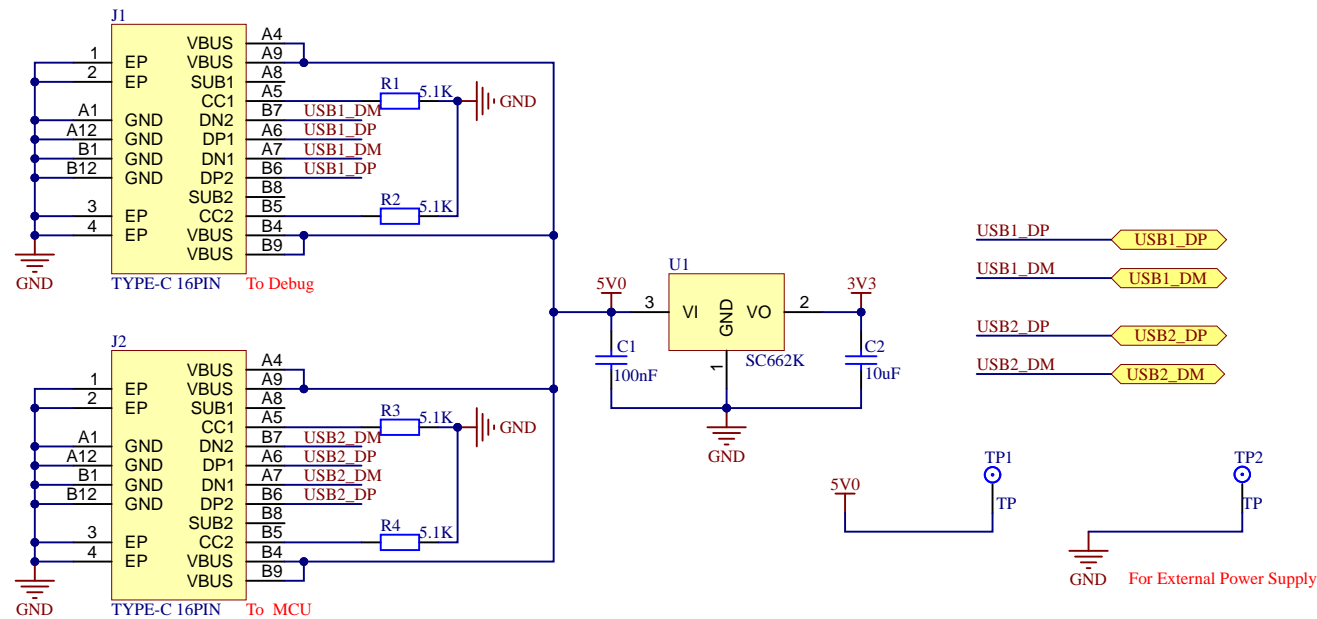
Sheet	Sheet Name
01	系统框图与基本信息
02	电源与Type-C接口
03	DAP调试器与串口
04	主控核心与扩展接口

修订记录(Revision History)

Date	Revison	Change Note
20220520	V1.0	First editor
20220601	V2.0	Add dap debugger

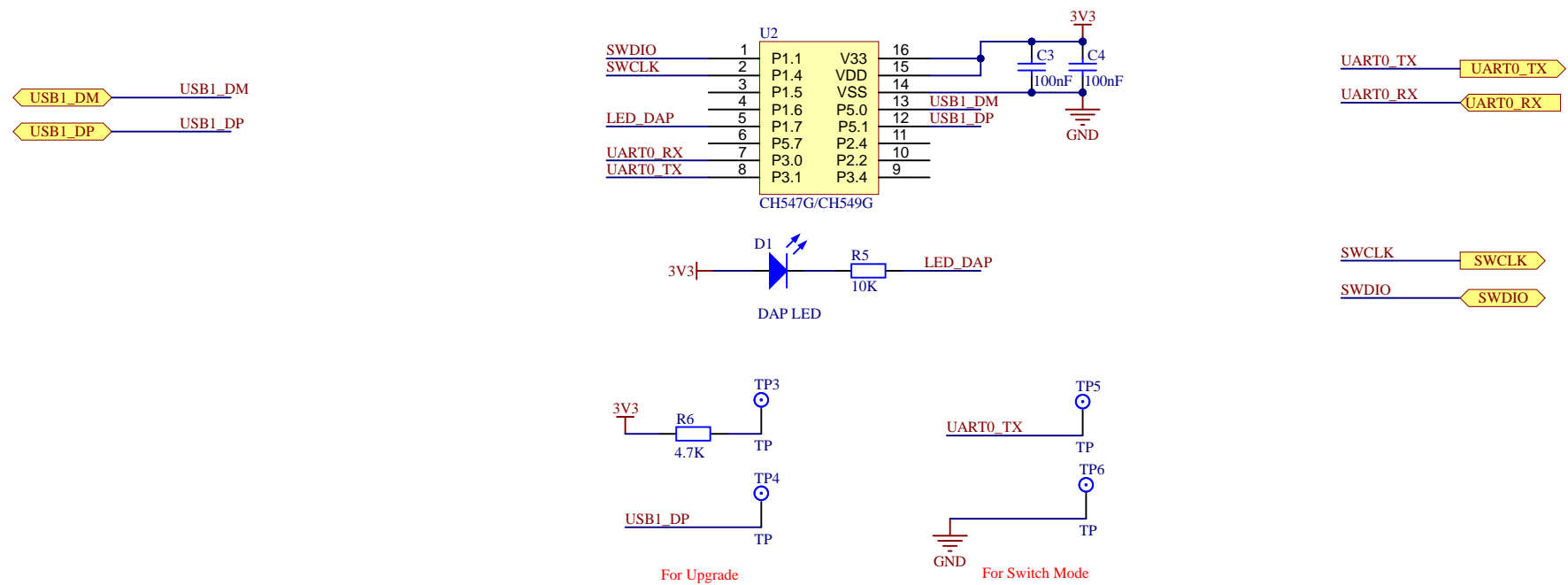
电源与Type-C接口

Power & Type-C



DAP调试器与串口

DAPLink & Serial



主控核心

MCU

USB2_DP USB2_DM

UART1_TX PB6
UART1_RX PB7

SWCLK PA14
SWDIO PA13

