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CS-3853-001-Spring-2020-Computer Architecture

Content Tests

Review Test Submission: Midterm 2

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User	Darin Sokhomony Soeung
Course	CS-3853-001-Spring-2020-Computer Architecture
Test	Midterm 2
Started	4/8/20 3:53 PM
Submitted	4/8/20 4:53 PM
Due Date	4/8/20 5:15 PM
Status	Completed
Attempt Score	49 out of 50 points
Time Elapsed	59 minutes out of 1 hour and 15 minutes

Instructions

- 1. Please let me know ASAP if you have technical difficulties.
- 2. The length of this exam is one hour and 15 minutes (i.e., 75 minutes). I will not force submission, and your submission may be late for <10 minutes. But I will take 20% late penalty for any submissions that are late for more than 10 minutes. It is much better to submit in-time than wait. For students who need special accommodation, the test length is based on the time set by SDS.
- 3. This exam is closed-book, closed-notes, and closed-everything. Please be honest.
- 4. Please answer all the questions and check your answers before submission.
- 5. This exam will be **manually graded**. Please ignore the auto-grading.
- 6. Test must be finished in one sitting.
- 7. Test must be taken during the designated time (Apr 8th 4pm to 5:15pm, 2020), unless explicitly permitted by the instructor.

Results Displayed Submitted Answers, Correct Answers, Feedback, Incorrectly Answered Questions



Question 1 2 out of 2 points



The simple RISC architecture we learned in this class has five stages. What operations does the second stage perform? Please give all the operations performed in that stage.

The second stage Instruction Decode (ID) performs the Instruction Decode and Register Read Selected Answer:

operations.

Correct Answer: 👩 Instruction decode and register read.

Response

[None Given]

Feedback:

Question 2 2 out of 2 points



A multiplexer is used to select inputs. What are the two inputs to the multiplexer used in the writeback (WB) stage?

Selected Answers: 👩 3. Mem Data

4. ALU_Out

Correct Answers: 👩 3. Mem Data

4. ALU_Out

Ouestion 3 3 out of 3 points



Why immediate values in the instructions cannot be directly used for ALU operations? How does the CPU process the immediate values before using them as ALU operands?

Selected

The immediate values are initially encoded as instructions, so we must use sign extension to be able to

Answer: pass it through the ALU.

Correct Answer: 👩

Because the immediate values have fewer bits than the operands used for ALU. Immediate values need

be "sign extended": extending a immediate value with fewer bits (e.g., 16 bits) to an immediate value

more bits (e.g., 32 bits) while retaining the sign.

Response

[None Given]

Feedback:

Question 4 1 out of 1 points



In our simple CPU implementation, which stage does the CPU check the condition of a branch instruction?

Selected Answer: Memory Stage

Response Feedback: [None Given]

Correct Answer: C EX

Question 5 2 out of 2 points



What is the difference between an exception and an interrupt?

Selected Answer:

An exception is generated from software inside the processor, and interrupt is generated outside of the processor from external sources.

Correct

🔇 An interruption is an exception generated by an external source.

Answer:

Response

[None Given]

Feedback:

Question 6 1 out of 1 points

In our simple CPU implementation, which stage does the CPU completes a branch instruction?



Selected Answer: 👩 ME

Correct Answer: 👩 ME

Question 7 2 out of 2 points



What is the motivation of employing separated instruction cache and data cache?

Selected Answer: To avoid running into structural hazards.

👩 To eliminate structural hazards for memory accesses. Correct Answer:

Response Feedback: [None Given]

Question 8 2 out of 2 points



Why assume-taken is not a solution to control hazards?

Selected Assume-taken, as opposed to assume-not taken, is not a solution because branch target is computed at Answer: execution stage far from when the next instruction needs to be provided.

Correct

Answer:

Assume taken still requires computing the branch target, which will not be available until after the execution

Response [None Given]

Feedback:

Question 9 1 out of 1 points



Consider a pipelined RISC CPU with 15 stages. What is maximum speedup of this CPU over a non-pipelined implementation?

Selected Answer: Stages = m = 15, assuming infinite instructions for maximum speedup,

Speedup = 15

Correct Answer: **3** 15

Response Feedback: [None Given]

Question 10 1 out of 1 points



Given a 3-bit Global Branch History Buffer, how many Branch History Tables do we need?

Selected Answer: 3-bit. m = 3 = 3 branches

BHT = $2^m = 2^3 = 8$ Branch History Tables

Correct Answer:

Response Feedback: [None Given]

Question 11 0 out of 1 points



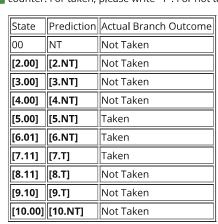
A branch that is used to check a loop's condition is better predicted with a 2-bit saturating counter.

Selected Answer: 🔞 False Correct Answer: 👩 True

Question 12 12 out of 12 points



Please fill the following table with branch predictor states and branch outcome predictions using the 2-bit saturated 🌠 counter. For taken, please write "T". For not taken, please write "NT".



Specified Answer for: 2.00 **3** 00

Specified Answer for: 2.NT 👩 NT

Specified Answer for: 3.00 **6**00

Specified Answer for: 3.NT

Specified Answer for: 4.00 00

Specified Answer for: 4.NT 👩 NT

Specified Answer for: 5.00 **00**

Specified Answer for: 5.NT 👩 NT

Correct Answers for: 2.00	
Specified Answer for: 10.NT	🥎 NT
Specified Answer for: 10.00	% 00
Specified Answer for: 9.T	🍼 T
Specified Answer for: 9.10	% 10
Specified Answer for: 8.T	o T
Specified Answer for: 8.11	% 11
Specified Answer for: 7.T	o T
Specified Answer for: 7.11	% 11
Specified Answer for: 6.NT	🥎 NT
Specified Answer for: 6.01	% 01

Specified Answer for: 10.NT 👩 NT		
Correct Answers for: 2.00		
Evaluation Method	Correct Answer	Case Sensitivity
Sexual Match	00	
Correct Answers for: 2.NT		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	NT	
Correct Answers for: 3.00		
Evaluation Method	Correct Answer	Case Sensitivity
Sexual Match	00	
Correct Answers for: 3.NT		
Evaluation Method	Correct Answer	Case Sensitivity
Sexual Match	NT	
Correct Answers for: 4.00		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	00	
Correct Answers for: 4.NT		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	NT	
Correct Answers for: 5.00		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	00	
Correct Answers for: 5.NT		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	NT	
Correct Answers for: 6.01		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	01	
Correct Answers for: 6.NT		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	NT	
Correct Answers for: 7.11		
Evaluation Method	Correct Answer	Case Sensitivity
Sexact Match	11	
Correct Answers for: 7.T		
Evaluation Method	Correct Answer	Case Sensitivity
Sexual Match	Т	
Correct Answers for: 8.11		



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Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	11		
Correct Answers for: 8.T			
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	Т		
Correct Answers for: 9.10			
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	10		
Correct Answers for: 9.T			
Evaluation Method	Correct Answer	Case Sensitivity	
Sexact Match	Т		
Correct Answers for: 10.00			
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	00		
Correct Answers for: 10.NT			
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	NT		

Question 13 20 out of 20 points



Consider the code segment below. Assume that every pipeline stage takes a single cycle. Assume X and Y are constants. 🛂 Assume that at the branch instruction, R2 is not zero and **the branch will be taken**. Assume data bypassing/forwarding is implemented. Assume there is no branch prediction (or any other control hazard solutions). You may not reorder instructions. You can fill the pipeline slots with stalls/bubbles as needed. The instructions are in the format of "opcode, dest, src1, src2".

```
L0: mov R1. [X]
   mov R2, [Y]
   sub R2, R2, R1
   jnz R2, L1
L1: add R3, R4, R5
```

Please fill the following table to represent the execution of this code on a standard 5-stage RISC pipeline. From empty cells (empty cycles), please write "_" (underscore).

Cycles	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
mov R1, [X]	[1. IF]	[1.ID]	[1.EX]	[1.ME]	[1.WB]	[1.X1]	[1.X2]	[1.X3]	[1.X4]								
mov R2, [Y]		[2.IF]	[2. I D]	[2.EX]	[2.ME]	[2.WB]	[2.X1]	[2.X2]	[2.X3]	[2.X4]							
sub R2, R2, R1			[3.IF]	[3. I D]	[3.BU]	[3.EX]	[3.ME]	[3.WB]	[3.X1]	[3.X2]	[3.X3]						
jnz R2, L1				[4.IF]	[4.BU]	[4. I D]	[4.EX]	[4.ME]	[4.WB]	[4.X1]	[4.X2]	[4.X3]					
add R3, R4, R5					[5.BU1]	[5.BU2]	[5.BU3]	[5.BU4]	[5.IF]	[5.ID]	[5.EX]	[5.ME]	[5.WB]	[5.X1]	[5.X2]	[5.X3]	[5.X4]

Specified Answer for: 1.IF



Specified Answer for: 1.ID





Specified Answer for: 1.EX	🔞 E
Specified Answer for: 1.ME	3 M
Specified Answer for: 1.WB	🔞 W
Specified Answer for: 1.X1	Ø _
Specified Answer for: 1.X2	Ø _
Specified Answer for: 1.X3	Ø _
Specified Answer for: 1.X4	Ø _
Specified Answer for: 2.IF	o IF
Specified Answer for: 2.ID	o ID
Specified Answer for: 2.EX	③ E
Specified Answer for: 2.ME	3 M
Specified Answer for: 2.WB	3 W
Specified Answer for: 2.X1	_
Specified Answer for: 2.X2	_
Specified Answer for: 2.X3	_
Specified Answer for: 2.X4	Ø _
Specified Answer for: 3.IF	⊘ IF
Specified Answer for: 3.ID	o ID
Specified Answer for: 3.BU	© В
Specified Answer for: 3.EX	⊙ E
Specified Answer for: 3.ME	3 M
Specified Answer for: 3.WB	⊙ W
Specified Answer for: 3.X1	Ø _
Specified Answer for: 3.X2	% _
Specified Answer for: 3.X3	_
Specified Answer for: 4.IF	o IF
Specified Answer for: 4.BU	© В
Specified Answer for: 4.ID	o ID
Specified Answer for: 4.EX	😘 E
Specified Answer for: 4.ME	3 M
Specified Answer for: 4.WB	O W
Specified Answer for: 4.X1	_ _
Specified Answer for: 4.X2	% _
Specified Answer for: 4.X3	_
Specified Answer for: 5.BU1	© В
Specified Answer for: 5.BU2	3 В
Specified Answer for: 5.BU3	3 В
Specified Answer for: 5.BU4	3 В
Specified Answer for: 5.IF	⊘ IF
Specified Answer for: 5.ID	o ID
Specified Answer for: 5.EX	③ E
Specified Answer for: 5.ME	3 M
Specified Answer for: 5.WB	3 W
Specified Answer for: 5.X1	Ø _
Specified Answer for: 5.X2	0 _
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Review	Test Submission: Midterm 2 – CS-38	53-001-Spring	
Specified Answer for: 5.X3			
Specified Answer for: 5.X4 👩 _			
Correct Answers for: 1.IF			
Evaluation Method	Correct Answer	Case Sensitivity	
🤡 Exact Match	IF		
Correct Answers for: 1.ID			
Evaluation Method	Correct Answer	Case Sensitivity	
🔇 Exact Match	ID		
Correct Answers for: 1.EX			
Evaluation Method	Correct Answer	Case Sensitivity	
🔇 Exact Match	EX		
Correct Answers for: 1.ME			
Evaluation Method	Correct Answer	Case Sensitivity	
🤡 Exact Match	ME		
Correct Answers for: 1.WB			
Evaluation Method	Correct Answer	Case Sensitivity	
📀 Exact Match	WB		,
Correct Answers for: 1.X1			
Evaluation Method	Correct Answer	Case Sensitivity	
or Contains	_		I
Correct Answers for: 1.X2			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	_		I
Correct Answers for: 1.X3			
Evaluation Method	Correct Answer	Case Sensitivity	
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Correct Answers for: 1.X4			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	_	•	
Correct Answers for: 2.IF			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	lF		
Correct Answers for: 2.ID	<u> </u>		
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	ID	case sensitivity	
Correct Answers for: 2.EX			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	EX	cuse sensitivity	
Correct Answers for: 2.ME	LA		
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	ME	case sensitivity	
Correct Answers for: 2.WB	IVIL		
Evaluation Method	Correct Anguer	Casa Sonsitivity	
	Correct Answer	Case Sensitivity	
Sowroot Appropriators 2 V1	WB		
Correct Answers for: 2.X1	C A.	Cons Constitution	
Evaluation Method	Correct Answer	Case Sensitivity	
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	Test Submission: Midterm 2 – CS-38	53-001-Spring	I
Correct Answers for: 2.X2			
Evaluation Method	Correct Answer	Case Sensitivity	
ontains Contains	-		
Correct Answers for: 2.X3			
Evaluation Method	Correct Answer	Case Sensitivity	
ontains Contains	-		
Correct Answers for: 2.X4			
Evaluation Method	Correct Answer	Case Sensitivity	
ontains Contains	-		
Correct Answers for: 3.IF			
Evaluation Method	Correct Answer	Case Sensitivity	
📀 Exact Match	IF		
Correct Answers for: 3.ID			
Evaluation Method	Correct Answer	Case Sensitivity	
🤡 Exact Match	ID		·
Correct Answers for: 3.BU			
Evaluation Method	Correct Answer	Case Sensitivity	
or Contains	BU		'
or Contains	ST		
Correct Answers for: 3.EX			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	EX		'
Correct Answers for: 3.ME			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	ME	•	I
Correct Answers for: 3.WB			
Evaluation Method	Correct Answer	Case Sensitivity	
🔇 Exact Match	WB	•	I
Correct Answers for: 3.X1			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	Correct Allower	case sensitivity	
Correct Answers for: 3.X2	-		
Evaluation Method	Correct Answer	Case Sensitivity	
	Correct Allswer	case sensitivity	
Contains	-		
Correct Answers for: 3.X3			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	-		
Correct Answers for: 4.IF			
Evaluation Method	Correct Answer	Case Sensitivity	
Secondary Exact Match	IF		
Correct Answers for: 4.BU			
Evaluation Method	Correct Answer	Case Sensitivity	
🔇 Contains	BU		ı
ontains Contains	ST		
Correct Answers for: 4.ID			
Evaluation Method	Correct Answer	Case Sensitivity	
👩 Exact Match	ID		



Re	eview Test Submission: Midterm 2 – CS-385	53-001-Spring	
Evaluation Method	Correct Answer	Case Sensitivity	
Ontains	EX		
Correct Answers for: 4.ME			
Evaluation Method	Correct Answer	Case Sensitivity	
Ontains	ME		
Correct Answers for: 4.WB			
Evaluation Method	Correct Answer	Case Sensitivity	
Sexact Match	WB		'
Correct Answers for: 4.X1			
Evaluation Method	Correct Answer	Case Sensitivity	
Ontains	-		ı
Correct Answers for: 4.X2			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	_	·	I
Correct Answers for: 4.X3			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains		•	I
Correct Answers for: 5.BU1	-		
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	BU	,	
	ST		
Correct Answers for: 5.BU2			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	BU		
	ST		
Correct Answers for: 5.BU3			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	BU	•	I
⊘ Contains	ST		
Correct Answers for: 5.BU4			
Evaluation Method	Correct Answer	Case Sensitivity	
⊘ Contains	BU		
Exact Match	ST		
Correct Answers for: 5.IF			
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	IF	,	I
Correct Answers for: 5.ID	<u> </u>		
Evaluation Method	Correct Answer	Case Sensitivity	
Exact Match	ID		
Correct Answers for: 5.EX			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	EX		
Correct Answers for: 5.ME			
Evaluation Method	Correct Answer	Case Sensitivity	
Contains	ME		
Correct Answers for: 5.WB			
Evaluation Method	Correct Answer	Case Sensitivity	
	COLLECTAIDME	case sensitivity	



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Correct Answers for: 5.X1		
Evaluation Method	Correct Answer	Case Sensitivity
Contains	_	
Correct Answers for: 5.X2		
Evaluation Method	Correct Answer	Case Sensitivity
⊘ Contains	-	
Correct Answers for: 5.X3		
Evaluation Method	Correct Answer	Case Sensitivity
Contains	-	
Correct Answers for: 5.X4		
Evaluation Method	Correct Answer	Case Sensitivity
C Contains	_	



 $\leftarrow \text{OK}$