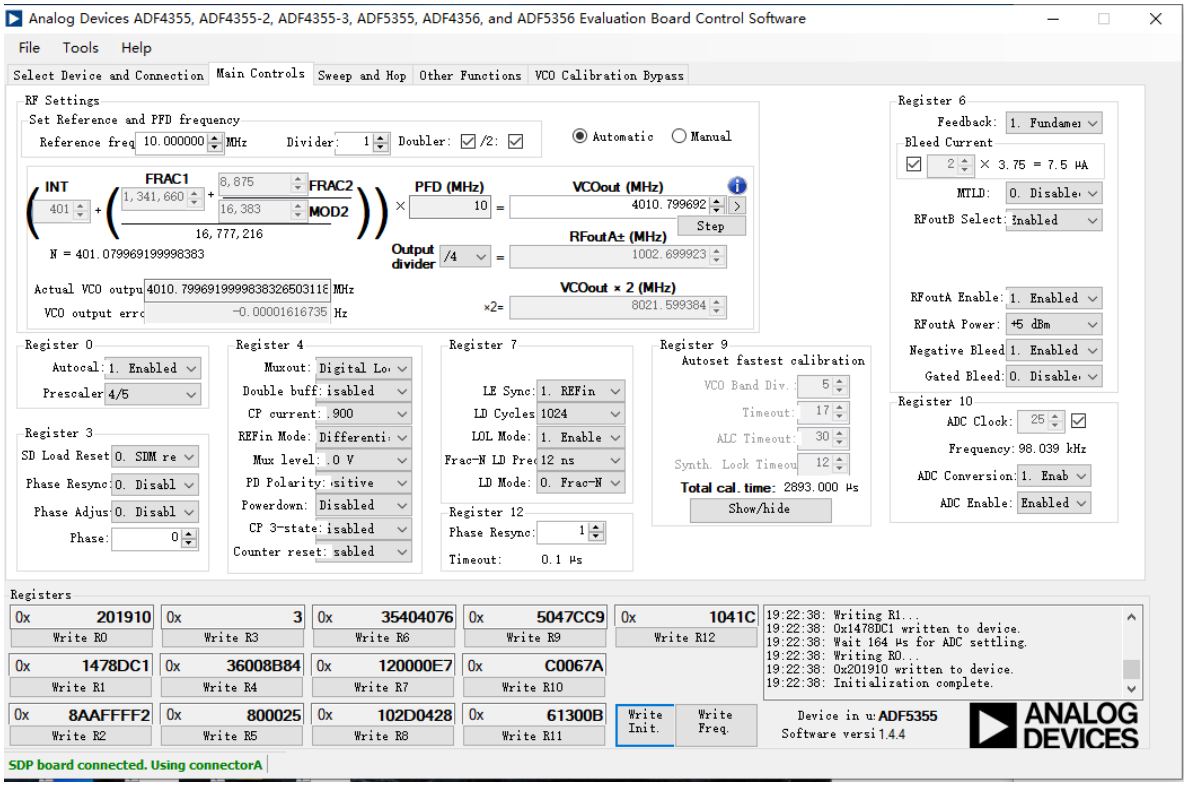
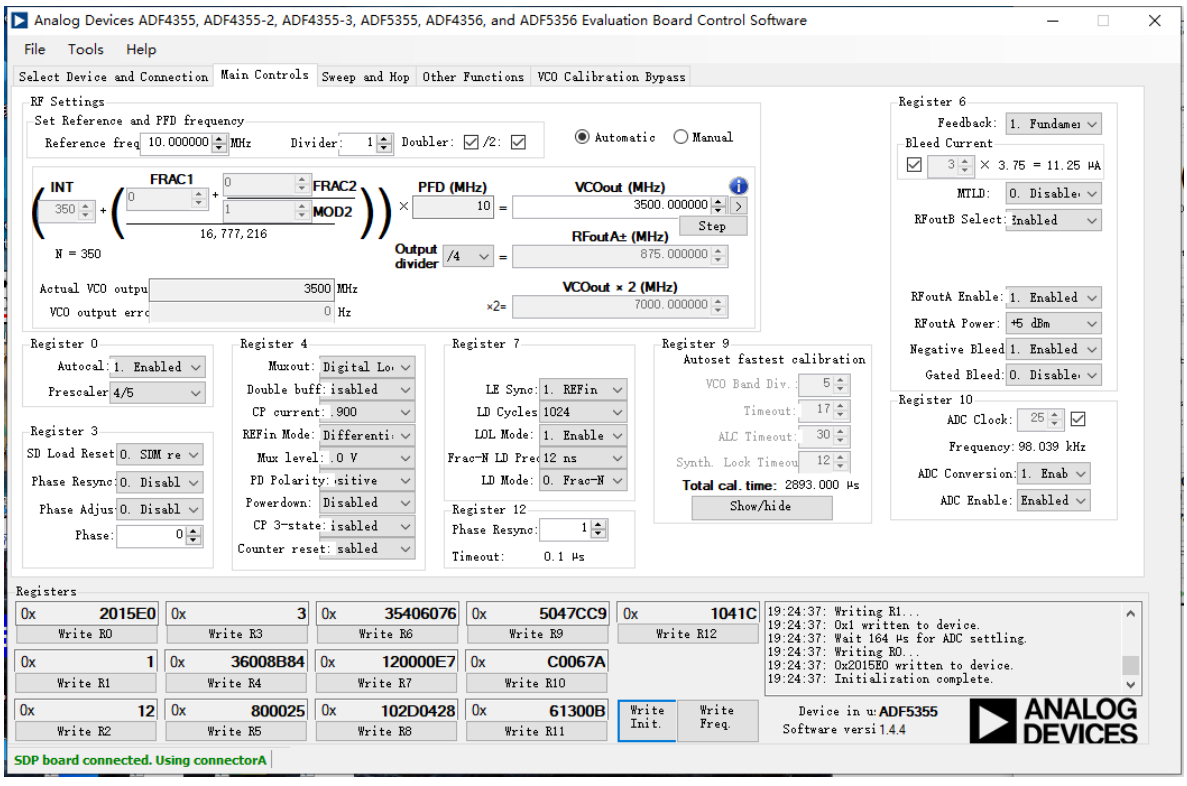

Analog Devices ADF4355, ADF4355-2, ADF4355-3, ADF5355, ADF4356, and ADF5356 Evaluation Board Control Software
File
Tools
Help
Select Device and Connection
Main Controls
Sweep and Hop
Other Functions
VCO Calibration Bypass
RF Settings
Set Reference and PFD frequency
Reference freq
10.000000
MHz
Divider:
Doubler:
/2:
Automatic
Manual
INT
401
FRAC1
1, 341, 660
FRAC2
8, 875
MOD2
16, 383
16, 777, 216
N = 401.079969199998383
PFD (MHz)
10
VCOout (MHz)
4010.799692
Step
RFoutA± (MHz)
1002.699923
Output divider
/4
VCOout × 2 (MHz)
8021.599384
×2=
Actual VCO outpu
4010.799691999983832650311
MHz
VCO output erro
-0.00001616735
Hz
Register 0
Autocal:
1. Enabled
Prescaler:
4/5
Register 3
SD Load Reset:
Phase Resync:
Phase Adjus:
Phase:
Register 4
Muxout:
Digital Lo
Double buff:
isabled
CP current:
.900
REFin Mode:
Differenti
Mux level:
.0 V
PD Polarity:
sitive
Powerdown:
Disabled
CP 3-state:
isabled
Counter reset:
sabled
Register 7
LE Sync:
1. REFin
LD Cycles:
1024
LDL Mode:
1. Enable
Frac-N LD Prec
12 ns
LD Mode:
0. Frac-N
Register 12
Phase Resync:
1
Timeout:
0.1 µs
Register 9
Autoset fastest calibration
VCO Band Div.:
5
Timeout:
17
ALC Timeout:
30
Synth. Lock Timeou
12
Total cal. time:
2893.000 µs
Show/hide
Register 6
Feedback:
1. Fundame
Bleed Current
2
× 3.75 = 7.5 µA
MTLD:
0. Disable
RFoutB Select:
Enabled
RFoutA Enable:
1. Enabled
RFoutA Power:
+5 dBm
Negative Bleed:
1. Enabled
Gated Bleed:
0. Disable
Register 10
ADC Clock:
25
Frequency:
98.039 kHz
ADC Conversion:
1. Enab
ADC Enable:
Enabled
Registers
0x 201910
Write R0
0x 3
Write R3
0x 35404076
Write R6
0x 5047CC9
Write R9
0x 1041C
Write R12
0x 1478DC1
Write R1
0x 36008B84
Write R4
0x 120000E7
Write R7
0x C0067A
Write R10
0x 8AAFFFF2
Write R2
0x 800025
Write R5
0x 102D0428
Write R8
0x 61300B
Write R11
Write Init.
Write Freq.
19:22:38: Writing R1...
19:22:38: 0x1478DC1 written to device.
19:22:38: Wait 164 µs for ADC settling.
19:22:38: Writing R0...
19:22:38: 0x201910 written to device.
19:22:38: Initialization complete.
Device in u: ADF5355
Software versi 1.4.4
ANALOG DEVICES
SDP board connected. Using connectorA


Analog Devices ADF4355, ADF4355-2, ADF4355-3, ADF5355, ADF4356, and ADF5356 Evaluation Board Control Software
File
Tools
Help
Select Device and Connection
Main Controls
Sweep and Hop
Other Functions
VCO Calibration Bypass
RF Settings
Set Reference and PFD frequency
Reference freq
10.000000
MHz
Divider:
Doubler:
/2:
Automatic
Manual
INT
350
FRAC1
0
FRAC2
0
MOD2
1
16, 777, 216
N = 350
PFD (MHz)
10
VCOout (MHz)
3500.000000
Step
RFoutA± (MHz)
875.000000
Output divider
/4
VCOout × 2 (MHz)
7000.000000
×2=
Actual VCO outpu
3500
MHz
VCO output erro
0
Hz
Register 0
Autocal:
1. Enabled
Prescaler:
4/5
Register 3
SD Load Reset:
Phase Resync:
Phase Adjus:
Phase:
Register 4
Muxout:
Digital Lo
Double buff:
isabled
CP current:
.900
REFin Mode:
Differenti
Mux level:
.0 V
PD Polarity:
sitive
Powerdown:
Disabled
CP 3-state:
isabled
Counter reset:
sabled
Register 7
LE Sync:
1. REFin
LD Cycles:
1024
LDL Mode:
1. Enable
Frac-N LD Prec
12 ns
LD Mode:
0. Frac-N
Register 12
Phase Resync:
1
Timeout:
0.1 µs
Register 9
Autoset fastest calibration
VCO Band Div.:
5
Timeout:
17
ALC Timeout:
30
Synth. Lock Timeou
12
Total cal. time:
2893.000 µs
Show/hide
Register 6
Feedback:
1. Fundame
Bleed Current
3
× 3.75 = 11.25 µA
MTLD:
0. Disable
RFoutB Select:
Enabled
RFoutA Enable:
1. Enabled
RFoutA Power:
+5 dBm
Negative Bleed:
1. Enabled
Gated Bleed:
0. Disable
Register 10
ADC Clock:
25
Frequency:
98.039 kHz
ADC Conversion:
1. Enab
ADC Enable:
Enabled
Registers
0x 2015E0
Write R0
0x 3
Write R3
0x 35406076
Write R6
0x 5047CC9
Write R9
0x 1041C
Write R12
0x 1
Write R1
0x 36008B84
Write R4
0x 120000E7
Write R7
0x C0067A
Write R10
0x 12
Write R2
0x 800025
Write R5
0x 102D0428
Write R8
0x 61300B
Write R11
Write Init.
Write Freq.
19:24:37: Writing R1...
19:24:37: 0x1 written to device.
19:24:37: Wait 164 µs for ADC settling.
19:24:37: Writing R0...
19:24:37: 0x2015E0 written to device.
19:24:37: Initialization complete.
Device in u: ADF5355
Software versi 1.4.4
ANALOG DEVICES
SDP board connected. Using connectorA