

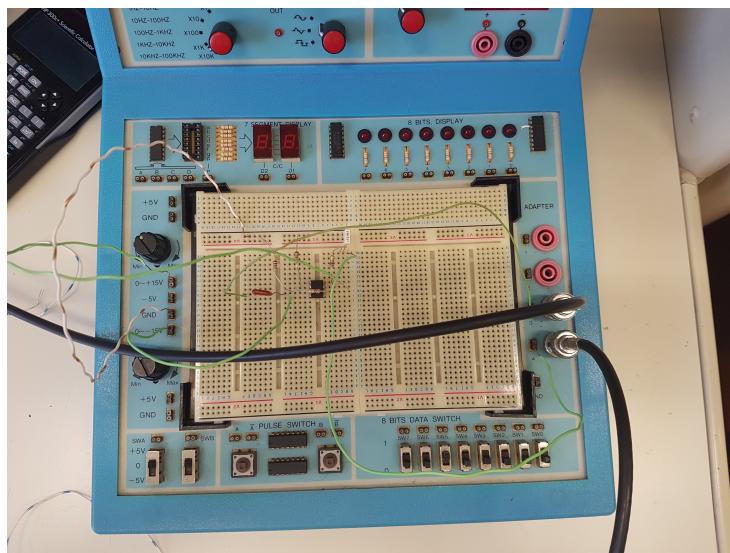
Circuit Theory and Electronics Fundamentals

Aerospace Engineering, Técnico, University of Lisbon

T5 Laboratory Report

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1 Introduction

The objective of this laboratory assignment is to make a Bandpass filter using an OP-AMP. The equivalent circuit can be seen in Figure 1.

This circuit is made up of an amplifier (an OP-AMP and resistors R_3 and R_4), a high pass filter (the left part, that is C_1 and R_1), and a low pass filter (the right part, that is R_2 and C_2).

In Section 2, the circuit is analysed by means of a ngspice simulation. In Section 3, a theoretical analysis of the circuit is presented. The results are then compared in Section 3. The conclusions of this study are outlined in Section 4.

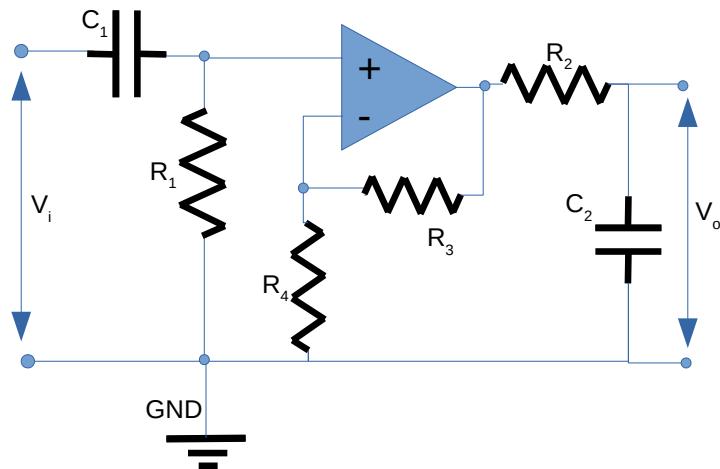


Figure 1: Used Circuit.

2 Simulation Analysis

2.1 Incremental Modifications

In order to maximize the M value, the provided circuit was used, and small changes were made to its parameters in order to evaluate their results. Since the number and type of components available were limited, not only were the best parameters evaluated but it was also looked into the best way of making the respective component from coupling other components in various ways.

In order to maximize the merit value, we started with the cheapest combination of C's and R's ($C = 0.22 \mu\text{F}$ and $R = 1 \text{k}\Omega$). Then we calculated the corresponding frequency with the formulas 1, 2 and 3 discussed in the theoretical analysis. We noted that it was lower than the target central frequency. That meant that we needed the appropriate higher frequency to keep the 1kHz central frequency (formula 3). Using once again formula 1 and keeping the same resistance we found that the C_2 value we were looking for was $0.110 \mu\text{F}$, that could be achieved by combining 2 $0.220 \mu\text{F}$ capacitors in series.

For R_3 and R_4 , we took a more trial and error focused approach. We fixed the value of R_4 to $1\text{k}\Omega$ as it was the lowest value, and then played with the value of R_3 , knowing that a higher resistance means a higher gain, to make the gain as close to 40 dB while also trying to maximize the merit.

2.1.1 Cost

Component	Parameter Value
Op Amp	13323 Mu
C_1	$0.220 \mu\text{F}$
C_2	$0.110 \mu\text{F}$ (2x $0.220 \mu\text{F}$)
R_1	$1 \text{k}\Omega$
R_2	$1 \text{k}\Omega$
R_3	$110 \text{k}\Omega$
R_4	$1 \text{k}\Omega$
Total cost	13436.66 MU

Table 1: Table of costs. Note that the total cost is sum of the the other values.

2.1.2 Results

Parameter	Value
Voltage Gain	37.344430
lowerCutoffFreq	406.289000
higherCutoffFreq	2463.671000
f0	1000.481098
input impedance	$999.988900 + i\cdot 723.544000 \text{ ohm}$
output impedance	$681.052100 + i\cdot 466.790000 \text{ ohm}$
Cost	13437.052039
M	0.000040

Table 2: Spice Results

3 Theoretical Analysis

3.1 Results at Central Frequency

Because this a band pass filter there is a lower cut off frequency, f_L and a high cut off frequency, f_H . With these frequencies, we can define ω_L and ω_H .

The former can be calculated like so,

$$\omega_L = \frac{1}{R_1 C_1} \quad (1)$$

and the latter can be calculated like so,

$$\omega_H = \frac{1}{R_2 C_2} \quad (2)$$

The central frequency can then be obtained through the following formula:

$$\omega_0 = \sqrt{\omega_L \omega_H} \quad (3)$$

Theoretical results	value (Hz)	Simulation results	value (Hz)
f_L	723.431560	f_L	406.289000
f_H	1446.863119	f_H	2463.671000
f_0	1023.086723	f_0	1000.481098

Despite not being able to predict accurately the lower and higher cut off frequency, the central frequency can be predicted quite well.

To compute gain of the circuit at the central frequency, the circuit was analysed in its three parts, the high pass filter, the amplification stage and the low pass filter, resulting in the respective formulae:

$$gain_{HPF} = \frac{j\omega_0 R_1 C_1}{1 + j\omega_0 R_1 C_1}; \quad (4)$$

$$gain_{Amplifier} = 1 + R_3/R_4; \quad (5)$$

$$gain_{LPF} = \frac{1}{1 + j\omega_0 R_2 C_2}. \quad (6)$$

The gain can then be obtained by the multiplication of each of the previous gains.

In order to obtain the input and output impedances the following formulae were used:

$$Z_i = \frac{1}{j\omega_0 C_1} + R_1 \quad (7)$$

$$Z_o = \frac{1}{1/R_2 + j\omega_0 C_2} \quad (8)$$

This results in the following results:

Theoretical results	value
gain	37.384634 (dB)
input impedance	$1000.000000 + i -707.106781 \text{ Ohm}$
output impedance	$666.666667 + i -471.404521 \text{ Ohm}$
Simulation results	value
gain	37.344430 (dB)
input impedance	$999.988900 + i -723.544000 \text{ Ohm}$
output impedance	$681.052100 + i -466.790000 \text{ Ohm}$

As it was expected, the gain, the input impedance and output impedances are similar.

3.2 Frequency Response

The frequency response can be obtained the same way as it was done with the gain, but by applying the formula to many frequencies. This way, the transfer function will be:

$$f_{res} = \frac{R_1 C_1 \omega j}{1 + R_1 C_1 \omega j} \times \left(1 + \frac{R_3}{R_4}\right) \times \frac{1}{1 + R_2 C_2 \omega j} \quad (9)$$

With this function we can obtain the following plots:

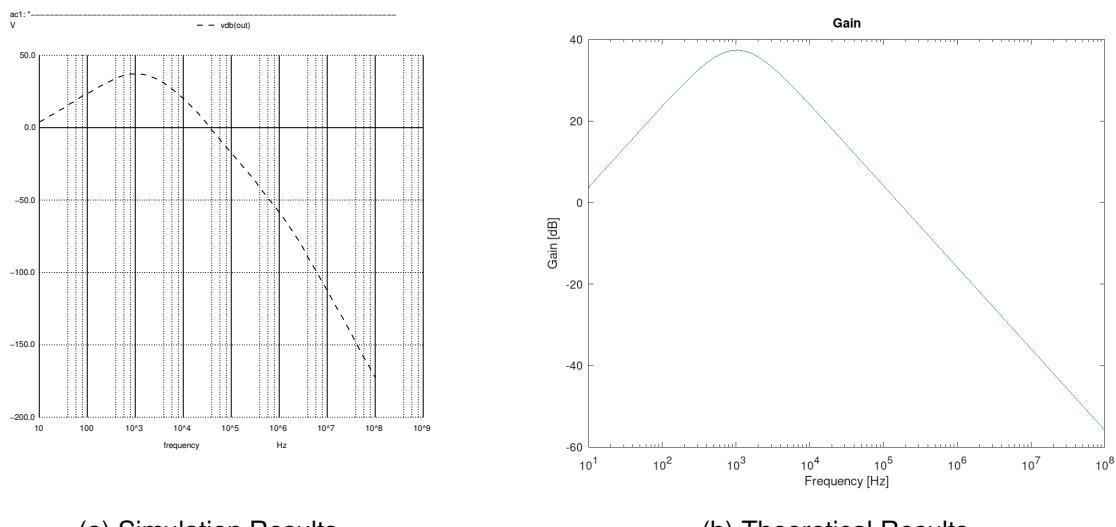


Figure 3: Gain

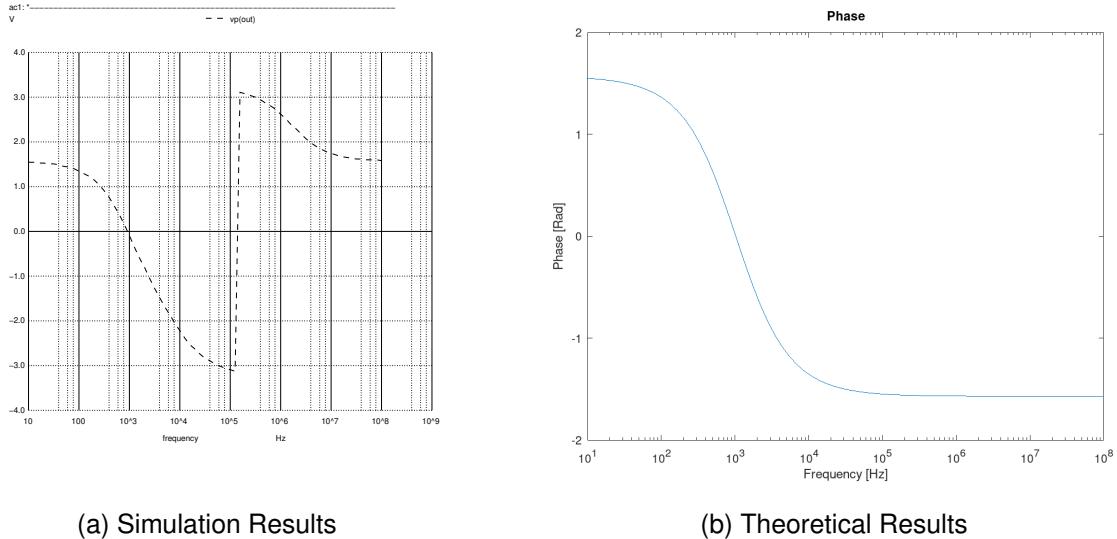


Figure 5: Phase

By looking at the previous results, it is possible to see that the theoretical model can predict quite well the value of the gain given a certain frequency. However, the same can't be said about the phase plots. Although, they start similar, as the frequencies get bigger, the results become completely different.

This way, the merit will be

M	1.5142e-06
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Table 3: Theoretical M

M	4.04744e-05
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Table 4: Simulation M

Even with the previous mistakes, the theoretical analysis yields a decent estimate for the merit value with a similar order of magnitude.

4 Conclusion

Despite some differences, the results can be said to be similar.

Furthermore, we believe that the differences are not that significant and they can be explained by how NGSpice solves the circuit compared to how it was done in the theoretical analysis.

To solve the circuit, NGSpice used far more advanced simulation methods for the OP-AMP, with many more parameters.

This way, the objective should have never been to have equal results, but rather, have results that are "close enough", which we believe it was achieved.

Furthermore, one detrimental goal was to analyse and choose values for the circuit's elements that assure it's best and most optimal performance, which by taking into consideration the obtained data, we believe it was achieved.

We would also like to add that this lab enabled us to have the experience of going to the lab and build the real circuit that it was used. It showcased the advances of using simulators like NGSpice over the physical circuit, because they solve the problem of having broken pieces and allow much more flexibility in the analysis. Nevertheless, we still think that is crucial to have the experience of going to the lab if building the circuits "by hand", in order to learn how to work with real equipment and how to deal with real world problems.