

Instituto Superior Técnico

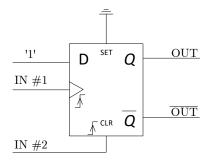
Sistemas de Processamento Digital de Sinais (SPDSina)

XOR phase detector

Consider two square-wave signals with period T. Determine and sketch the static phase detector characteristic of a XOR circuit operating with these signals with amplitude 0 V and 1 V. Generalize for amplitudes V_N e V_P .

Phase detector with a D-type flip-flop

Consider the phase detector in the figure which operates with rectangular signals with duty cycles δ_1 and $\delta_2 < \delta_1$, period T and amplitude 0 V and 1 V. Show that its operation is independent of the signals' duty-cycle. Determine and sketch the phase detector static characteristic. What happens when the input signals are switched?



thase Detectors 4XOR> = == ,0 < 0 < . (XOR) = 2(T-D), 72 KD ST Stime 2 Vp FOR VP, W: (xop)= VP=+ W (F=24) 05035 =Vp华+W(1-学) (XUR) (xur) - Vp 2(T-A)+VN T-2(T-A) モンロミナ = Vp 2(1字) + W(学-1) OSAST (中)=千1 < Q> = T=== 1-=, OSAST #1 and Q #2 switched Because both come and were inputs are edge-trisgered, the operation is only sensitive to jositive-going transitions. Therefore it does not depend on either of or or Columny-cycle) - The available phase is 2T because the detector only has one slope, ether 40> positive (KD)0) or negative (KD(0) when the inputs one switched KOLO - the characteritise (Q) is periodic withe period equal to 21. - Need to be careful about PLL return Hand #2 (21)