

Sistemas de Processamento Digital de Sinais (SPDSina)

IIR Digital Filter

Consider a digital signal processing system operating with $f_s=1/T_s=10$ kHz and the design of an IIR digital high-pass filter using the bilinear transformation applied to the first order analog filter $H_{\rm HP}(s)=\frac{s}{s+\omega_c}$ with cut-off frequency ω_c .

- a) Design the filter determining its transfer function H(z) so that the cut-off frequency of the **digital** filter is exactly 4π krad/s (2 kHz). Write the difference equation and sketch the signal flow diagram of the filter using direct form I and direct form II.
- b) Determine the gain of the digital filter for $\,f=0\,,\;f=f_{\!c}\,$ and $\,f=f_{\!s}\,\,/\,\,2\,.$
- c) Sketch qualitatively the magnitude of the frequency response of the analog and the digital filters for $0 \le f \le f_s$ (use the values from the previous question). Explain the differences between the two frequency responses.

