Lab6 Intro
Profiling and Performance Tuning

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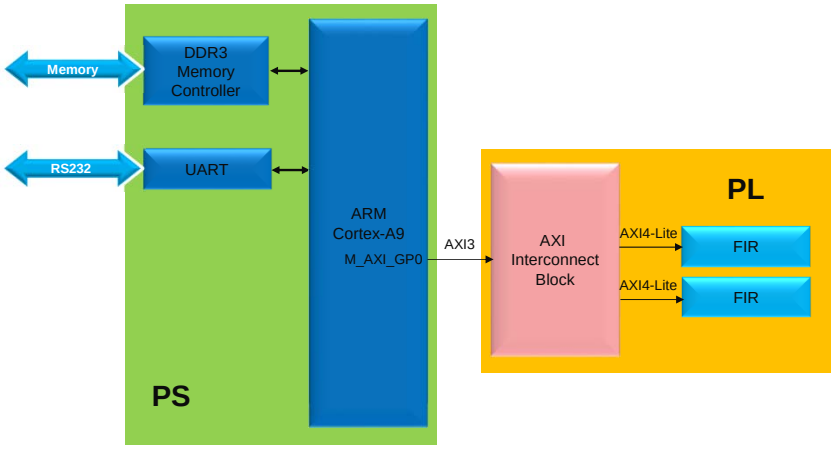
Introduction

- This lab guides you through the process of profiling an application with a function modeled in software, analyzing the profiled output, and then profiling the application using the hardware accelerated function in the application.

ARM Cortex-A9 based Embedded System Design



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Procedure

- Create a Vivado project
- Create the hardware system using IP Integrator
- Add two instances of the provided FIR core
- Generate the bitstream
- Export the design to the SDK
- Create the application
- Run the application and profile

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Summary

- This lab led you through enabling the software BSP and the application settings for the profiling. You went through creating the hardware which included the hardware IP and was later profiled in the application. You analyzed the profiled application output.