
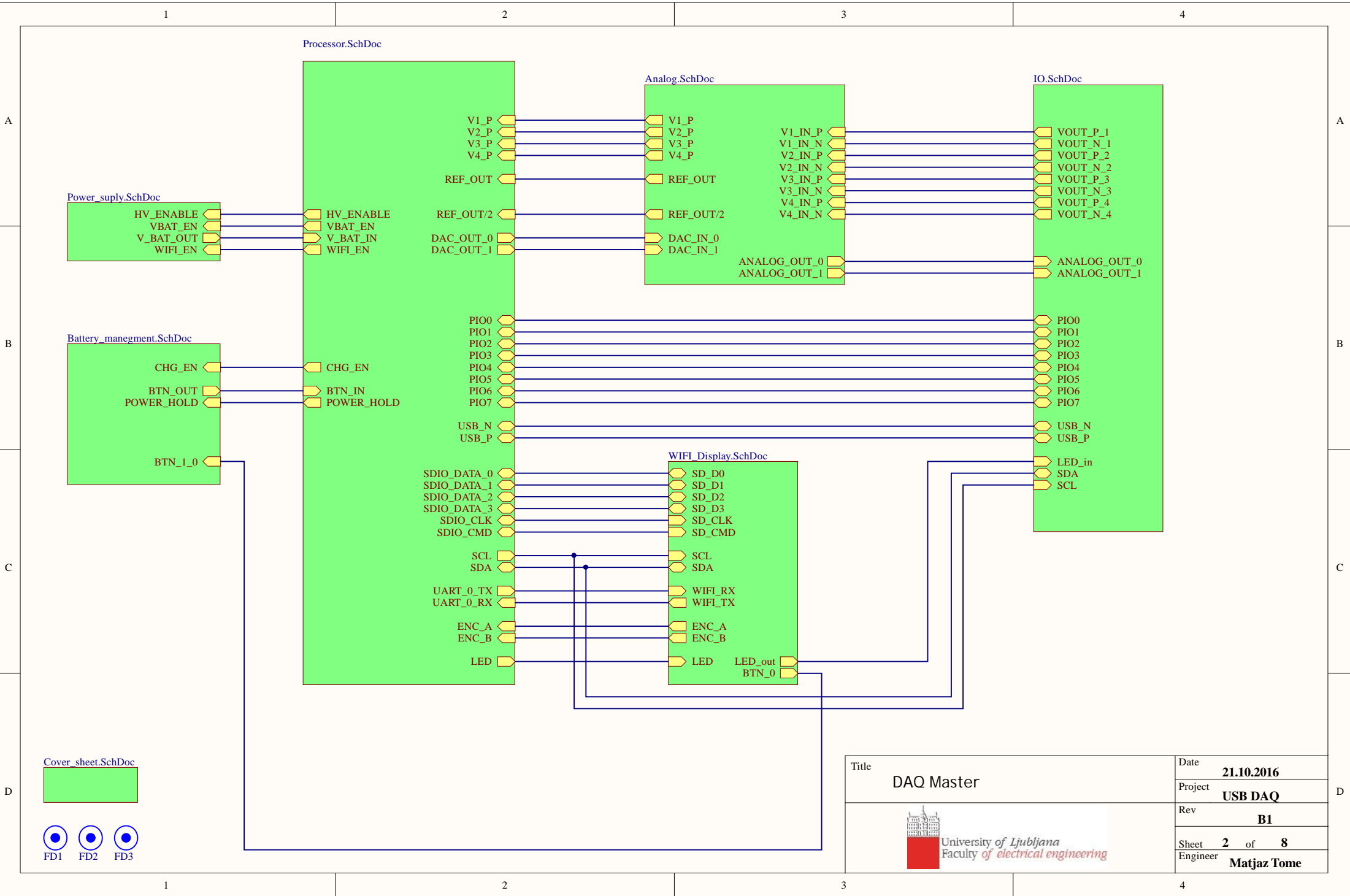


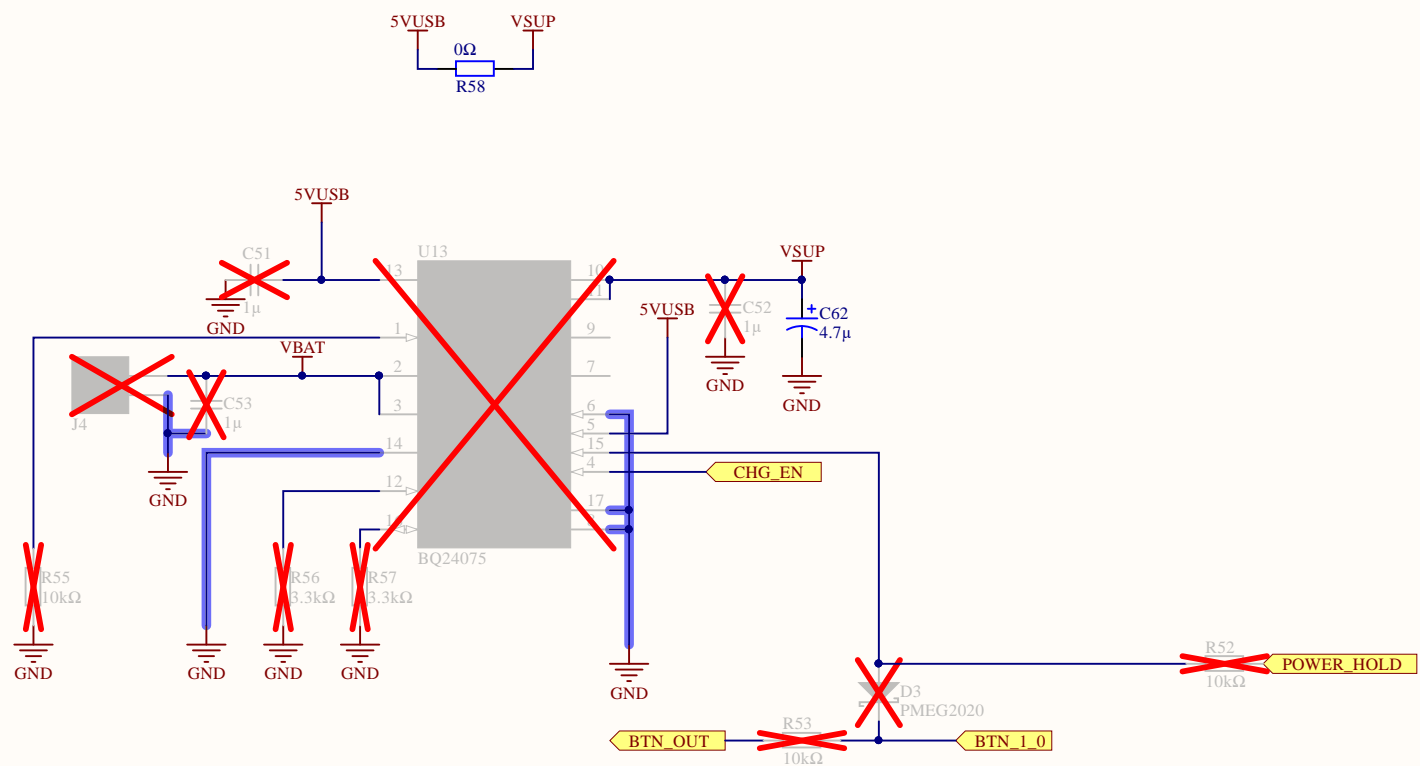
REV	Description	DATE
A	Initial version	20.8.2015
B	Added Wifi module, SD card Rotary encoder, LCD, Circuit for battery operation/charging Betteer power suply grouding	23.3.2016
B1	USB connector pin 5 is now connected to GND, pin 4 is NC Rotated Micro SD Socket Connected I/O connector to I2C, Fiducials fixed (now they are on both sides)	23.10.2016
B2	Component optimization for production	6.9.2017

TESTPOINT	SIGANL NAME
TP1	ANALOG IN CH2
TP2	ANALOG IN CH4
TP3	ANALOG OUT CH0
TP4	ANALOG IN CH1
TP5	ANALOG IN CH3
TP6	ANALOG OUT CH1
TP7	REFOUT / 2
TP8	REFOUT
TP9	+12V
TP10	VSUP
TP11	3V3
TP12	-12V
TP13	3V3W
TP14	GND

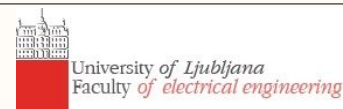
SHEET NAME	SHEET #
Cover Sheet	1
Block Schematics	2
Battaery manegment	3
Power suply	4
Processor + JTAG	5
Level shifters + Amplifiers	6
WIFI, Dispaly, Encoder	7
Digital I/O + ESD	8

Title Cover Sheet  University of Ljubljana Faculty of electrical engineering	Date 23.10.2016
	Project USB DAQ
	Rev B1
	Sheet 1 of 8 Engineer Matjaz Tome





Title Battery managment	Date 11.11.2015
	Project USB DAQ
	Rev B1
	Sheet 3 of 8 Engineer Matjaz Tome



A

B

C

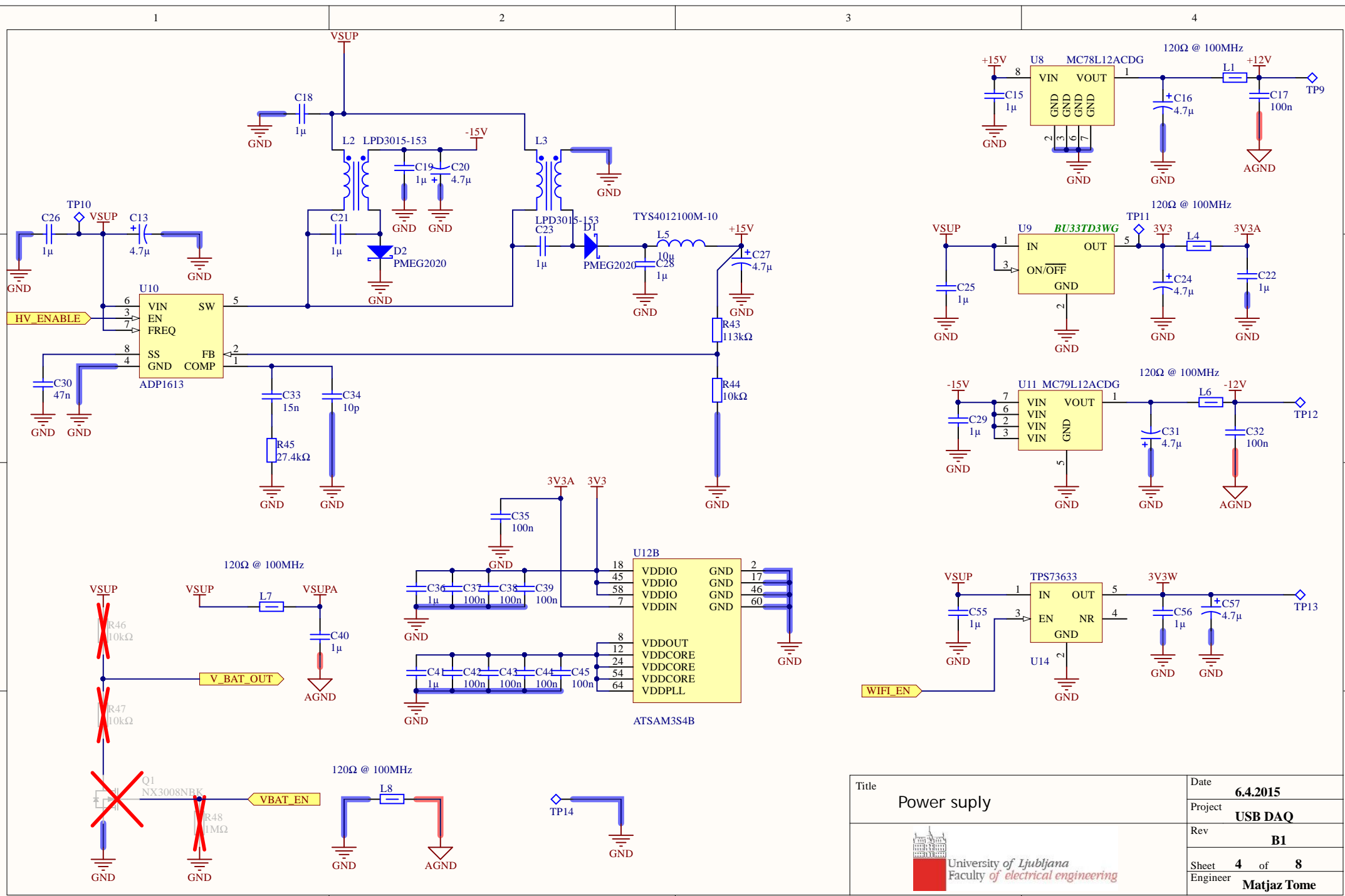
D


A

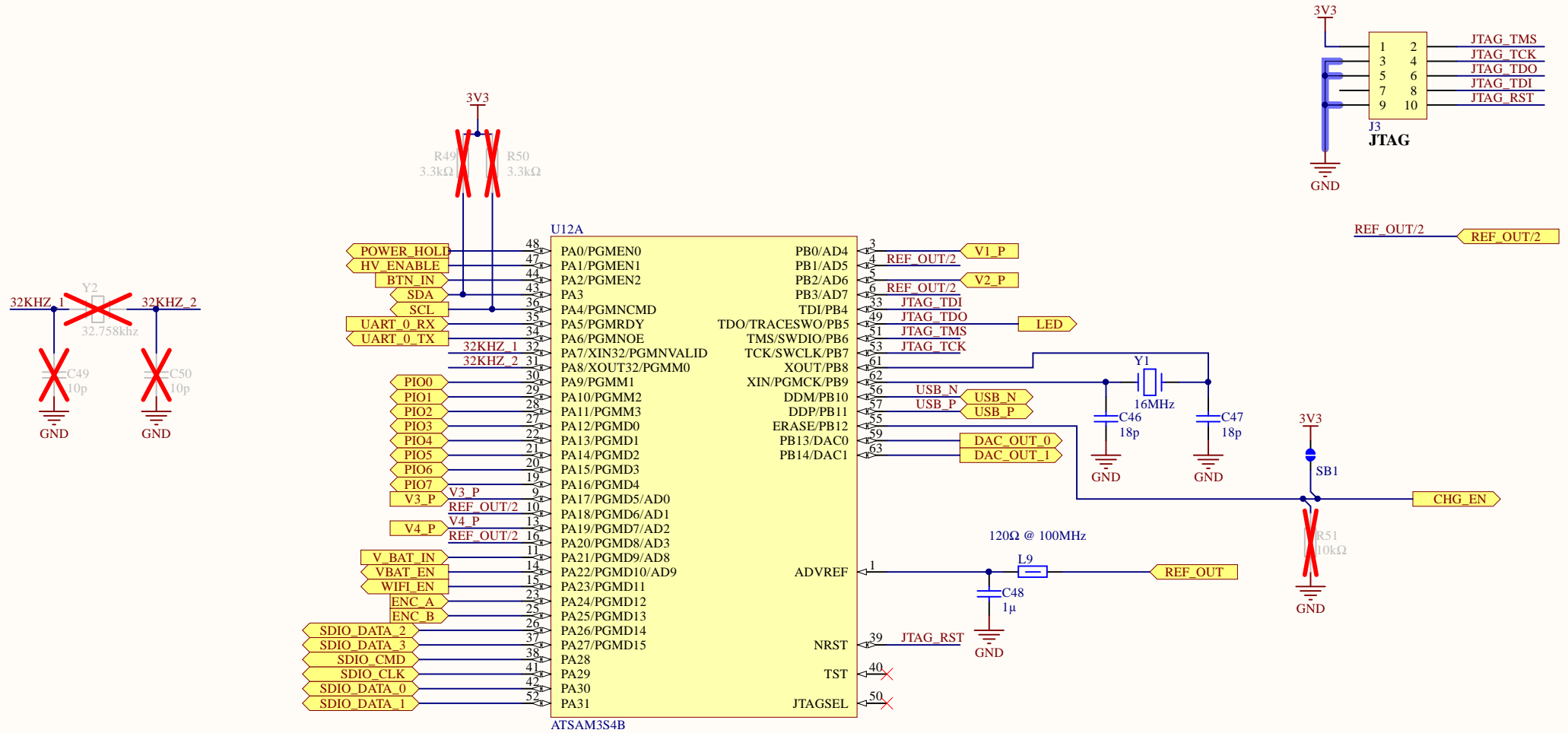
B

C

D



<div><div>Title</div><div>Power supply</div><div><div>University of Ljubljana Faculty of electrical engineering</div></div></div>	Date	6.4.2015
	Project	USB DAQ
	Rev	B1
	Sheet	4 of 8
Engineer		Matjaz Tome



Title Processor + JTAG	Date 23.10.2016
	Project USB DAQ
	Rev B1
	Sheet 5 of 8 Engineer Matjaz Tome

1

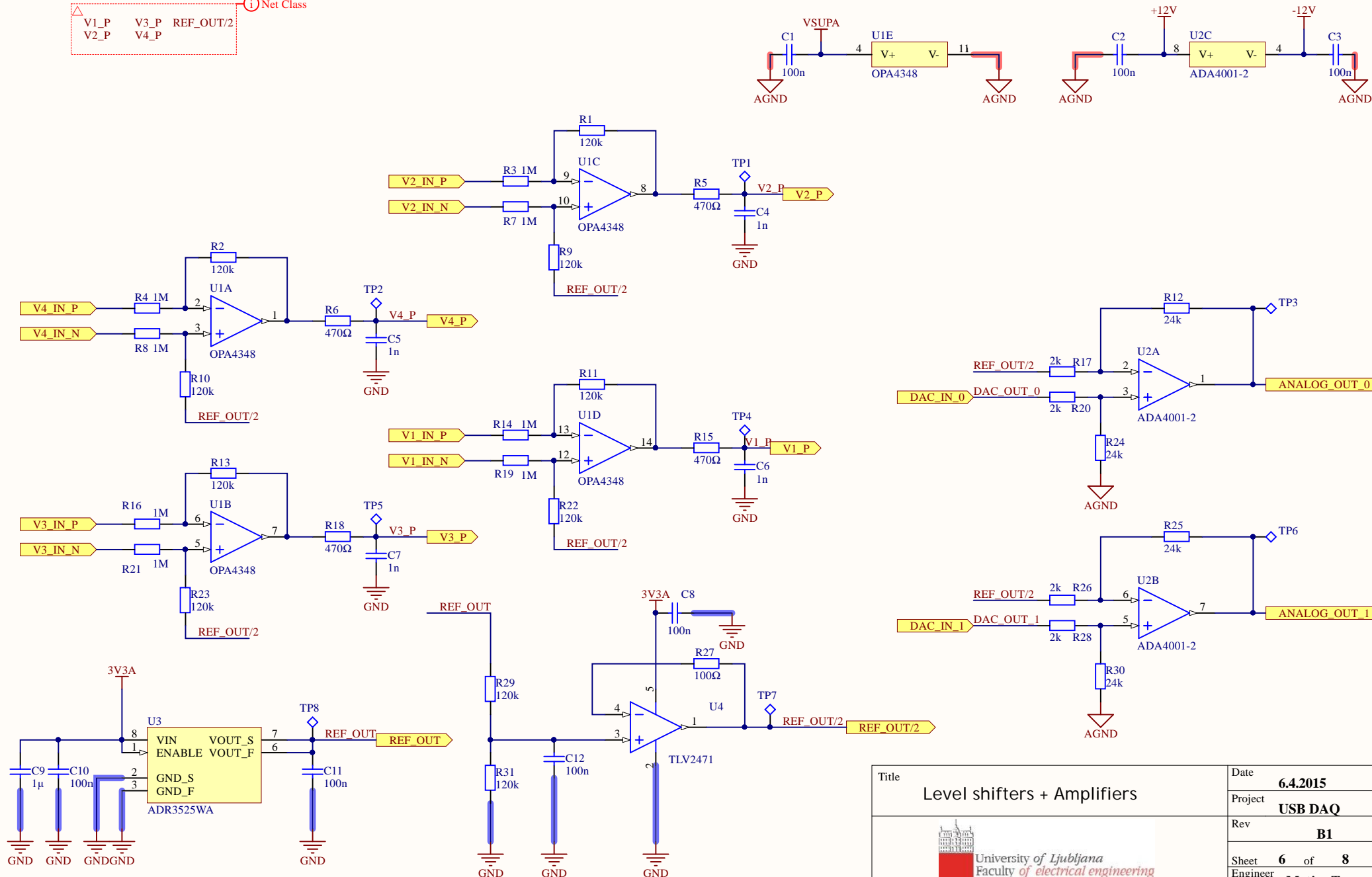
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3

4

Net Class

V1_P V3_P REF_OUT/2
V2_P V4_P



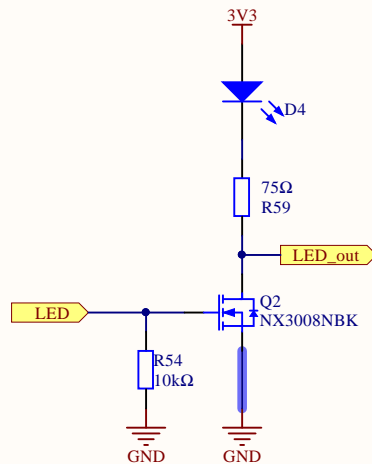
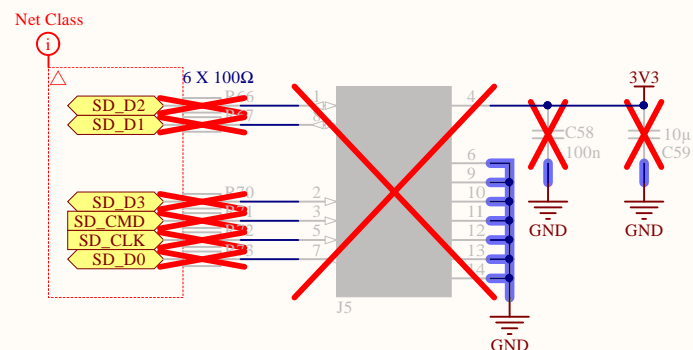
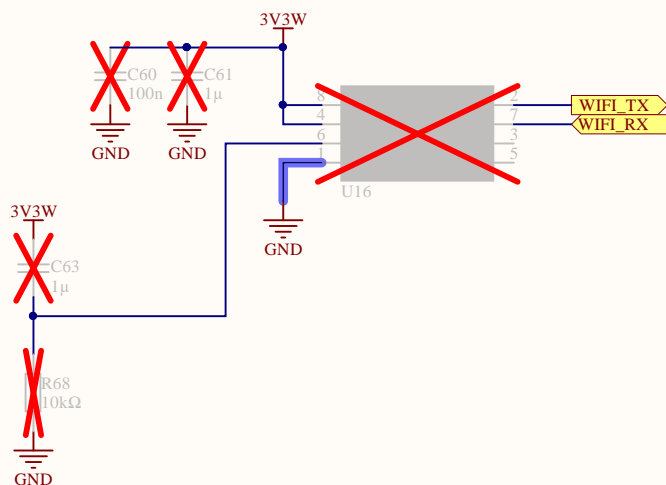
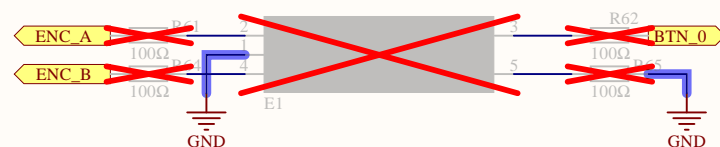
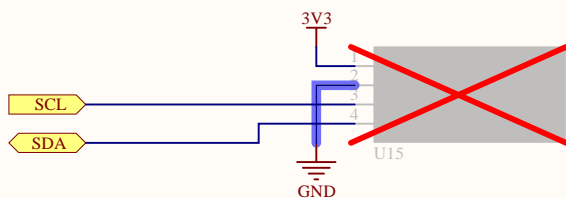
Title Level shifters + Amplifiers	Date 6.4.2015
	Project USB DAQ
	Rev B1
	Sheet 6 of 8 Engineer Matjaz Tome

1

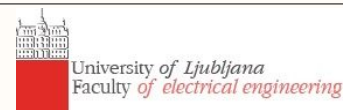
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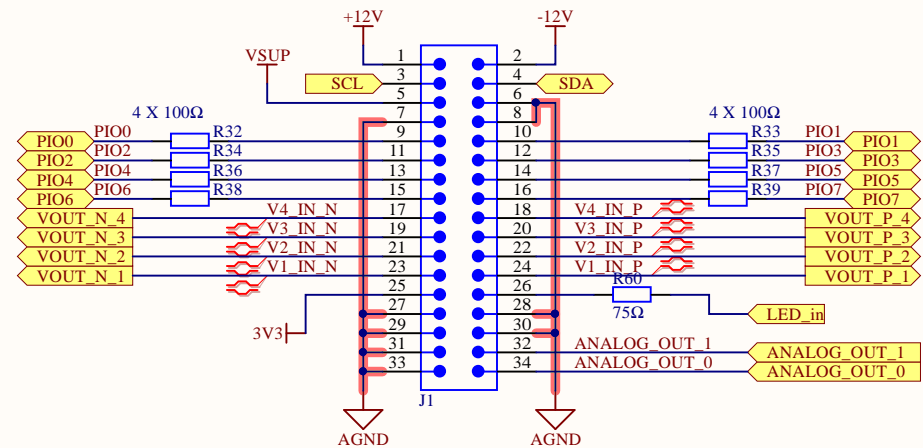
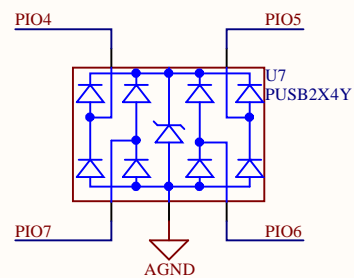
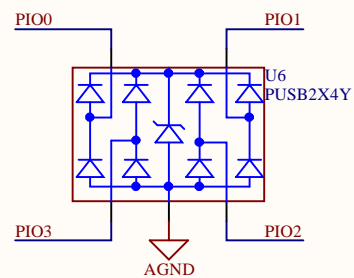
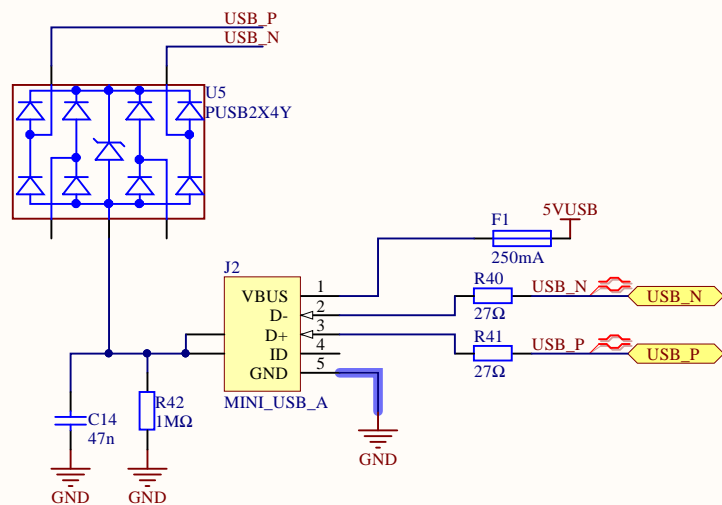
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4



Title WIFI, Display, Encoder	Date 18.12.2015
	Project USB DAQ
	Rev B1
	Sheet 7 of 8 Engineer Matjaz Tome





Boards