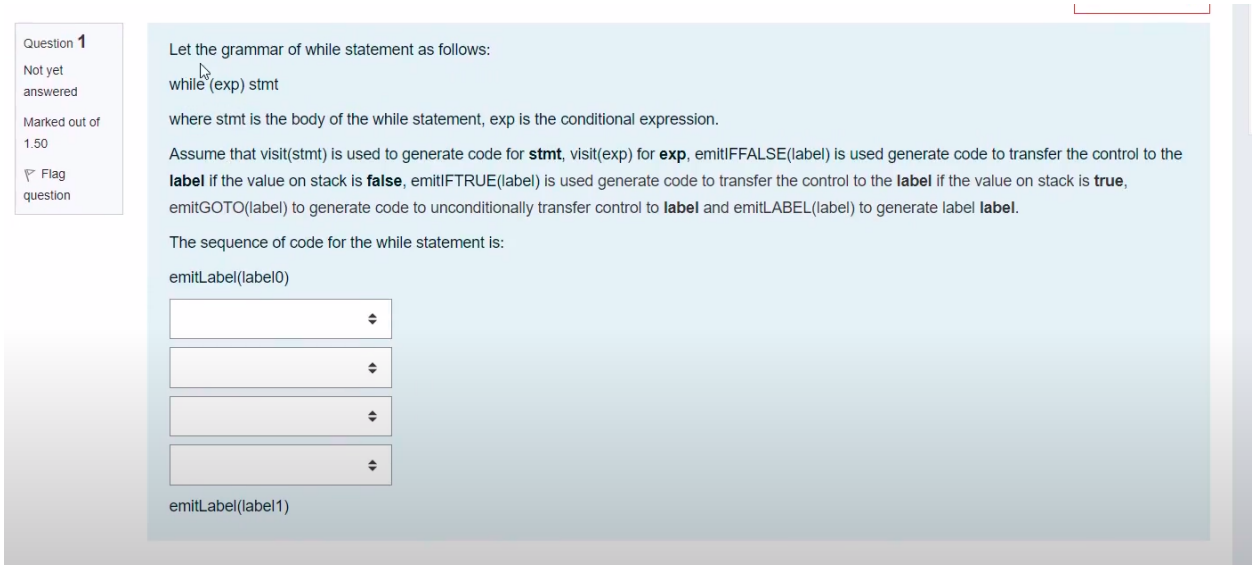


visit(exp)
emitIFFALSE(label1)
visit(stmt)
emitGOTO(label0)

Question **2**

Not yet answered

Marked out of 1.50

Flag question

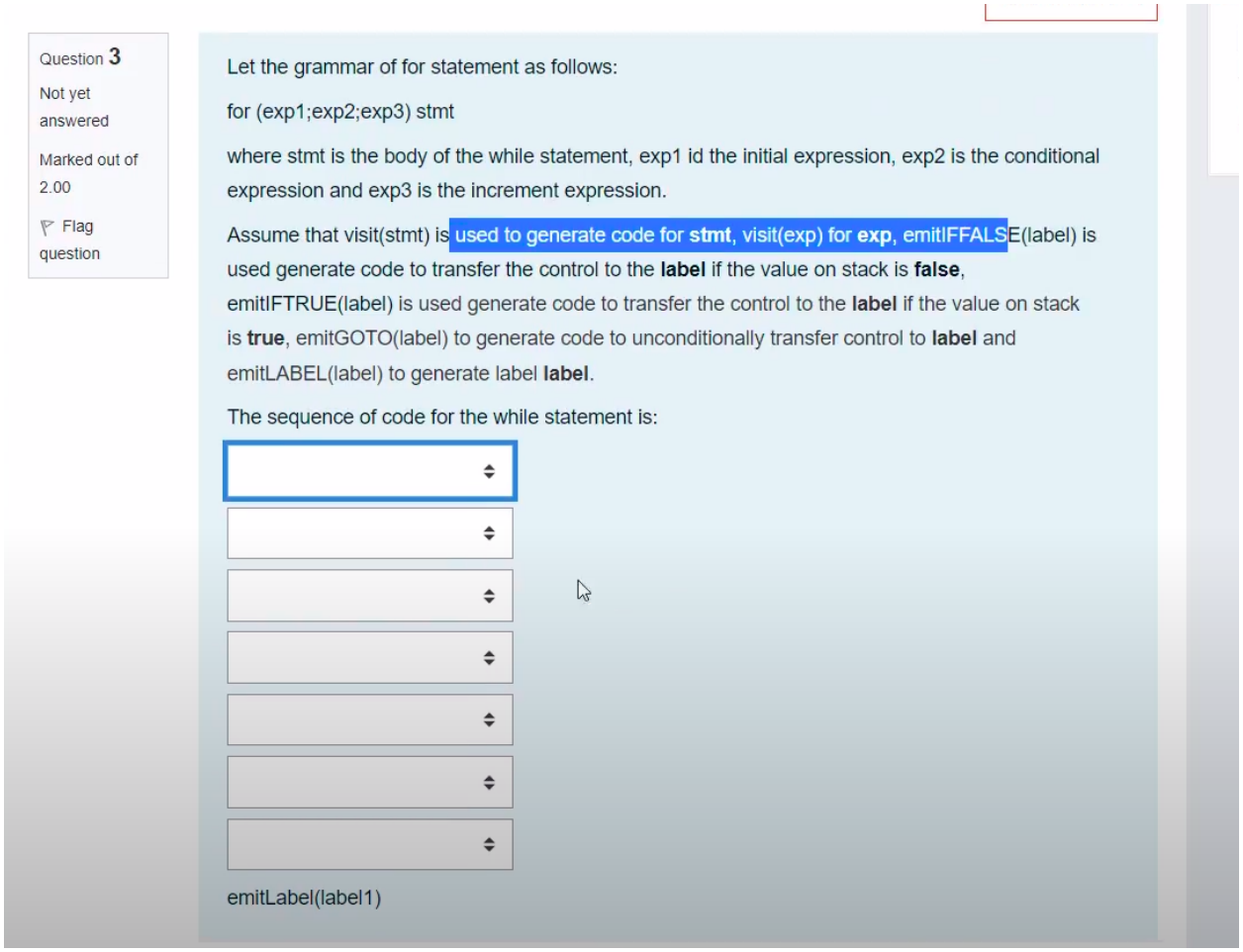
Let op be a binary operator like + or * and the binary expression is written as follows:

exp1 op exp2 where exp1, exp2 are sub expressions and op is a binary operator.

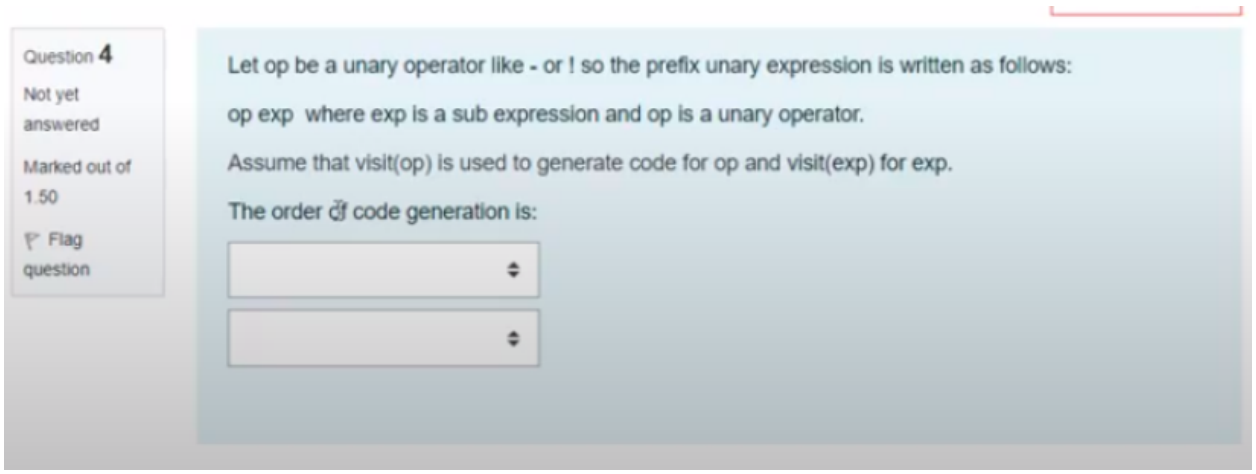
Assume that visit(op) is used to generate code for op and visit(exp1) for exp1 and visit(exp2) for exp2.

The order of code generation is:

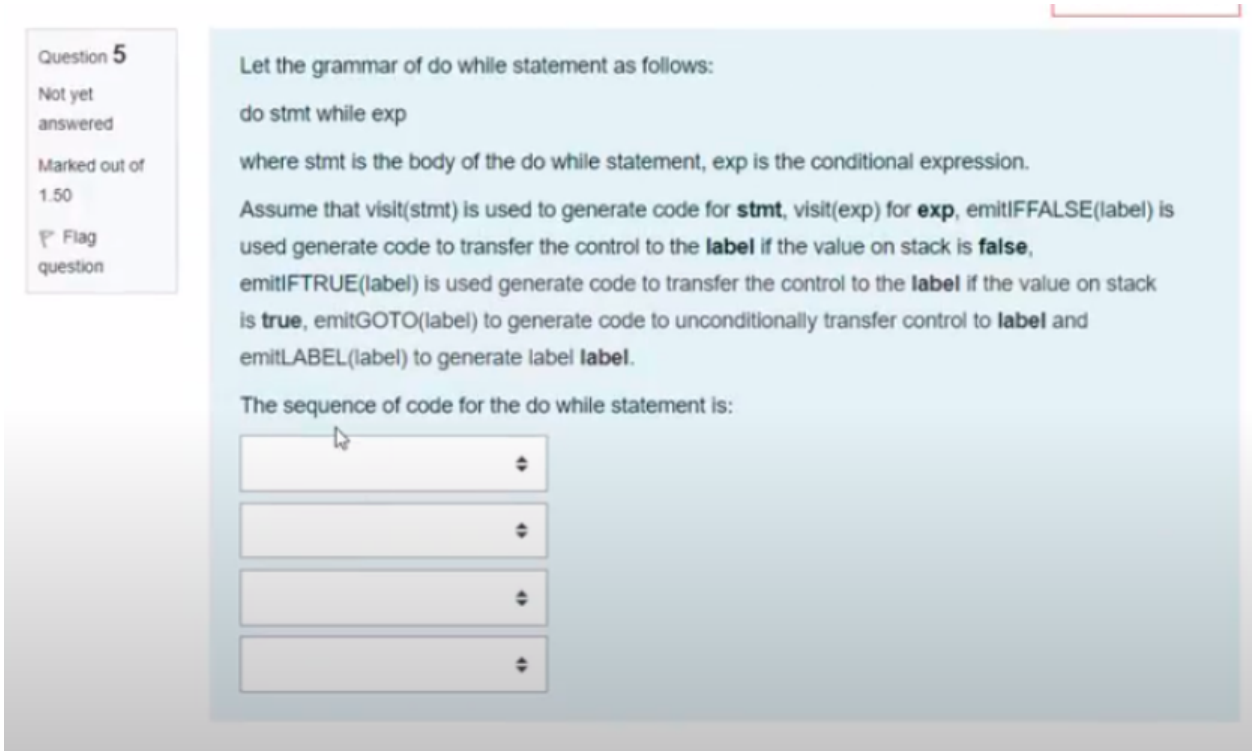
visit(exp1)
visit(exp2)
visit(op)



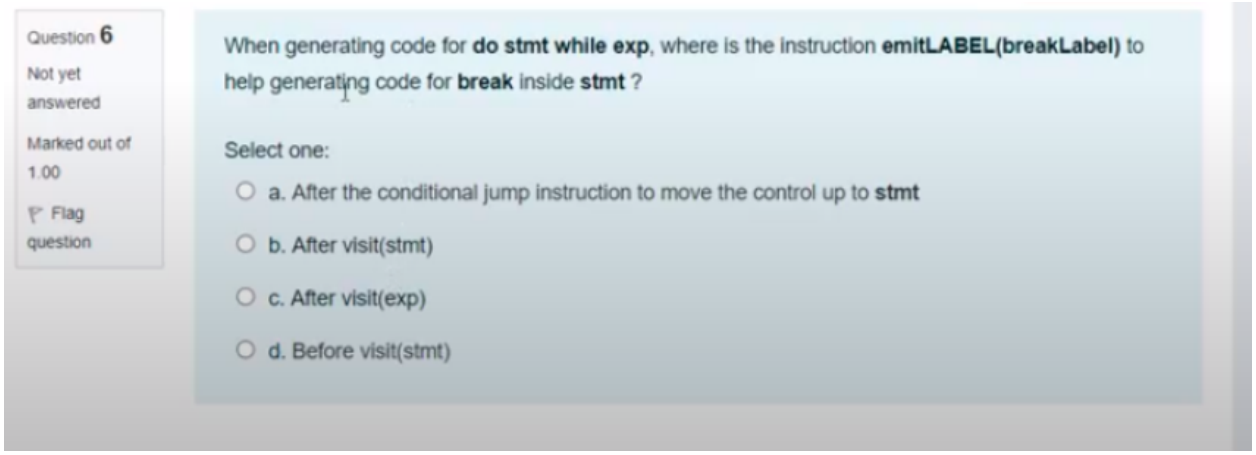
visit(exp1)
emitLabel(label0)
visit(exp2)
emitIFFALSE(label1)
visit(stmt)
visit(exp3)
emitGOTO(label0)



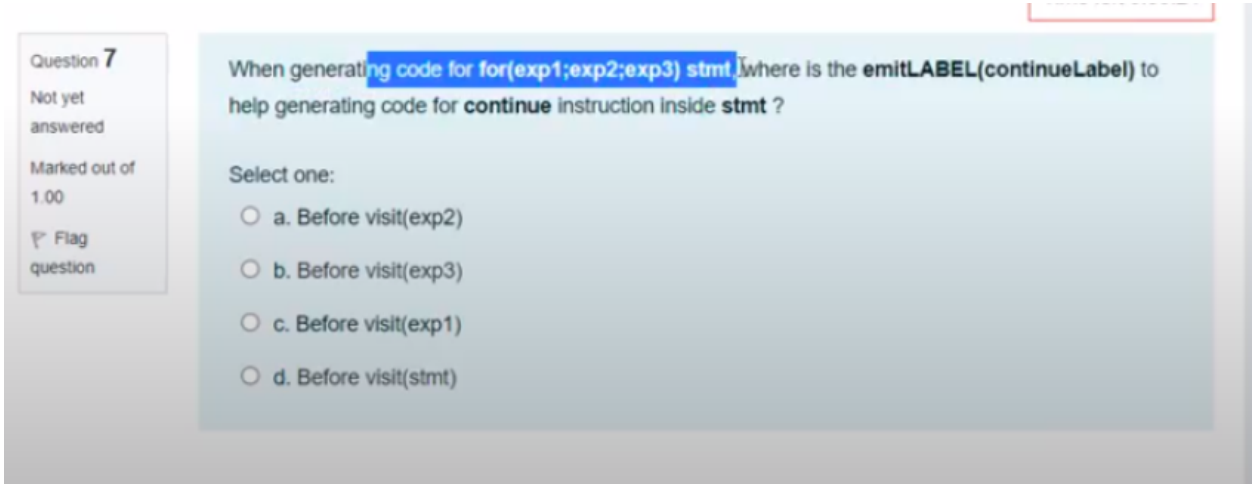
visit(exp)
visit(op)



emitLABEL(label0)
visit(stmt)
visit(exp)
emitIFTRUE(label0)


Question 6
Not yet answered
Marked out of 1.00
Flag question
When generating code for do stmt while exp, where is the instruction emitLABEL(breakLabel) to help generating code for break inside stmt ?
Select one:
a. After the conditional jump instruction to move the control up to stmt
b. After visit(stmt)
c. After visit(exp)
d. Before visit(stmt)

A


Question 7
Not yet answered
Marked out of 1.00
Flag question
When generating code for for(exp1;exp2;exp3) stmt, where is the emitLABEL(continueLabel) to help generating code for continue instruction inside stmt ?
Select one:
a. Before visit(exp2)
b. Before visit(exp3)
c. Before visit(exp1)
d. Before visit(stmt)

B