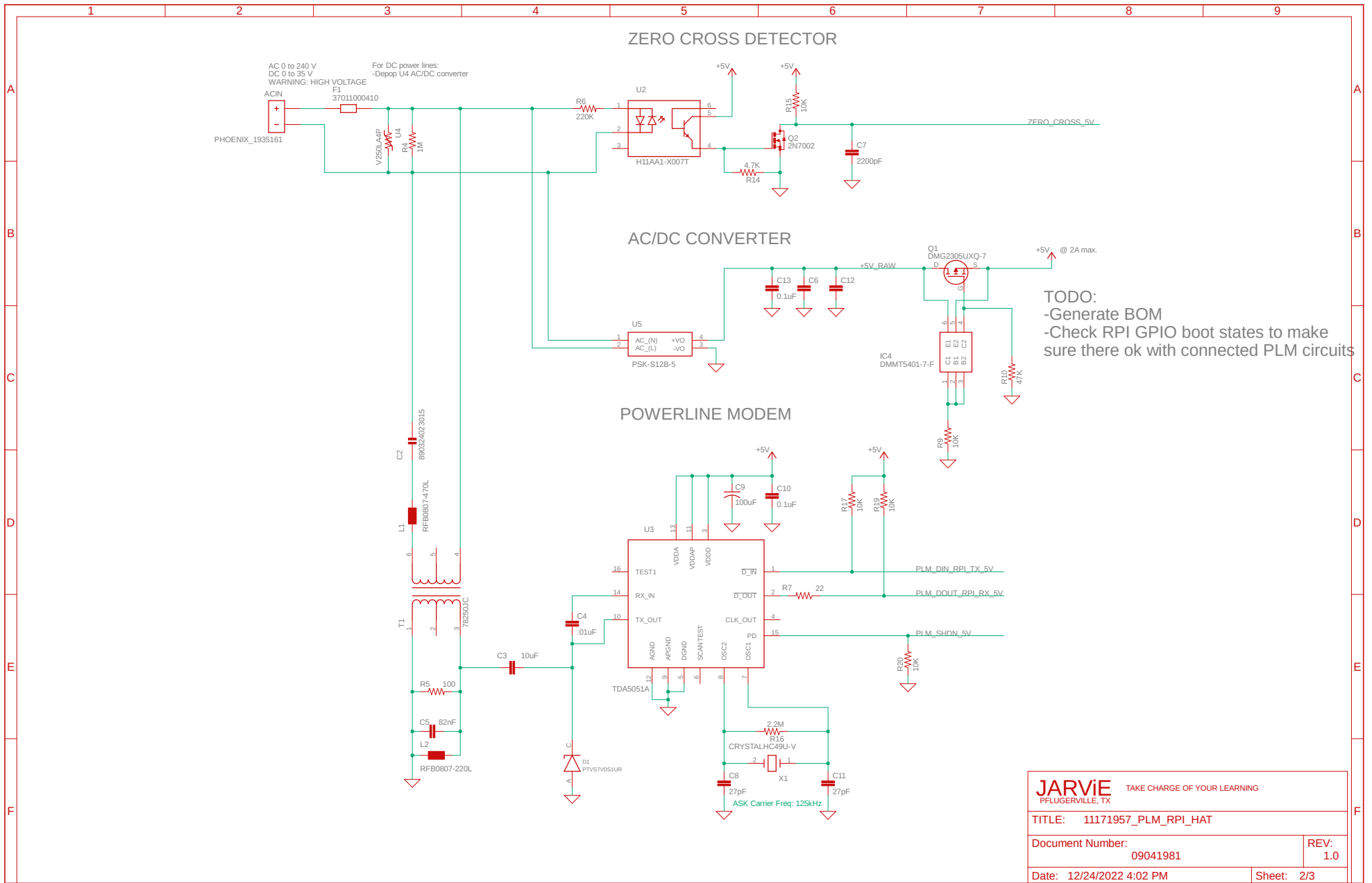
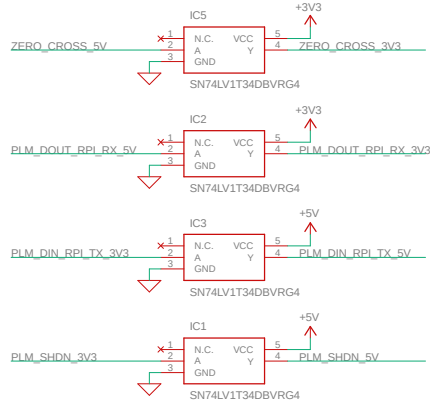


## BLOCK DIAGRAM

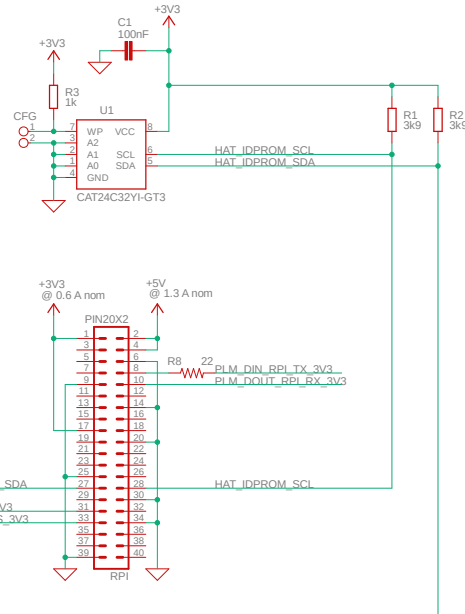
<div>JARViE PFLUGERVILLE, TX</div>		TAKE CHARGE OF YOUR LEARNING	
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## RPI to PLM DIO LEVEL TRANSLATION



## RPI HDR & ID EEPROM



NOTE:  
-HAT spec recommends that 5V input can source 2 A cont. to PI. Reverse current diode is recommended.  
-3V3 is an output from teh RPI. Not sure how much current it can source.  
Rail should only be used to power ID EEPROM

## RASPBERRY PI 40-PIN GPIO HEADER PINOUT

PIN #	RPI SIGNAL	HAT SIGNAL
1	3V3 Power	3V3 Power
2	5V Power	5V Power
3	GPIO2 (SDA)	HAT_IDPROM_SDA
4	5V Power	5V Power
5	GPIO3 (SCL)	HAT_IDPROM_SCL
6	GND	GND
7	GPIO4 (GPCLK0)	GPIO4 (GPCLK0)
8	GPIO14 (TXD)	PLM_DIN_RPI_TX_3V3
9	GND	GND
10	GPIO15 (RXD)	PLM_DOUT_RPI_RX_3V3
11	GPIO17	GPIO17
12	GPIO18 (PCM_CLK)	GPIO18 (PCM_CLK)
13	GPIO27	GPIO27
14	GND	GND
15	GPIO22	GPIO22
16	GPIO23	GPIO23
17	3V3 Power	3V3 Power
18	GPIO24	GPIO24
19	GPIO10 (MOSI)	GPIO10 (MOSI)
20	GND	GND
21	GPIO9 (MISO)	GPIO9 (MISO)
22	GPIO25	GPIO25
23	GPIO11 (SCK)	GPIO11 (SCK)
24	GPIO8 (CE0)	GPIO8 (CE0)
25	GND	GND
26	GPIO7 (CE1)	GPIO7 (CE1)
27	GPIO0 (ID_SD)	GPIO0 (ID_SD)
28	GPIO1 (ID_SC)	GPIO1 (ID_SC)
29	GPIO5	GPIO5
30	GND	GND
31	GPIO6	GPIO6
32	GPIO12 (PWM0)	GPIO12 (PWM0)
33	GPIO13 (PWM1)	GPIO13 (PWM1)
34	GND	GND
35	GPIO19 (PCM_ES)	GPIO19 (PCM_ES)
36	GPIO16	GPIO16
37	GPIO26	GPIO26
38	GPIO20 (PCM_DIN)	GPIO20 (PCM_DIN)
39	GND	GND
40	GPIO21 (PCM_DOUT)	GPIO21 (PCM_DOUT)

## HAT R-PI INTERFACE & EEPROM

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