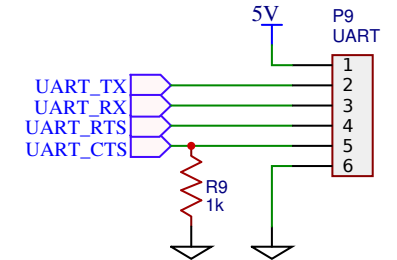
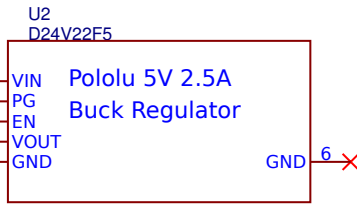


100K pull-downs on RSSI
signal needed to keep voltage below
Teensy internal 1.2V reference
2.2K resistor and 0.1u cap form a
low-pass RC filter for the RSSI signal
with cutoff around 724 Hz.

Wire to
XT60 Connector, and
power from 4S-6S LiPo

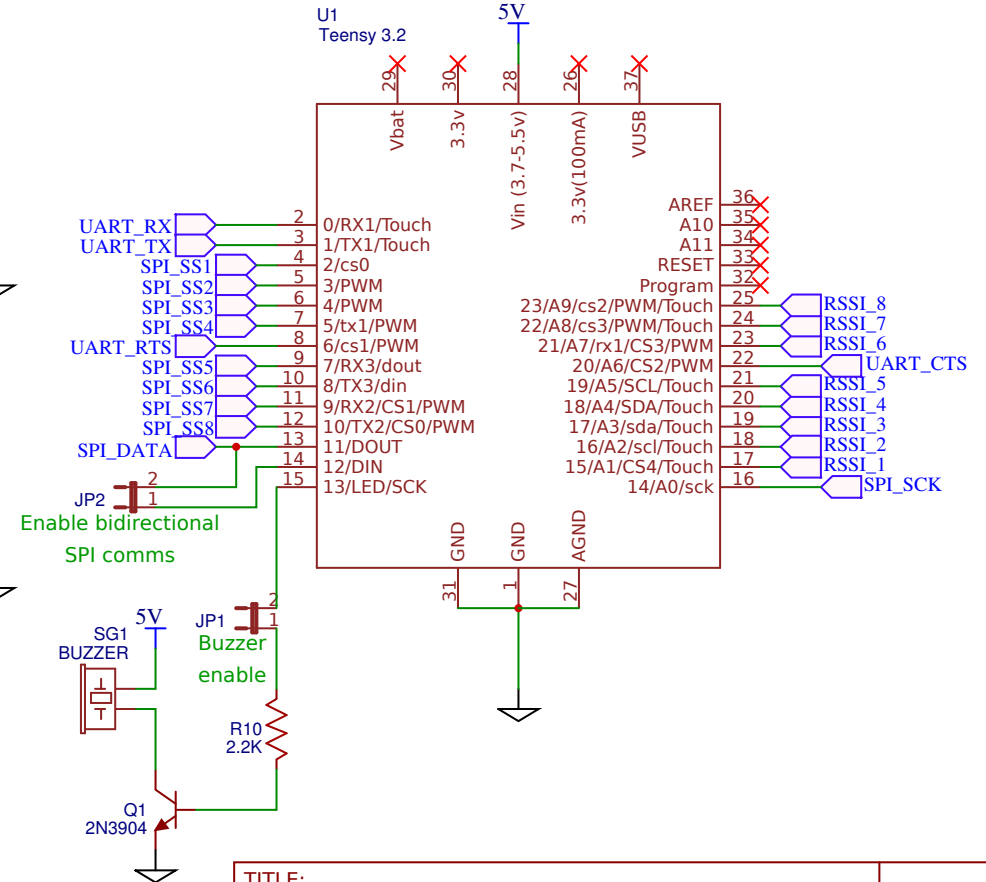
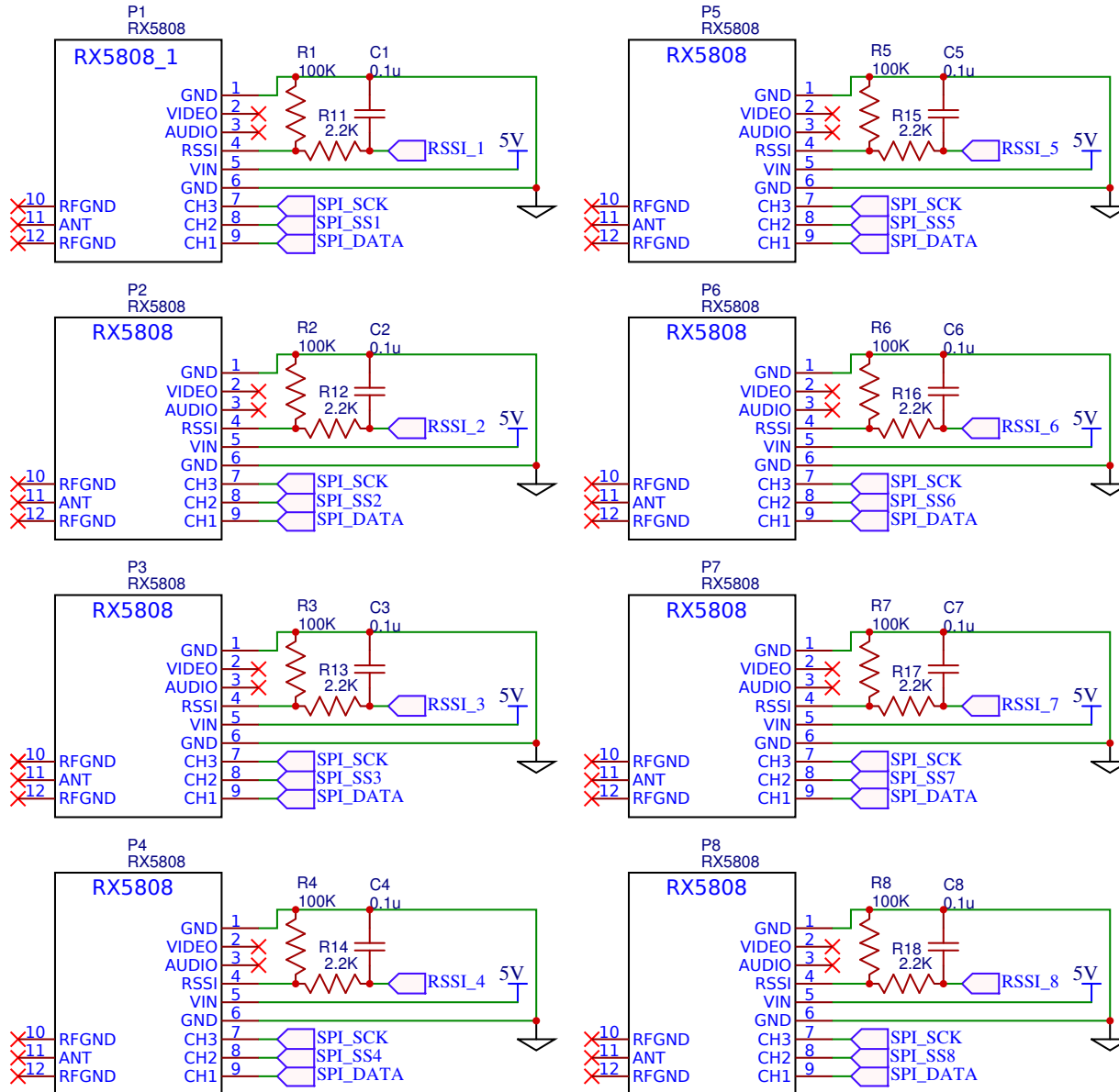


UART comms interface, up to 115200 baud,
suitable for one of the following:

- 3DR V2 Telemetry Radio
- HC-06 Bluetooth Serial
- FT232 USB Serial

Note: UART signals are 3.3V

Note: The VUSB trace must be cut on
the back of the Teensy board
before powering the Teensy from USB.



TITLE: LapRSSI		REV: 1.3
Date: 2017-08-31	Sheet: 1/1	
EasyEDA V4.7.6	Drawn By: Steve Lilly	