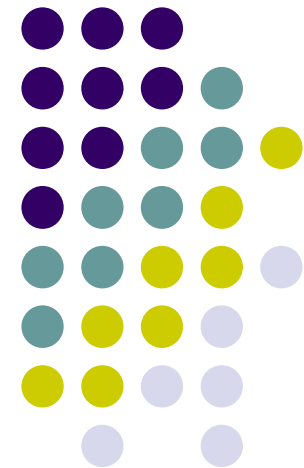


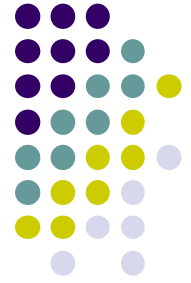
Computer Architecture

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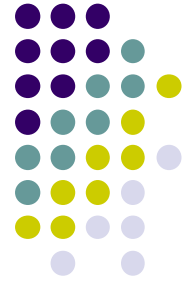


Introduction



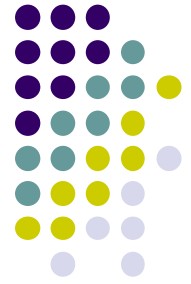
Course objectives

- Understand
 - what composes a computer
 - the main task of each component
 - how to make a CPU
 - how the CPU works
 - how to compile C program into machine language
 - how to optimize code
- Basic knowledge for embedded programming



The Computer Revolution

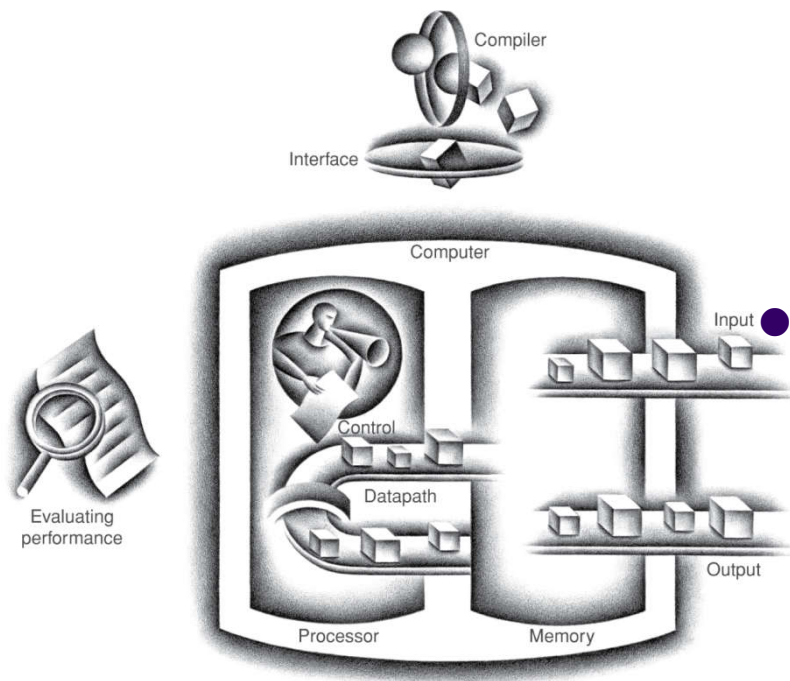
- Makes novel applications feasible
 - Computers in automobiles
 - Cell phones
 - World Wide Web
 - Search Engines
- Computers are pervasive



Classes of Computers

- Desktop computers
 - General purpose, variety of software
 - Subject to cost/performance tradeoff
- Server computers
 - Network based
 - High capacity, performance, reliability
 - Range from small servers to building sized
- Embedded computers
 - Hidden as components of systems
 - Stringent power/performance/cost constraints

Components of a Computer



- Same components for all kinds of computer
 - Desktop, server, embedded
- Input/output includes
 - User-interface devices
 - Display, keyboard, mouse
 - Storage devices
 - Hard disk, CD/DVD, flash
 - Network adapters
 - For communicating with other computers

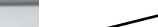


Anatomy of a Computer

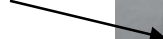
Output
device



Network
cable



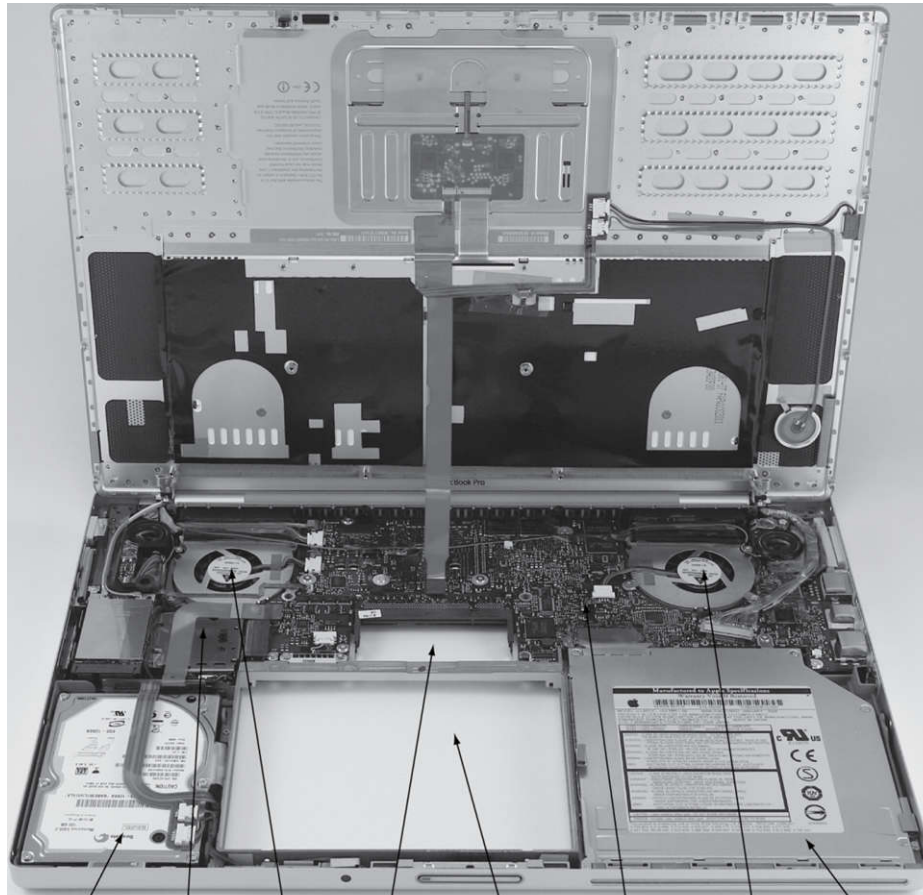
Input
device



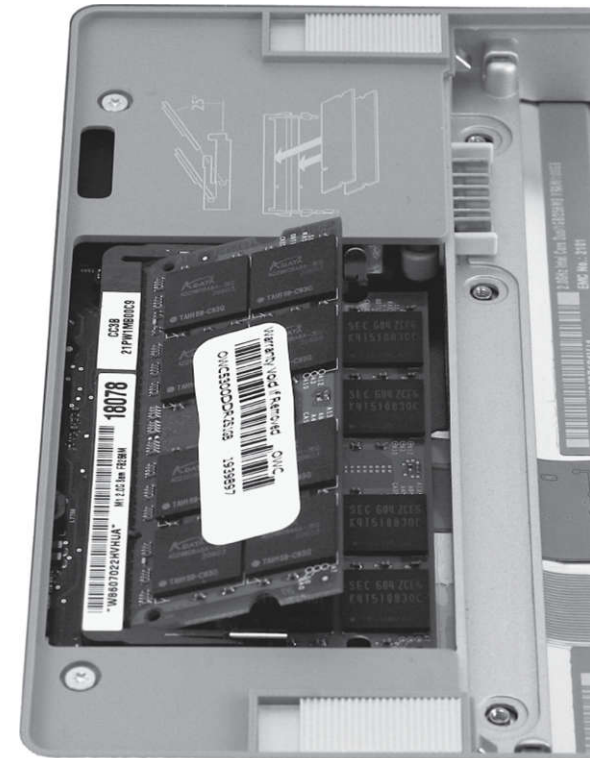
Input
device



Opening the Box



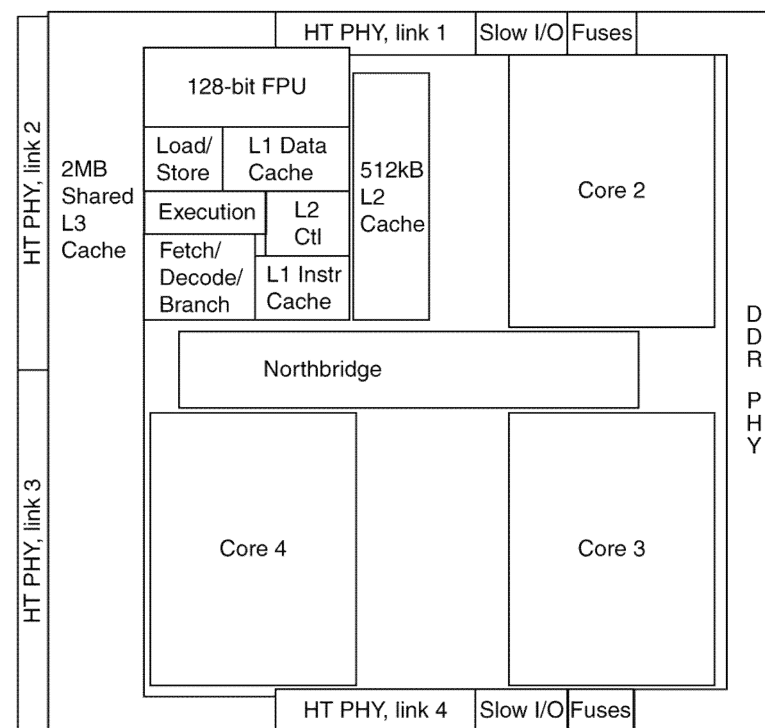
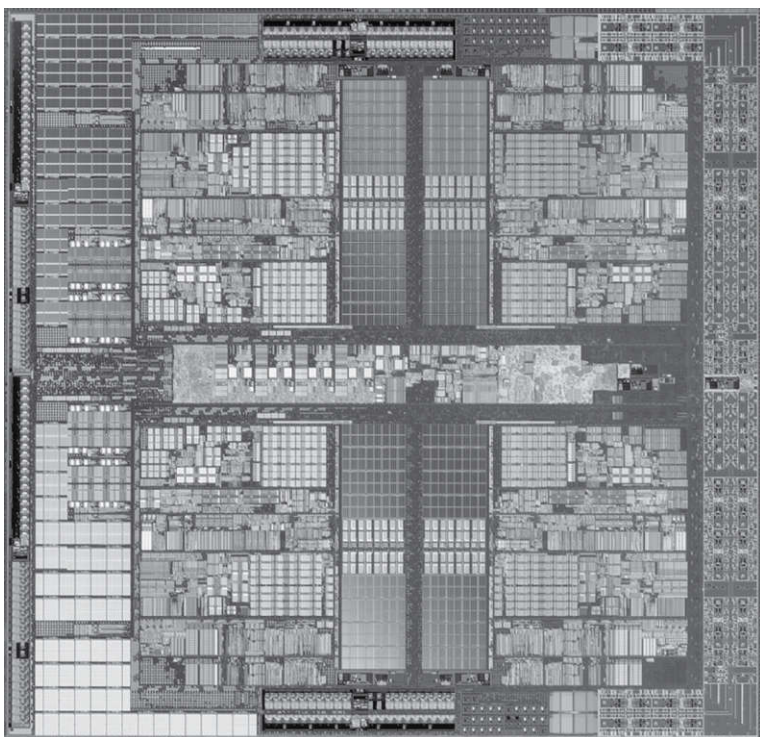
Hard drive Processor Fan with cover Spot for memory DIMMs Spot for battery Motherboard Fan with cover DVD drive





The Processor

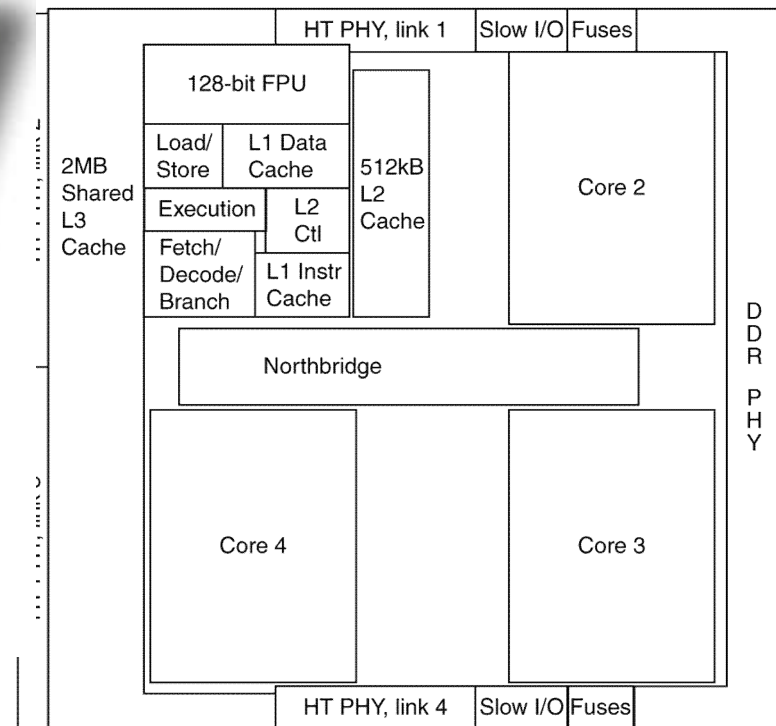
- AMD Barcelona: 4 processor cores



The Processor



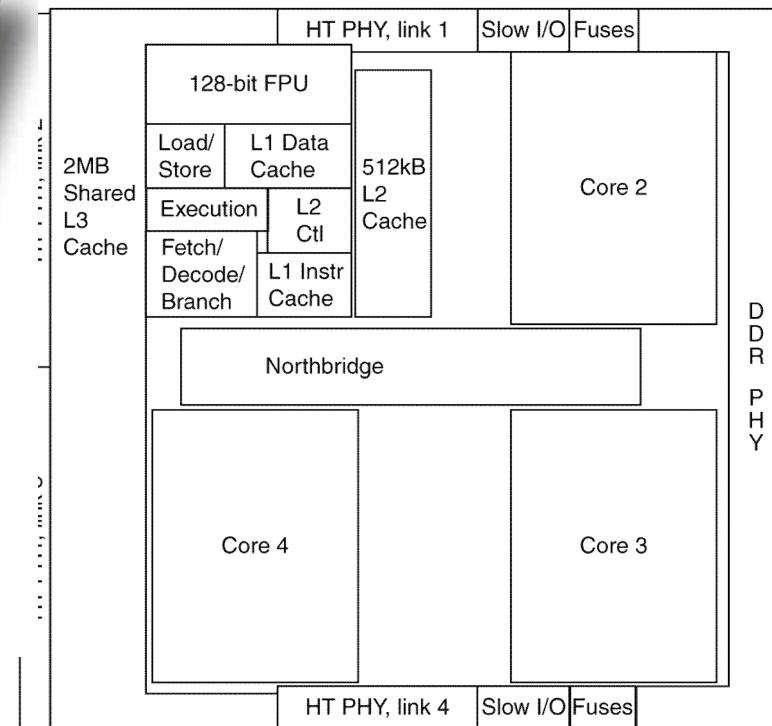
Processor cores

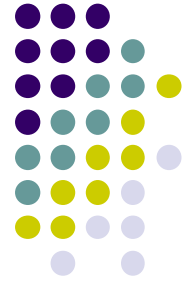


The Processor



Processor cores





The Processor

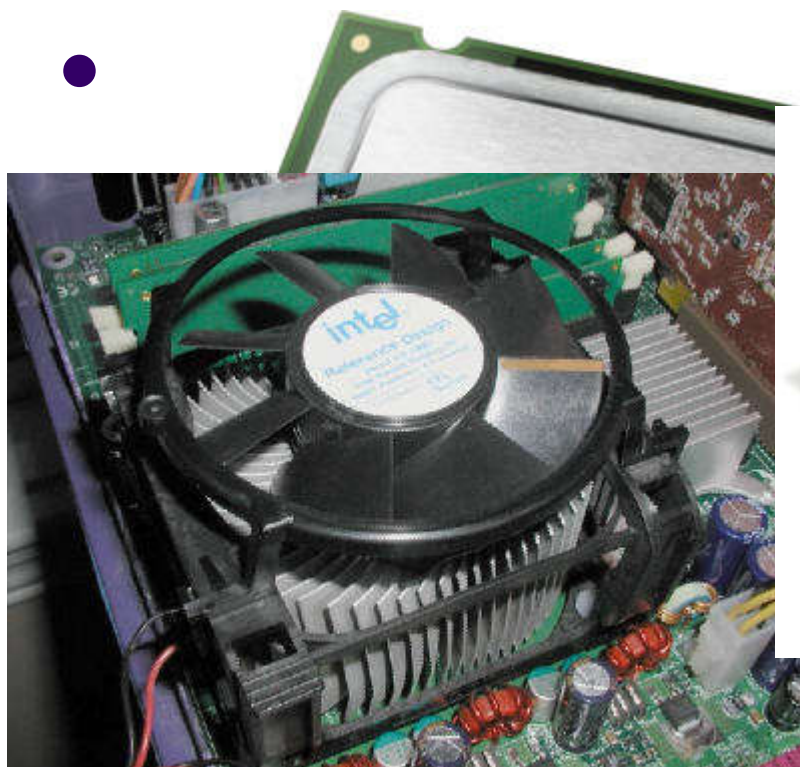


ssor cores

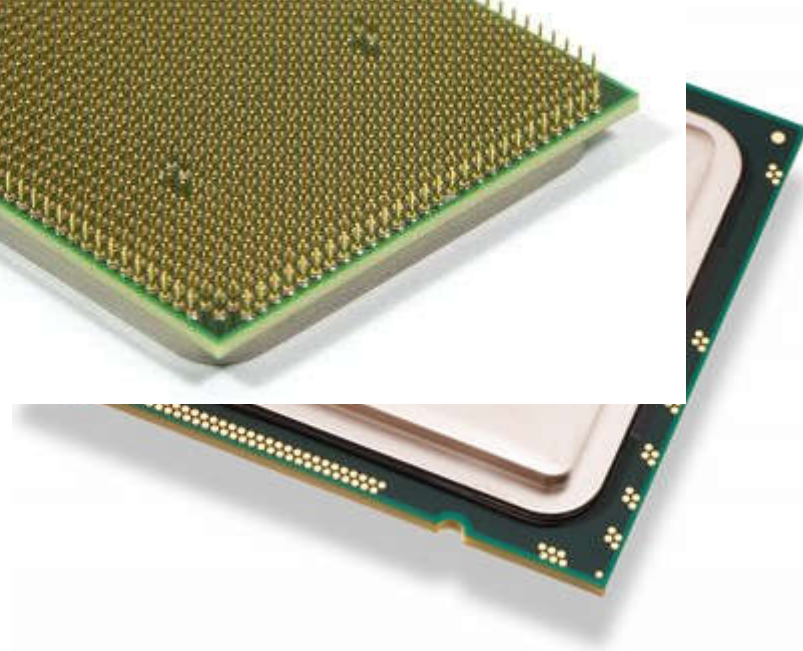
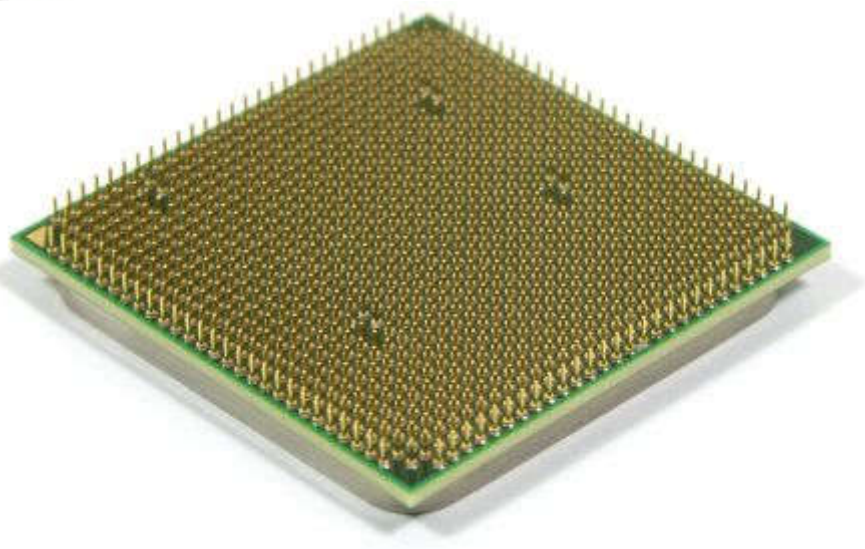




The Processor



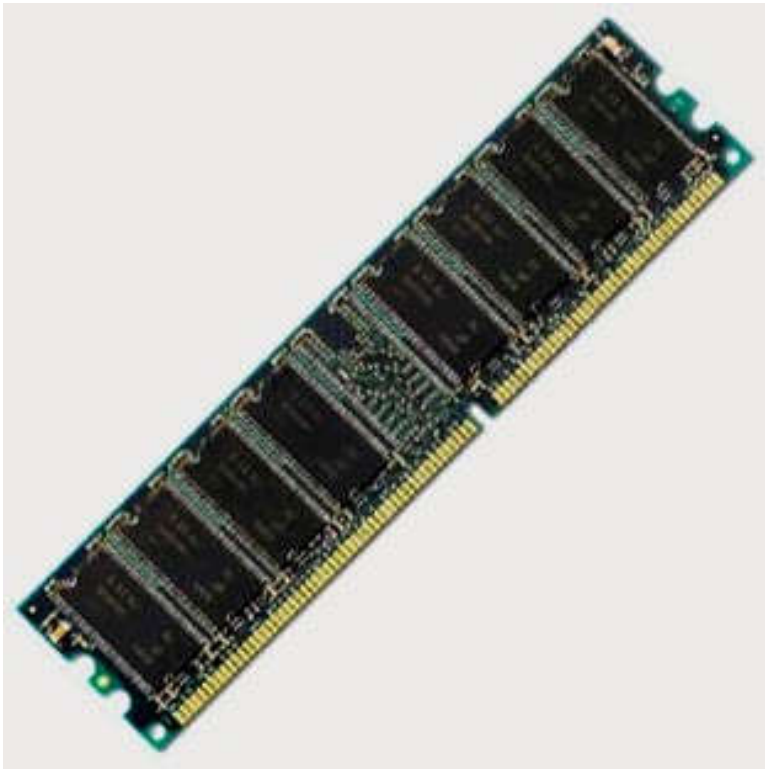
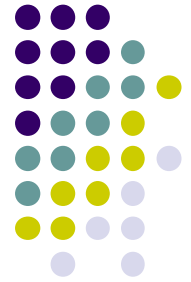
ssor cores



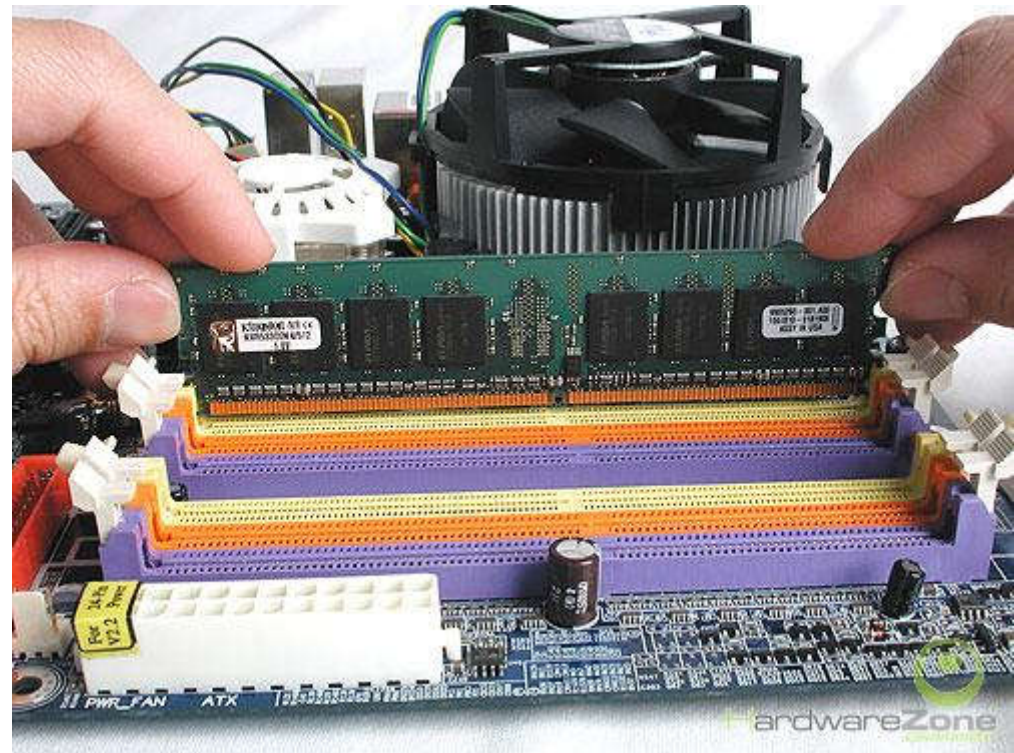
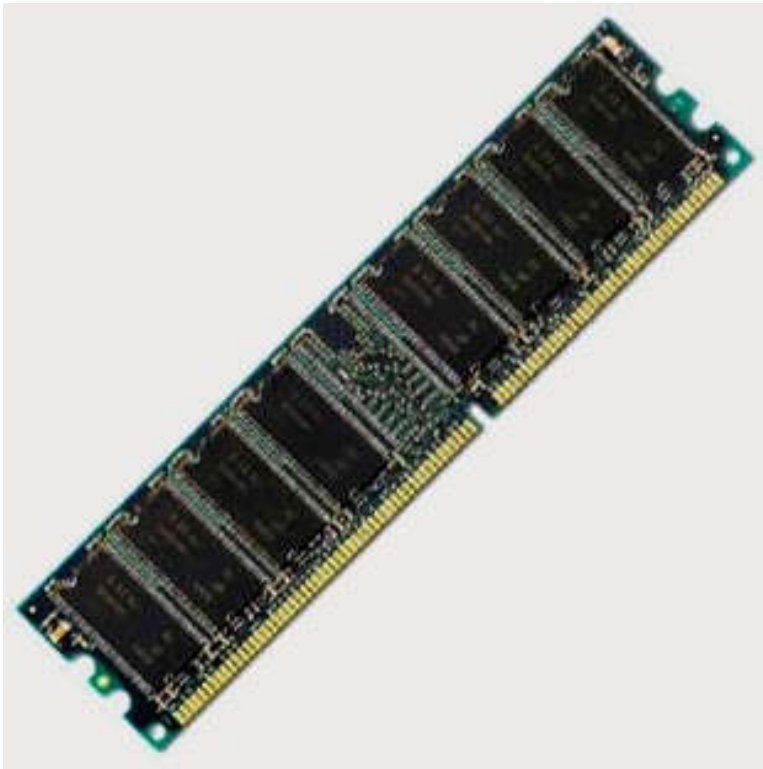
RAM (Random Access Memory)



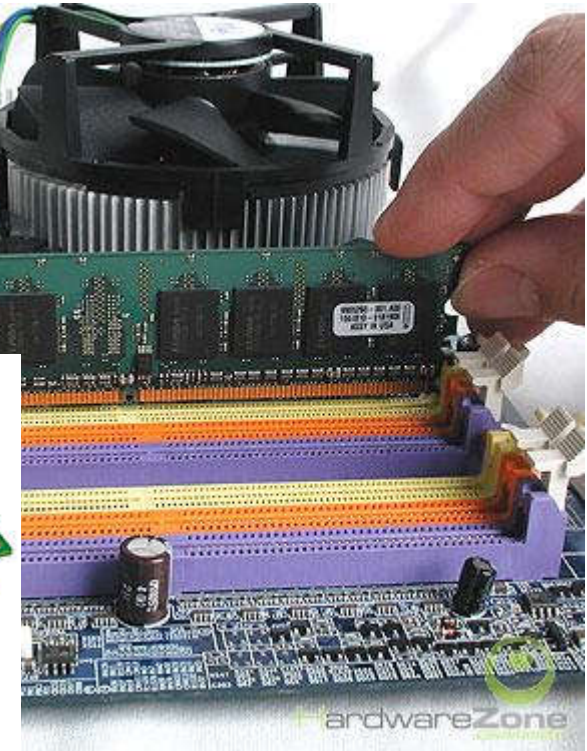
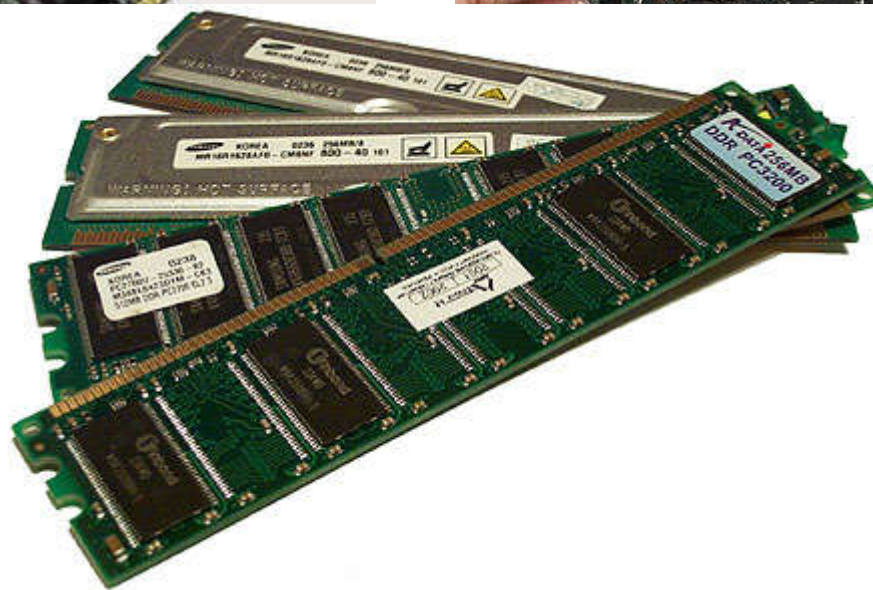
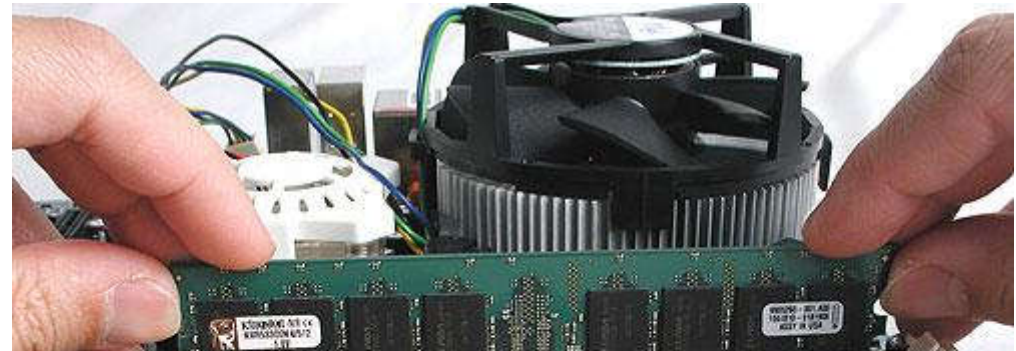
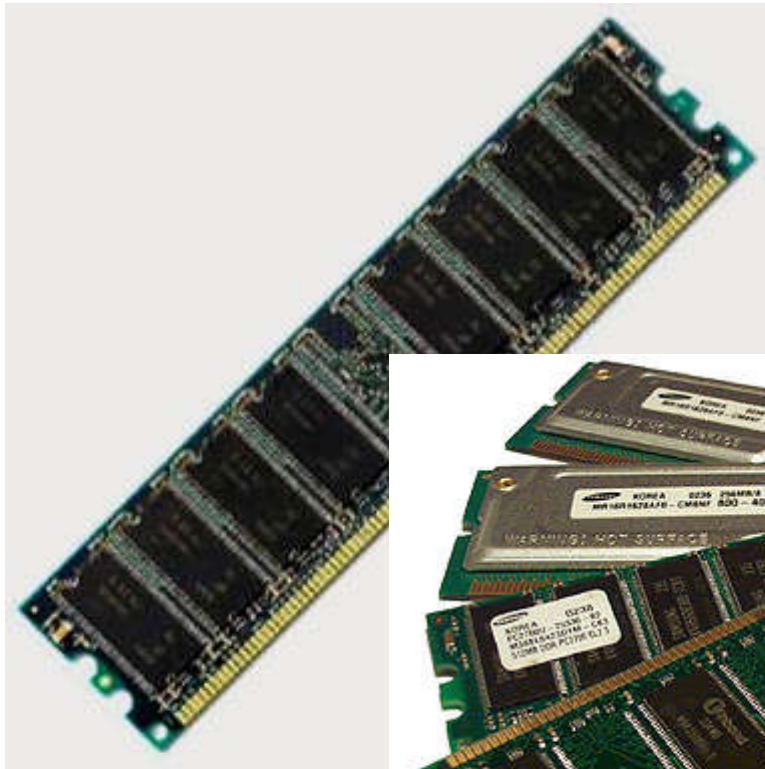
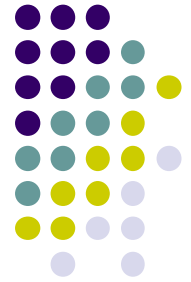
RAM (Random Access Memory)



RAM (Random Access Memory)

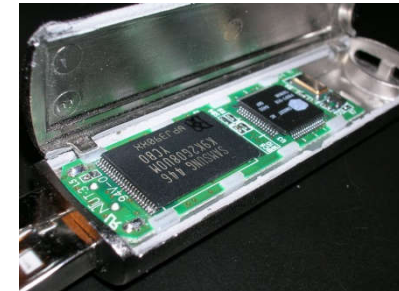


RAM (Random Access Memory)



A Safe Place for Data

- Volatile main memory
 - Loses instructions and data when power off
- Non-volatile secondary memory
 - Magnetic disk
 - Flash memory



Memory Hierarchy Levels



Capacity
Access Time
Cost

Staging
Xfer Unit

CPU Registers
100s Bytes
300 - 500 ps (0.3-0.5 ns)

L1 and L2 Cache
10s-100s K Bytes
~1 ns - ~10 ns
\$1000s/ GByte

Main Memory
G Bytes
80ns- 200ns
~ \$100/ GByte

Disk
10s T Bytes, 10 ms
(10,000,000 ns)
~ \$1 / GByte

Tape
infinite
sec-min
~\$1 / GByte

Registers

Instr. Operands

L1 Cache

Blocks

L2 Cache

Blocks

Memory

Pages

Disk

Files

Tape

prog./compiler
1-8 bytes

cache cntl
32-64 bytes

cache cntl
64-128 bytes

OS
4K-8K bytes

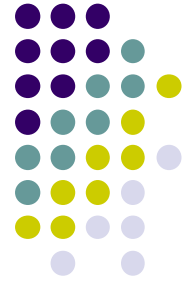
user/operator
Mbytes

Upper Level

faster

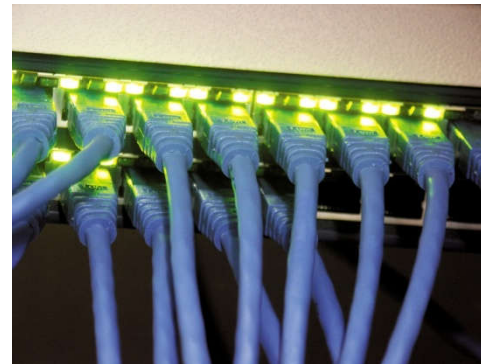
Larger

Lower Level



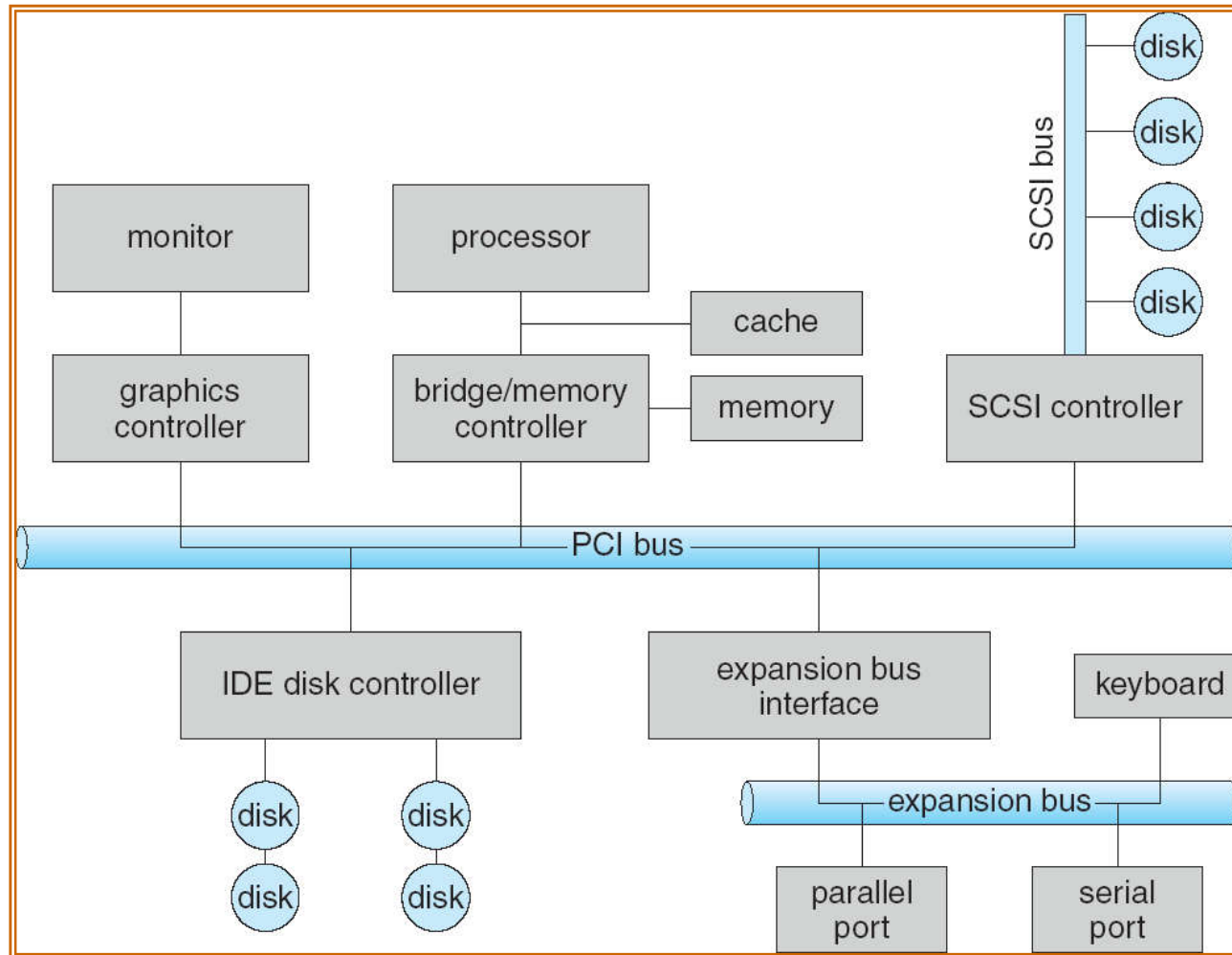
Networks

- Communication and resource sharing
- Local area network (LAN): Ethernet
 - Within a building
- Wide area network (WAN: the Internet
- Wireless network: WiFi, Bluetooth

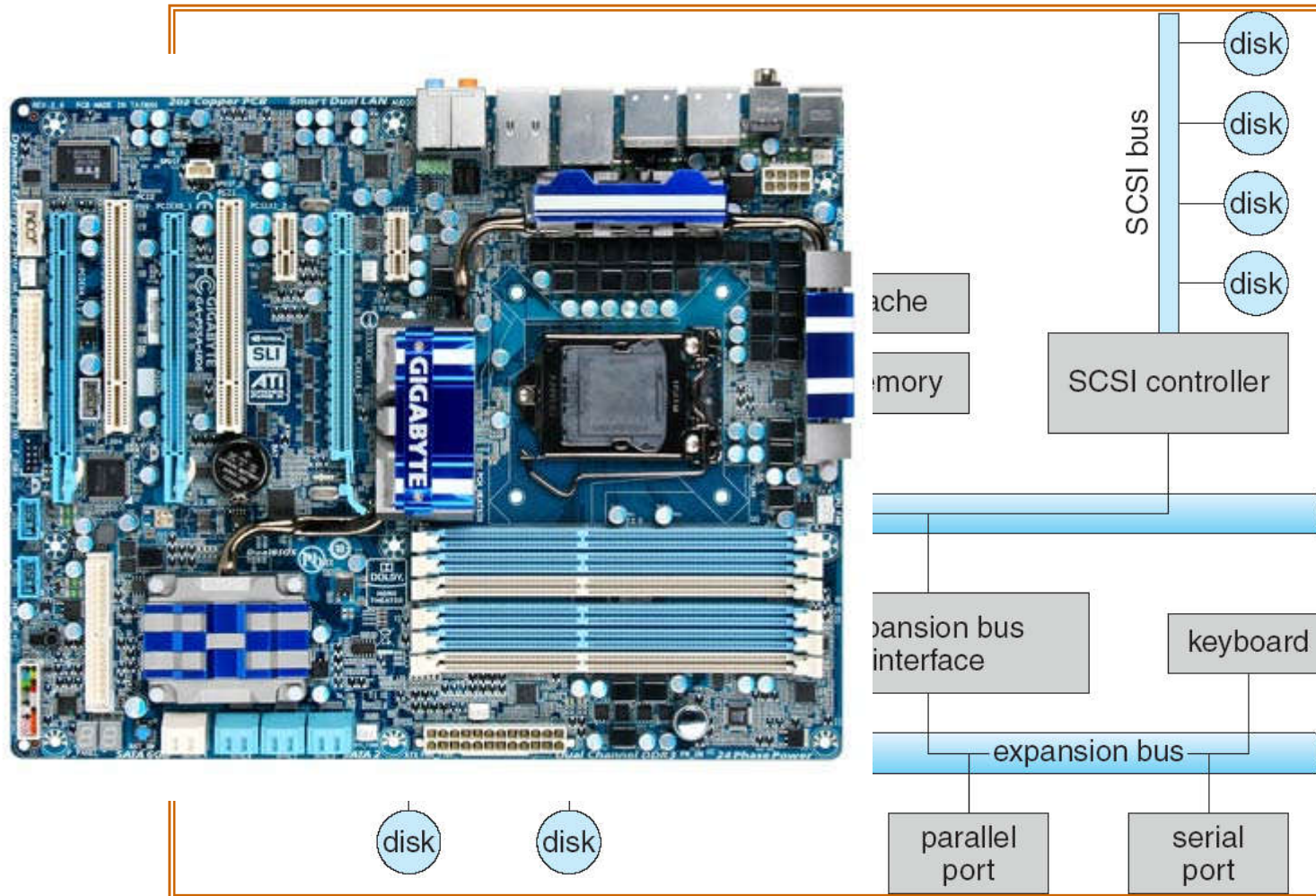




Inside structure



Inside structure



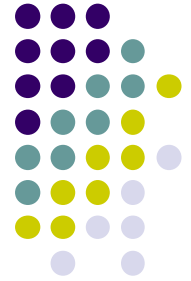
Inside structure



disk

disk





Abstractions

- Abstraction helps us deal with complexity
 - Hide lower-level detail
- Instruction set architecture (ISA)
 - The hardware/software interface
- Application binary interface
 - The ISA plus system software interface
- Implementation
 - The details underlying and interface

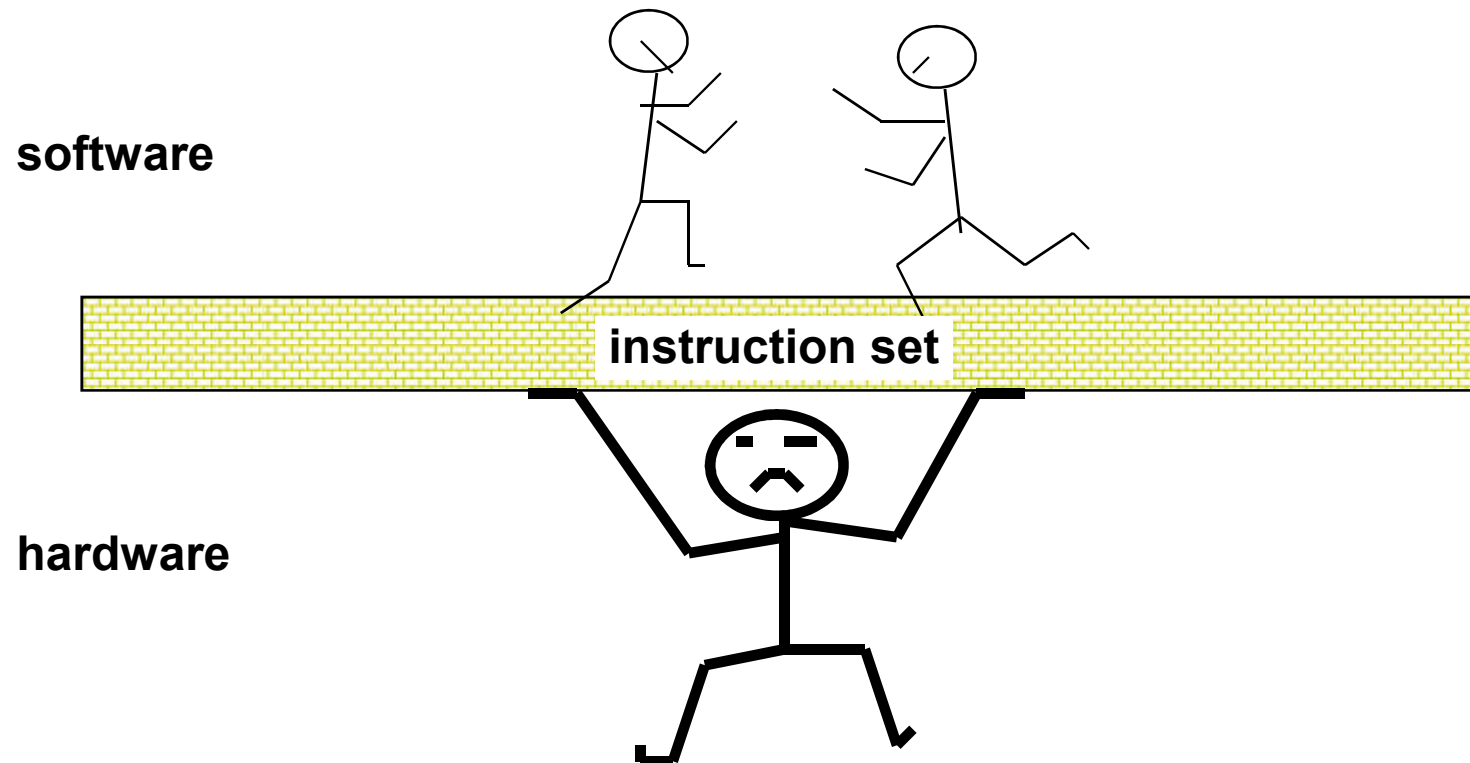
What is Computer Architecture?

Easy Answer



Computer Architecture =
Instruction Set Architecture +
Machine Organization

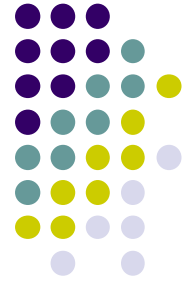
The Instruction Set: a Critical Interface



Instruction Set Architecture

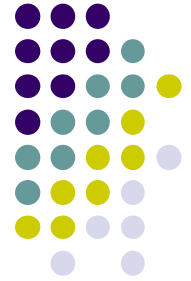


- A very important abstraction:
 - *interface* between hardware and low-level software
 - *standardizes* instructions, machine language bit patterns, etc.
 - advantage: *allows different implementations of the same architecture*
 - disadvantage: *sometimes prevents adding new innovations*
- Modern instruction set architectures:
 - 80x86/Pentium/K6, PowerPC, DEC Alpha, MIPS, SPARC, HP

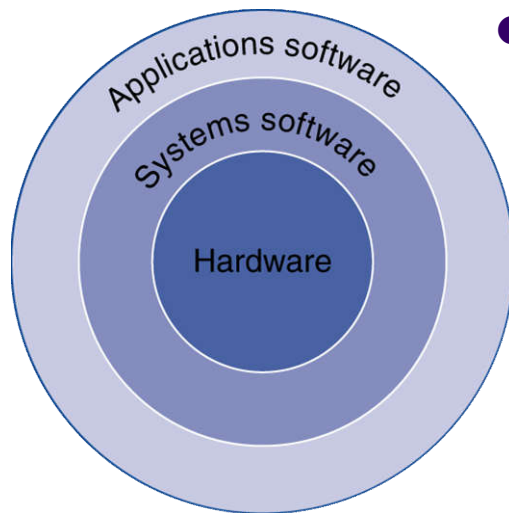


What You Will Learn

- How programs are translated into the machine language
 - And how the hardware executes them
- The hardware/software interface
- What determines program performance
 - And how it can be improved
- How hardware designers improve performance



Below Your Program



- Application software
 - Written in high-level language
- System software
 - Compiler: translates HLL code to machine code
 - Operating System: service code
 - Handling input/output
 - Managing memory and storage
 - Scheduling tasks & sharing resources
- Hardware
 - Processor, memory, I/O controllers

Levels of Program Code

- High-level language
 - Level of abstraction closer to problem domain
 - Provides for productivity and portability
- Assembly language
 - Textual representation of instructions
- Hardware representation
 - Binary digits (bits)
 - Encoded instructions and data

High-level
language
program
(in C)

```
swap(int v[], int k)
{int temp;
 temp = v[k];
 v[k] = v[k+1];
 v[k+1] = temp;
}
```

C compiler

Assembly
language
program
(for MIPS)

```
swap:
    muli $2, $5, 4
    add $2, $4, $2
    lw $15, 0($2)
    lw $16, 4($2)
    sw $16, 0($2)
    sw $15, 4($2)
    jr $31
```

Assembler

Binary machine
language
program
(for MIPS)

```
000000001010000100000000000011000
00000000100011100001100000100001
10001100011000100000000000000000
100011001111001000000000000000100
101011001111001000000000000000010
101011000110001000000000000000100
00000011111000000000000000001000
```



Quiz?

- How can computers play audio files?
- How can they understand characters?

