Generating pulse or train for controlling digital device by op-amp comparator:

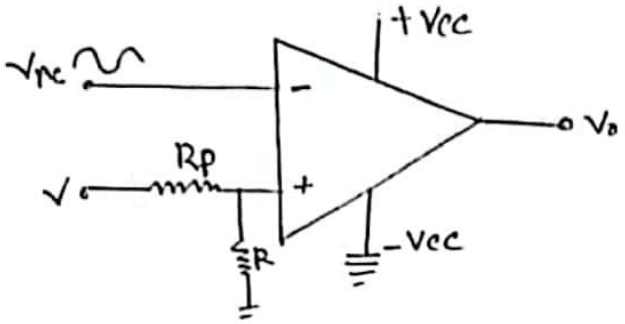
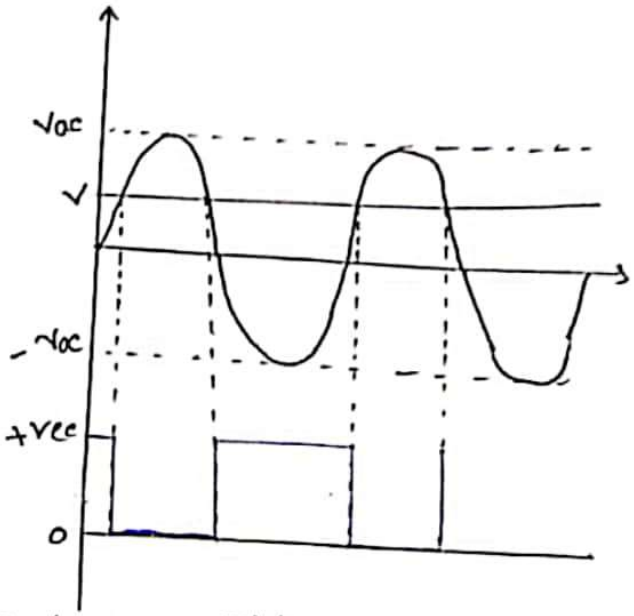


fig: Op-Amp Comparator



First we take a comparator circuit. This comparator circuit has two input part: (i) Inverting and (ii) non-inverting. Here adding a sin wave in inverting input part and create reference voltage (R_f) for non-inverting and adding a register (R). Now comparing sin wave and R_f. If non-inverting voltage is greater then the output is $+V_{sat}$. On the other if inverting voltage is greater then the output is $-V_{sat}$. But can't take $-V_{sat}$ because two voltage level is 0 and. There 1 means $+V_{sat}$ and 0

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means ground. So, ground adding on -Vcc in opamp. That ground means when op-amp inverting voltage bigreater than non-inverting than output voltage is 0.

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