



HARAMAYA UNIVERSITY

HARAMAYA INSTITUTE OF TECHNOLOGY SCHOOL OF ELECTRICAL AND COMPUTER ENGINEERING

Computer Focus

Embedded System Project I.

Title: Automatic Four way Traffic Light

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1. AUTOMATIC FOUR WAY TRAFFIC LIGHT

1.1 Problem statement:

The system have four way traffic light, namely NORTH, SOUTH, EAST, AND WEST. Each traffic light has its own counter which is displayed using two digit seven segment. Each of these ways have its own state which are green, yellow, and green. They can have only one state at a time with a delay of 10 second for green and yellow and 20 second for red state for next state.

1.2 Tables and Figures

Table 1: Required materials.

	Device used	Quantity
1	8051 family micro controller	1
2	Traffic light	4
3	Two digit seven segment	4
4	Resistor pack(pull up resistor)-8	1
5	Proteuse software 8.3	-
6	Keil micro vision software 5	-

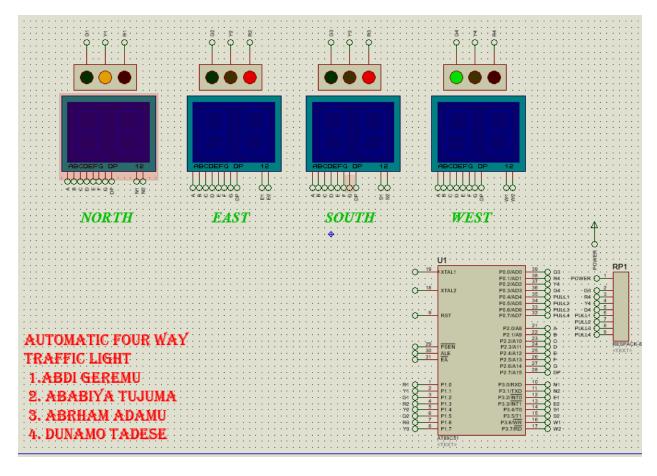
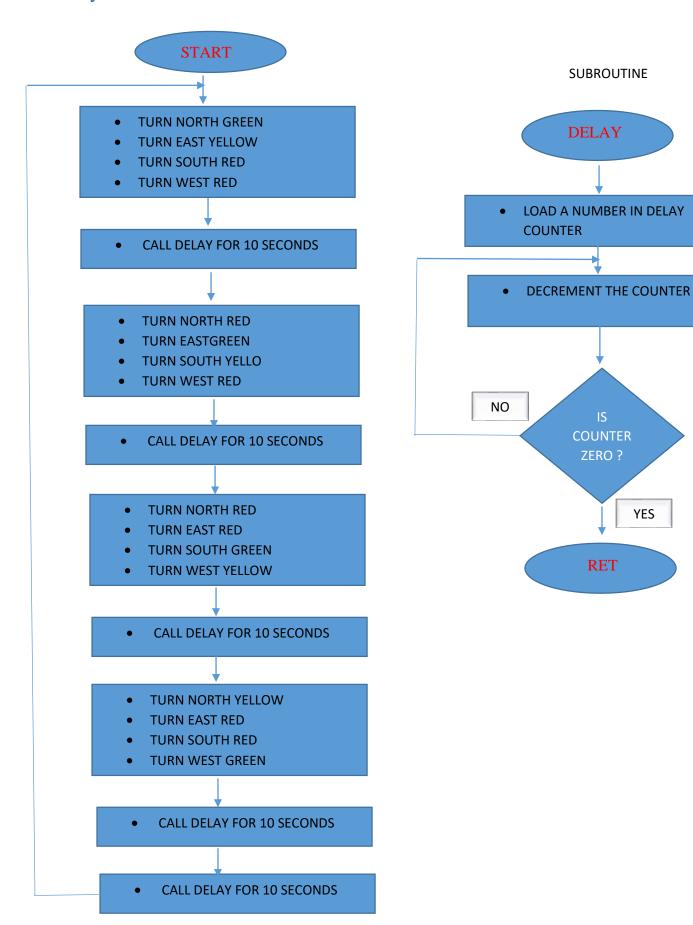


Figure 1 proteuse design

1.3 System flow Chart



1.4 Assembly Code

	riosermony estate	Ĩ	
2		57	CLR P3.2
3	ORG 00H	58	MOV A,45H
4	LJMP MAIN	59	MOVC A, @A+DPTR
5	ORG 300H	60	MOV P2,A
6	TBL: DB	61	ACALL DELAY
	ОСОН, ОБ9Н, ОА4Н, ОВОН, 99Н, 92Н, 82Н,	62	MOV P3,#00H
	0F8H,80H,90H ;7seg	63	
7		64	
7	ORG 30H	65	MO17 7 4 C11
8			MOV A, 46H
9	MAIN: MOV P2,#00H	66	MOV B,#10
10	MOV P3,#00H	67	DIV AB
11	ACALL FRONT	68	MOV 47H,A
12		69	MOV 48H,B
	MOV DPTR, #TBL	70	·
13	CLR A		SETB P3.4
14	MOV 40H,#10	71	CLR P3.5
15	MOV 43H,#10	72	MOV A,47H
16	MOV 46H,#20	73	MOVC A, @A+DPTR
17	MOV 49H, #20	74	MOV P2,A
	•	75	ACALL DELAY
18	MOV R0,#35		
19	MOV R6,#30	76	MOV P3,#00H
20	MOV R7,#40	77	SETB P3.5
21	X1: MOV A,40H	78	CLR P3.4
22	•	79	MOV A, 48H
	MOV B,#10	80	MOVC A, @A+DPTR
23	DIV AB		
24	MOV 41H,A	81	MOV P2,A
25	MOV 42H,B	82	ACALL DELAY
26		83	MOV P3,#00H
27		84	·
	7.1	85	
	A1: SETB P3.0		NOT7 7 4011
29	CLR P3.1	86	MOV A, 49H
30	MOV A,41H	87	MOV B,#10
31	MOVC A, @A+DPTR	88	DIV AB
32	MOV P2, A	89	MOV 50H, A
33	ACALL DELAY	90	MOV 51H,B
		91	SETB P3.6
34	MOV P3,#00H		
35	SETB P3.1	92	CLR P3.7
36	CLR P3.0	93	MOV A,50H
37	MOV A, 42H	94	MOVC A, @A+DPTR
38	MOVC A, @A+DPTR	95	MOV P2,A
39		96	ACALL DELAY
	MOV P2,A	97	
40	ACALL DELAY		MOV P3,#00H
41	MOV P3,#00H	98	SETB P3.7
42	SJMP X3	99	CLR P3.6
43	X2: SJMP X1	100	MOV A,51H
	X3: MOV A, 43H	101	MOVC A, @A+DPTR
	·	102	MOV P2,A
45	MOV B,#10		
46	DIV AB	103	ACALL DELAY
47	MOV 44H,A	104	MOV P3,#00H
48	MOV 45H,B	105	
49	SETB P3.2	106	
50		107	DJNZ RO,X2
	CLR P3.3	108	MOV R0,#35
51	MOV A, 44H		1.10 A 1/0, #22
52	MOVC A, @A+DPTR	109	
53	MOV P2,A	110	DJNZ 40H,Q1
54	ACALL DELAY	111	MOV 40H,#20
55	MOV P3,#00H	112	
56	SETB P3.3	113Q1:	DJNZ 43H,Q2
20	SEID FS.S	I×-•	20112 1011, 22

114	MOV 43H,#10
115	ACALL RIGHT
116	
117Q2:	DJNZ 46H,Q3
118	MOV 43H,#20
119	DJNZ 46H,Q3 MOV 43H,#20 MOV 46H,#10
120	
121Q3:	DJNZ 49H,Q4
122	MOV 49H,#10
123	ACALL BACK
124	
125Q4:	DJNZ R6,X4
126	ACALL LEFT
127	MOV 40H,#10
128	MOV 43H,#10
129	MOV 46H,#30
130	
	DJNZ R7,L1
132	LJMP MAIN
133L1:	LJMP X1
134	11011 D 4 E
135DELAY:	MOV R4,#5 MOV R5,#0FFH
136H2:	MOV R5, #UFFH
15/111.	DJNZ R5,H1
138	DJNZ R4,H2
139 140	RET
141 FRONT:	MOM D1 #54U
141FRONT:	MOV P1,#54H MOV P0,#02H
143	RET
144	KEI
145RIGHT:	MOV P1,#0A1H
145K10III.	MOV PO, #02H
147	RET
148	TUL
149BACK:	MOV P1,#09H
150	MOV P0,#05H
151	RET
152	-
153LEFT:	MOV P1,#4AH
154	MOV PO, #08H
155	RET

1.5 Help

- 1. Open circuit file from proteuse circuit folder with proteuse software.
- 2. Upload hex file in object folder for 8051 microcontroller
- 3. Run the simulation.