

Nhung van de cua v1.0: Ve sai vi trí 8.4V\_2CELL Chuyen C202 sang SMD Check lai phu mass vì khá khó hàn Chon lai giá tri R208/R205 de ra 12V tròn, thêm 2 ĐT Thêm giai pháp on/off FET CHG/DSG tu MCU Them test point Đoi connector noi tu các cell den mach sang dang cam chac chan hon Đoi kieu chân cua STM32 Tính thêm vi trí bat NTC Phuong án dùng tín hieu cua anten de wake up MCU U\_POWER POWER.SchDoo U\_NFC NFC.SchDoc high\_voltage BAT+ BAT+ +5V [ → +5V U\_MCU MCU.SchDoc NFC\_SPI2\_CS NFC\_SPI2\_SCK NFC\_SPI2\_MISO NFC\_SPI2\_MOSI NFC\_SPI2\_CS
NFC\_SPI2\_SCK
NFC\_SPI2\_MISO
NFC\_SPI2\_MOSI +3.3V 3.3V\_REG\_AFE < EN\_NFC\_H EN NFC H U\_AFE AFE.SchDoc NFC\_IRQ < NFC\_IRQ BAT+ 3.3V\_REG\_AFE \_\_\_\_ +3.3V \_\_\_\_ +5V PACK\_I-AFE\_I2C\_ALERT AFE\_I2C\_SCL AFE\_I2C\_SDA AFE\_I2C\_ALERT
AFE\_I2C\_SCL
AFE\_I2C\_SDA NFC\_CLK\_OUT VDD\_X > PACK (1) TEST\_SW\_PS TEST\_SW TEST\_SW\_PS TEST\_SW BAT-1.65V\_REF [ PACK\_CURR [ 1.65V\_REF PACK\_CURR Number Revision Size A3 11-May-19 Sheet
C:\Users\..\HW\_OVERVIEW.SchDoc Drawn By









