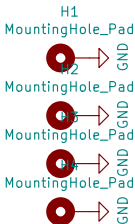
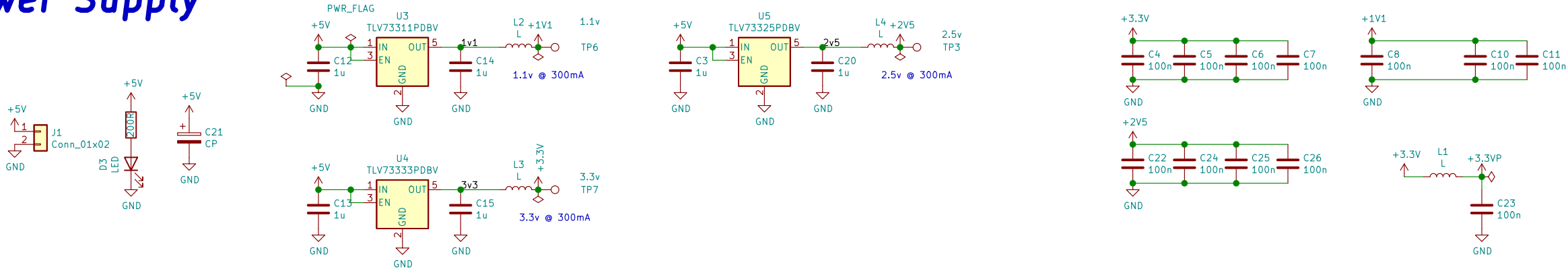
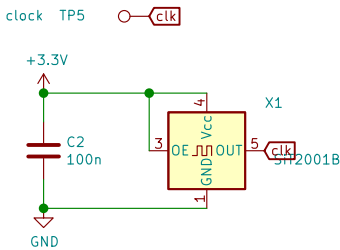


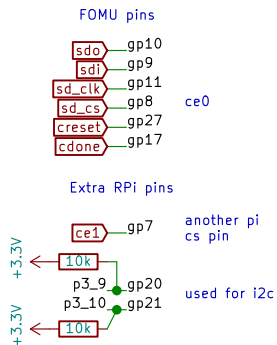
Power Supply



Clock



Programming

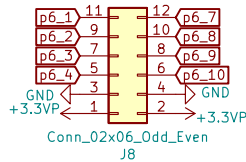
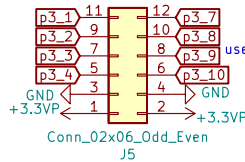
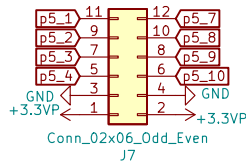
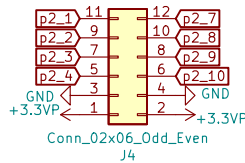
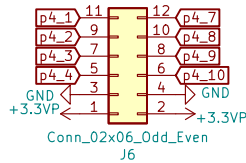
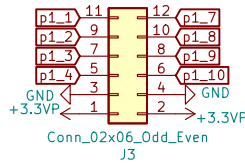
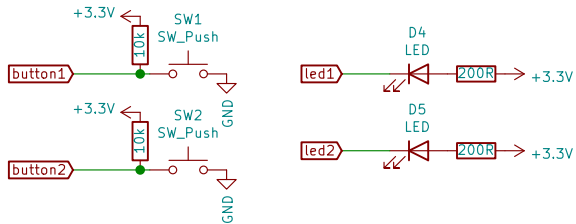


FPGA

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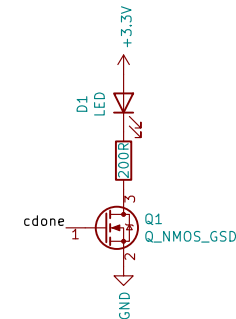
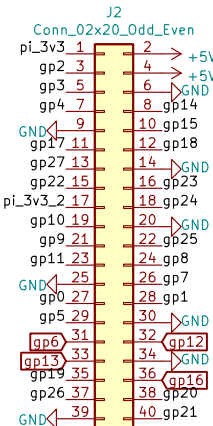
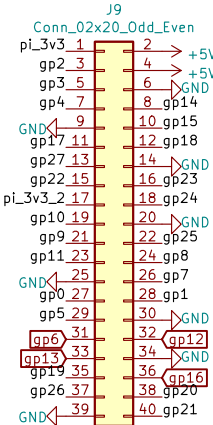
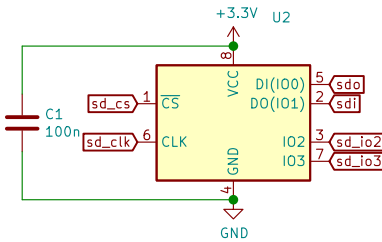
Peripherals



FLASH

16Mb FLASH supports 0Bh fast read

IS25LP016D-JBLE



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Title: Matt-FPGA

Size: A3 Date: 2019-12-30

KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

Rev: 0.1

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