

República Bolivariana de Venezuela Universidad Politécnica Antonio José de Sucre "UNEXPO" Materia: Microprocesadores II

LABORATORIO 5

Profesor: Rafael Surga Integrantes: Brito Eduardo Duran Alejandro Zorrilla Paola

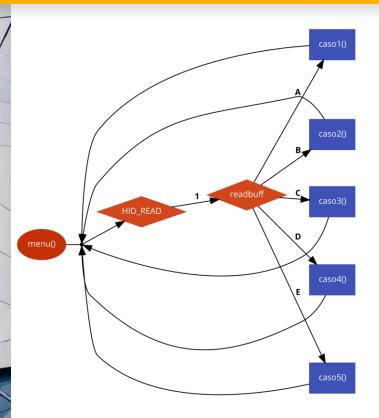
ESQUEMATICO

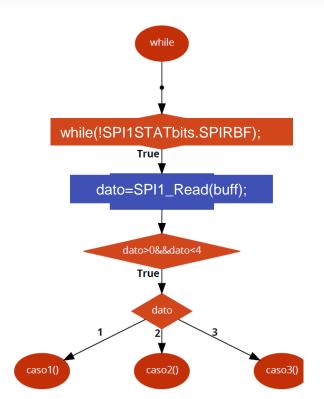


MENU DSPIC 1

MENU DSPIC 2





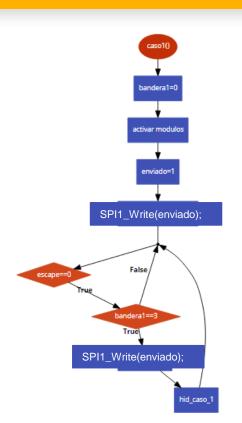


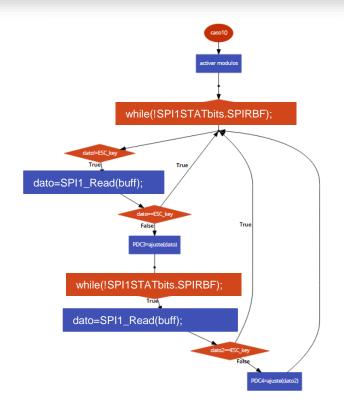


CASO 1 DSPIC 1

CASO 1 DSPIC 2





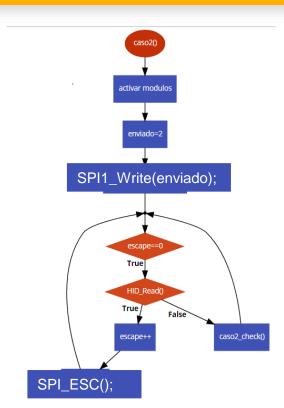


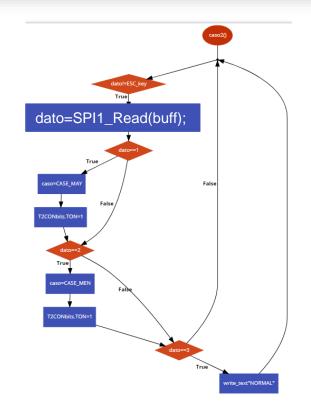


CASO2 DSPIC1

CASO 2 DSPIC 2







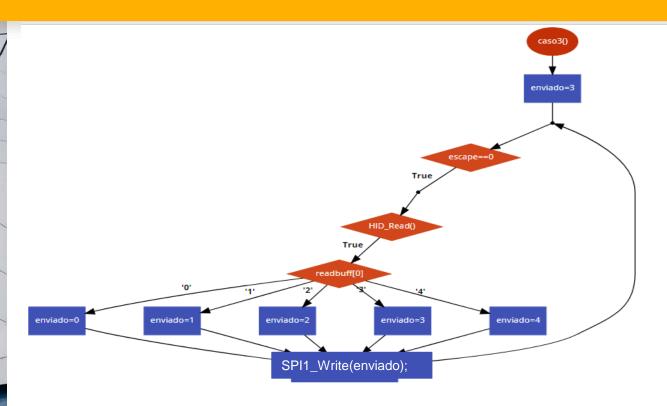
caso2_check() CM3CONbits.COUT==0 True CM1CONbits.COUT==0 bandera2==0 True True bandera2=1 bandera2==0 CM1CONbits.COUT==1&&CM3CONbits.COUT==1 True True write("Voltaje en limite superior") bandera2=2 bandera2<3&&bandera2>0 True False write("Voltaje en limite inferior") bandera2=3 bandera2=0 enviado=2 write("Voltaje Normal") enviado=1 enviado=3 UART1_Write(enviado)

CASO 2 CHECK

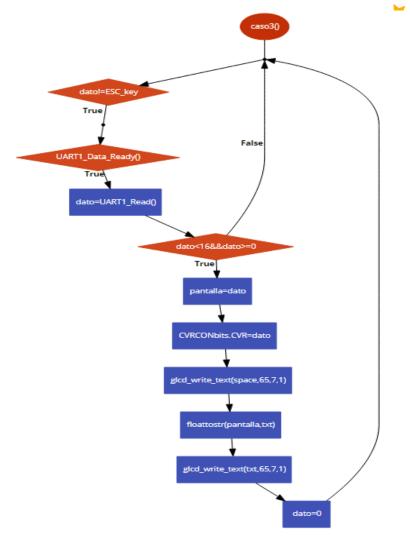


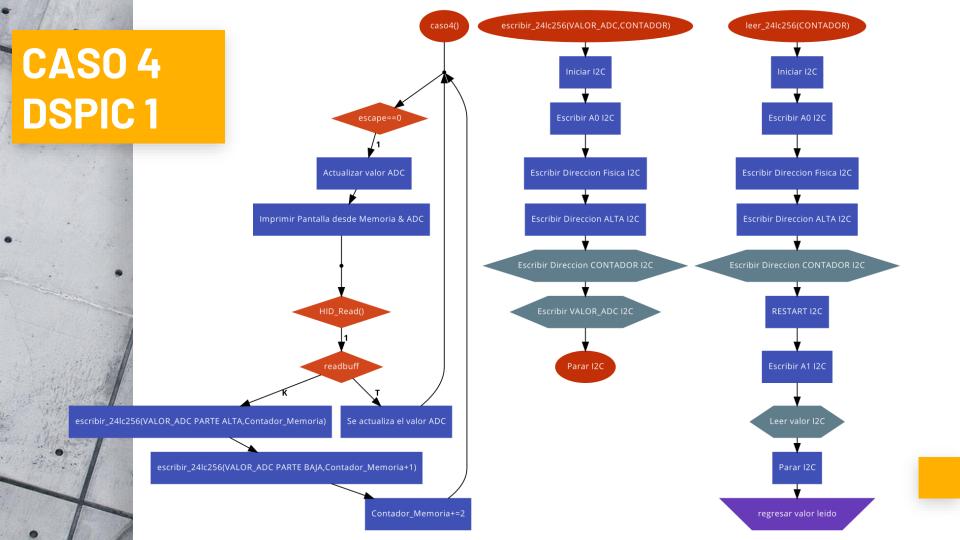
CASO 3 DSPIC 1

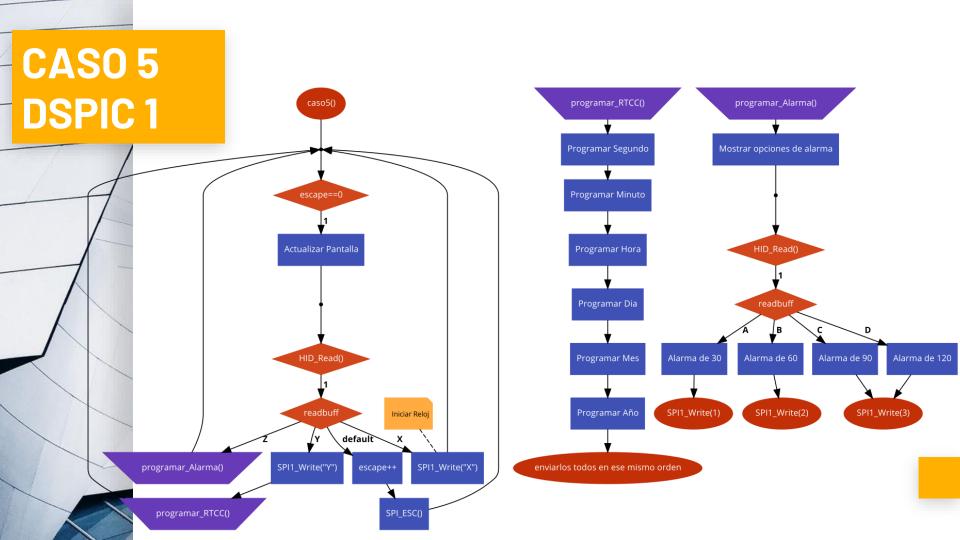




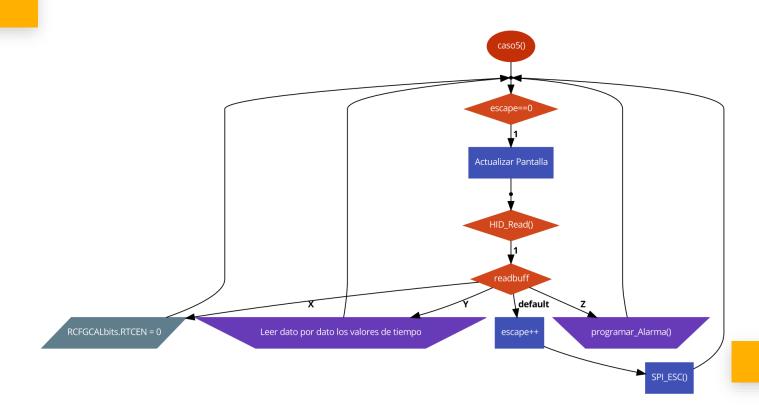
CASO 3 DSPIC 2

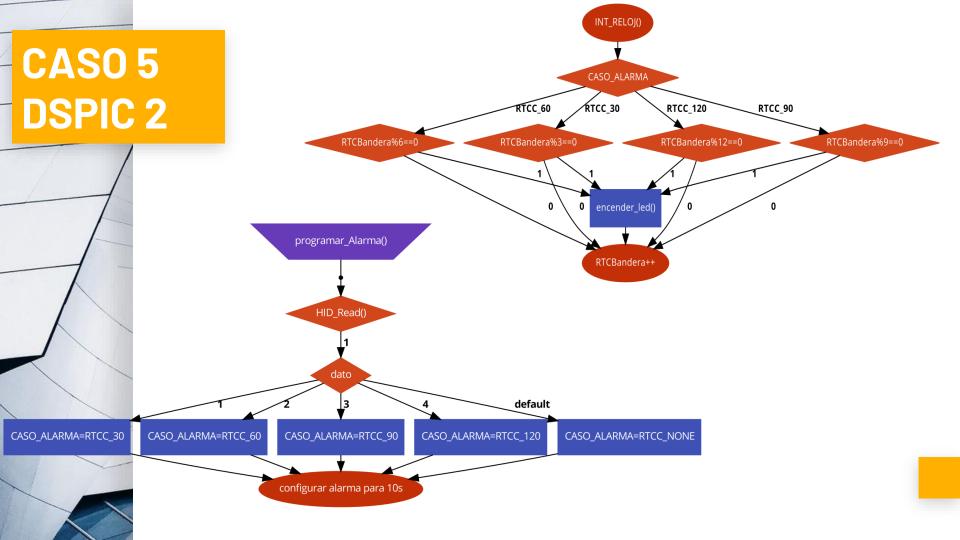






CASO 5 DSPIC 2







i Gracias por su atención!