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Start	n Wednesday, 13 December 2017, 9:56 PM	
	te Finished	
Complet	www. Wednesday, 13 December 2017, 9:58 PM	
Time	n 2 mins 37 secs	
	le 0.00 out of 106.00 (0 %)	
Question 1 Not answered Mark 0.00 out of 2.00	which of the following is true for an NPN BJT? elect one: a. Current flows when either Vbe or Vbc are negative vbb. The base current consists of mostly electrons c. Current flows primarily because of electrons injected d. An N-type base is sandwiched between a P-type er collector	d into the base
	e. All of these	

The correct answer is: Current flows primarily because of electrons injected into the base

Question 2 Not answered Mark 0.00 out of 2.00	Which of the following is true in modern bipolar junction transistors? Select one: a. All of these b. The base doping is much higher than the emitter doping to minimize the number of carriers injected from the base into the emitter c. Carriers diffuse across the base and are collected by the emitter d. The base is kept wide to minimize recombination in the base e. The minority carrier concentration in the base decreases almost linearly from the emitter to the collector
	The correct answer is: The minority carrier concentration in the base decreases almost linearly from the emitter to the collector
Question 3 Not answered Mark 0.00 out of 2.00	An PNP BJT operating in the cutoff region has : Select one: a. Vbe > 0 and Vbc < 0 b. Vbe < 0 and Vbc < 0 c. Vbe < 0 and Vbc > 0 d. None of these e. Vbe > 0 and Vbc > 0
	The correct answer is: Vbe > 0 and Vbc > 0

Question 4 Not answered Mark 0.00 out of 2.00	 For a BJT operating in saturation, which of the following is true? Select one: a. The output resistance, ro , is much larger than in the forward-active region b. All of these c. The base-collector junction can be forward biased by about 400mV before the collector current starts to decrease d. The collector current increases linearly as the base current is increased e. The common-emitter current gain, β , is much larger than in the forward-active region
	The correct answer is: The base-collector junction can be forward biased by about 400mV before the collector current starts to decrease
Question 5 Not answered Mark 0.00 out of 2.00	If an NPN BJT at 300°K with a constant collector current of 1mA has a Vbe voltage of 780mV, then what will Vbe be for this same BJT if the collector current is decreased to 100μΑ? Select one: a. 720mV b. None of these c. 780mV d. 660mV e. 840mV
	The correct answer is: 720mV

Question 6 Not answered Mark 0.00 out of 2.00	As Vce increases for a BJT in the forward active region, "base-width modulation" causes: Select one: a. None of these b. The output resistance, ro, to increase c. The width of the base to increase d. The width of the base-collector depletion region to decrease e. The collector current for the BJT to decrease The correct answer is: None of these
Question 7 Not answered Mark 0.00 out of 2.00	Considering the typical input and output resistances, which of the following BJT amplifier types is well suited to be used as a voltage amplifier? Select one: a. Emitter-follower b. None of these c. Common-emitter d. All of these e. Common-base The correct answer is: Emitter-follower

Question 8 Not answered Mark 0.00 out of 2.00	For a BJT common-collector amplifier, which of the following is true? Select one: a. The voltage gain is negative b. The output resistance is typically high c. The voltage gain is typically high d. The input resistance is typically low e. None of these The correct answer is: None of these
Question 9 Not answered Mark 0.00 out of 2.00	For a MOS source-follower amplifier, which of the following is true? Select one: a. The drain is used by both the input and output ports b. The input signal is applied to the gate c. All of these d. NMOS and PMOS FETs use the same circuit topologies e. The output signal is measured at the source
	The correct answer is: All of these

Question 10	For a MOS common-gate amplifier, which of the following is true?
Not answered Mark 0.00 out of	Select one:
2.00	a. None of these
	 b. NMOS and PMOS FETs use different circuit topologies
	c. The drain is used by both the input and output ports
	d. The output signal is measured at the source
	e. The input signal is applied to the gate
	The correct answer is: None of these
Question 11	For a BJT each PN junction can be either forward or reverse biased, which gives
Not answered	4 possible regions of operation.
Mark 0.00 out of 2.00	Select one:
	O True
	O False
	The correct answer is 'True'.
Question 12	If the base-emitter junction of a BJT is reverse biased and the base-collector junction is forward biased, then the BJT is operating in the reverse-active region
Not answered Mark 0.00 out of	of operation.
2.00	Select one:
	O True
	O False
	-
	The correct answer is 'True'.
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Question 13 Not answered Mark 0.00 out of 2.00	In the forward-active region the emitter current of an NPN BJT consists of holes injected from the emitter into the base and electrons injected from the base into the emitter. Select one: True False
	The correct answer is 'False'.
Question 14	As the Vce for a BJT increases, the width of the base decreases which causes
Not answered	the collector current to increase.
Mark 0.00 out of	Select one:
2.00	O True
	O False
	The correct answer is 'True'.
Question 15 Not answered	Voltage amplifiers need to have high input resistance and low output resistance.
Mark 0.00 out of	Select one:
2.00	O True
	O False
	The correct answer is 'True'.

Question 16 Not answered Mark 0.00 out of	When finding the resistance "looking into" a node, a voltage source Vx can be applied and the current, Ix, which flows in this source found. Then the resistance is equal to Ix/Vx.
2.00	Select one:
	O True
	O False
	The correct answer is 'False'.
47	
Question 17 Not answered	The gain of a common-base BJT amplifier can be estimated by the ratio of the bias voltage across the collector resistor to the bias voltage across the emitter
	resistor.
Mark 0.00 out of 2.00	
	Select one:
	O True
	O False
	The correct answer is 'True'.
Question 18 Not answered	All three BJT amplifier types (CE, CB, CC) are identical when the input and output are applied.
Mark 0.00 out of	
2.00	Select one:
	True
	O False
	The correct answer is 'False'.

Question 19 Not answered Mark 0.00 out of 2.00	The gain of a common-gate MOSFET amplifier can be estimated by the ratio of the drain resistor to the source resistor. Select one: True False The correct answer is 'True'.
Question 20 Not answered Mark 0.00 out of 2.00	Since current normally flows into the source of a NMOS FET, the source is usually drawn pointing up towards the positive power supply. Select one: True False The correct answer is 'False'.
Question 21 Not answered Mark 0.00 out of 6.00	What is the open-circuit voltage gain, μf, in V/V for an PNP BJT operating in the forward-active region at 27° C with Ic = 859μΑ? Use: β = 18, VA = 65V and Vt = kT/q = 26mV. Answer:

Question 2	22
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Not answered

Mark 0.00 out of 6.00

What is the output resistance, rds, in $k\Omega$ for an NMOS FET operating in saturation with Id = 835µA? Use: $\lambda=0.40$

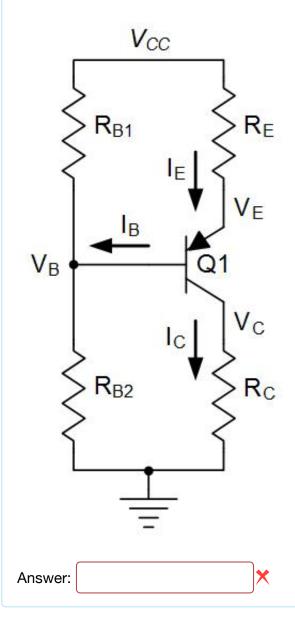
Answer:

The correct answer is: 2.99

Not answered

Mark 0.00 out of 6.00

For the BJT bias circuit shown, what is the collector current, Ic, in milliamps? Use Vcc=12V, $Rb1=27.5k\Omega$, $Rb2=20.2k\Omega$, $Rc=1.1k\Omega$, and $Re=4.5k\Omega$. Assume that the transistor is in the forward-active region, with $\beta=35$ and |Vbe(on)|=0.7V. Neglect the effects of base-width modulation.

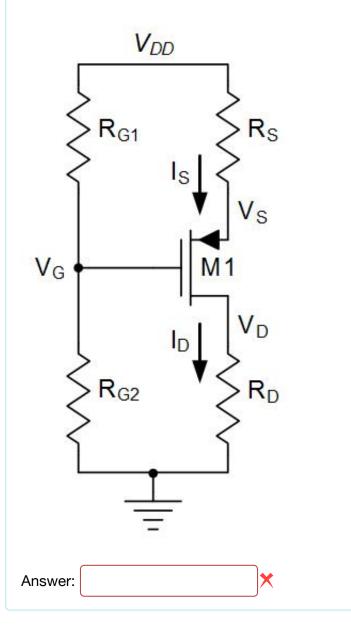


The correct answer is: 1.25

Not answered

Mark 0.00 out of 6.00

For the MOSFET bias circuit shown, what is the source voltage, Vs, in Volts? Assume that the transistor is in the saturation region, and use: Vdd = 11V, Rg1 = $49.4k\Omega$, Rg2 = $56.2k\Omega$, Rd = $4.1k\Omega$, Rs = $9.7k\Omega$, Vt = -0.5V, and |Von| = 0.44. (Remember that |Von| = |Vov| = |Vgs|-|Vt|) Neglect the effect of channel-length modulation and body effect.

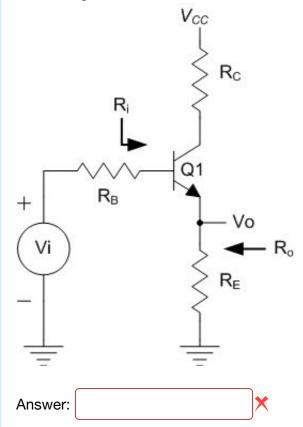


The correct answer is: 6.79

Not answered

Mark 0.00 out of 6.00

What is the low frequency voltage gain for the amplifier shown at 27° C with Rc = $39.2k\Omega$, Re = $4.5k\Omega$ and Rb = $0.3k\Omega$? Use: Ic = 668μ A, β = 74, and Vt = kT/q = 26mV. Neglect the effect of base-width modulation.

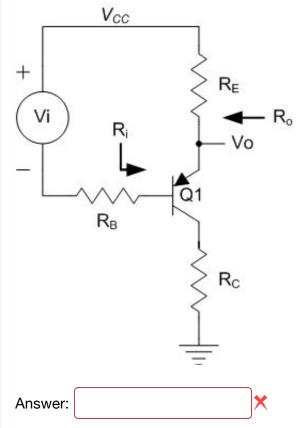


The correct answer is: 0.991

Not answered

Mark 0.00 out of 6.00

What is the low frequency voltage gain for the amplifier shown at 27° C with Rc = $32.7k\Omega$, Re = $0.5k\Omega$ and Rb = $0.8k\Omega$? Use: Ic = 631μ A, β = 50, and Vt = kT/q = 26mV. Neglect the effect of base-width modulation.

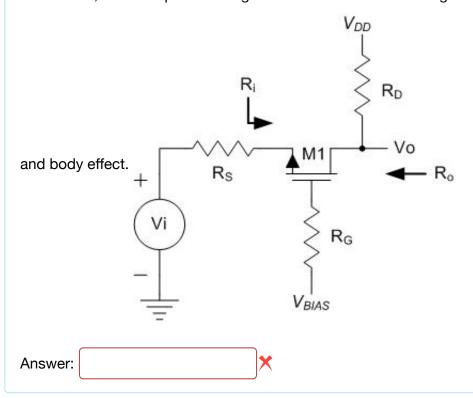


The correct answer is: 0.899

Not answered

Mark 0.00 out of 6.00

What is the low frequency input resistance, Ri, in Ω for the amplifier shown at 27° C with Rd = $48.4k\Omega$, Rs = $10.0k\Omega$ and Rg = $7.6k\Omega$? Use: W/L = 95, Id = 509μ A, VTN = 0.5V, k'n = 100μ A/V^2. Neglect the effect of channel-length modulation

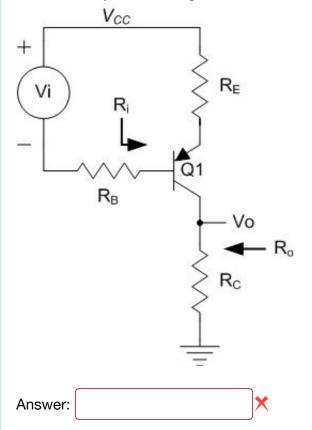


The correct answer is: 321.6

Not answered

Mark 0.00 out of 6.00

What is the low frequency input resistance, Ri, in $k\Omega$ for the amplifier shown at 27° C with Rc = $44.3k\Omega$, Re = $4.3k\Omega$ and Rb = $0.3k\Omega$? Use: Ic = 449μ A, β = 12, and Vt = kT/q = 26mV. Neglect the effect of base-width modulation.

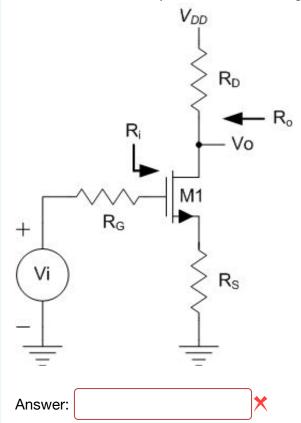


The correct answer is: 56.6

Not answered

Mark 0.00 out of 6.00

What is the low frequency output resistance, Ro, in $k\Omega$ for the amplifier shown at 27° C with Rd = $37.2k\Omega$, Rs = $0.8k\Omega$ and Rg = $2.9k\Omega$. Use: W/L = 16, Id = 482μ A, VTN = 0.5V, k'n = 100μ A/V^2, λ = 0.05 Neglect body effect.

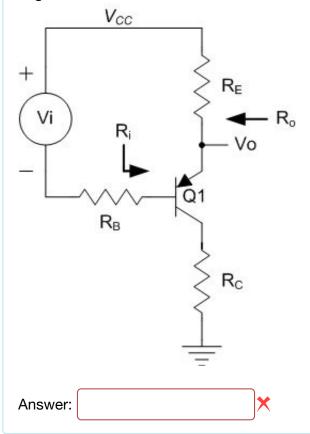


The correct answer is: 25.7

Not answered

Mark 0.00 out of 6.00

What is the low frequency output resistance, Ro, in Ω for the amplifier shown at 27° C with Rc = $34.7k\Omega$, Re = $0.5k\Omega$ and Rb = $0.8k\Omega$? Use: Ic = 410μ A, β = 10, and Vt = kT/q = 26mV. Use the "short-cut approach" discussed in class, and neglect the effect of base-width modulation.

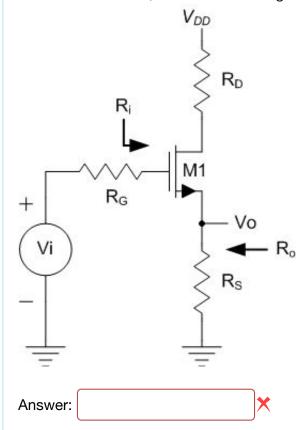


The correct answer is: 103.4

Not answered

Mark 0.00 out of 6.00

Estimate the maximum low frequency voltage gain for the amplifier shown at 27° C with Rd = $42.4k\Omega$, Rs = $0.2k\Omega$ and Rg = $2.0k\Omega$.



The correct answer is: 1.000