CpE 151 and EEE 234 CMOS and VLSI Design



Quiz – 2

For CpE 151, 5 lowest grade questions will be dropped. For EEE234 2 questions will be dropped.

Name:	
1. Shown belo	ow are transistor higher order effects. Identify a lateral field effect from below.
	Subthreshold conduction
b.	Mobility degradation
c.	Body effect
d .	Velocity saturation
2. Shown belo	ow are transistor higher order effects. Identify a vertical field effect from below.
a.	Subthreshold conduction
b.	Mobility degradation
c.	Body effect
d.	Velocity saturation
3. "Pinch-off"	because of channel length modulation occurs in which region of operation?
a.	Subthreshold
(b.)	Saturation
c.	Linear
d.	Both (b) and (c)
	t modulates the threshold voltage (Vt) of a MOSFET. As Vs increases/Vb decreases, what the Vt?
/ Happens to	(increases) decreases)
	short channel effect, the \sqrt{th} of a MOSFET increases (increases) decreases) using values of L.
7. Ion of a MC	OSFET <u>decreases</u> with temperature and loff increases with temp.
8 Forthons	ss transistor chain shown in figure - 1 below, what is the voltage at the output?
o. For the pas	
	VDD-VTH
	V_{DD} V_{DD}
	V_{DD} \top \top \top
	output

Figure – 1

9 For the pass transistor shown in figure - 2 below, what is the voltage at the output?

output
$$V_{SS}$$
Figure - 2

10. For the pass transistor chain shown in figure - 3 below, what is the voltage at the output?

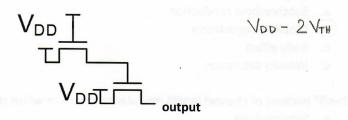
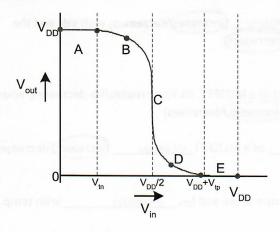


Figure – 3

11 For each of the regions shown below in the DC transfer curve, identify the regions of operation for the PMOS and NMOS. Fill in the table from figure – 4. (5 Points)



nMOS	pMOS
OFF	Linear
Saturation	Linear
Saturation	Saturation
Linear	Saturation
Linear	OFF
	OFF Saturation Saturation Linear

Figure - 4