

S2010 MT 1

1. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string, Hello_msg. They are:

DS = SEG Hello_msg, DX=OFFSET Hello_msg, Hello_msg terminated with 36

2. Moore's Law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate?

Doubling every 18 – 24 months

3. Given:

AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JNL 0118

What will the IP value be after a "t" command is executed in DOS Debug?

010B

4. A "NOP" instruction will:

Perform a No Operation

5. Given:

AX=F247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC
1D72:0106 EB0F JMP 0118

What will the IP value be after a "t" command is executed in DOS Debug?

0118h

6. In x86 architecture, BIU stands for which of the following?

Bus Interface Unit

7. Determine the contents of register BL after the following instructions have been executed:

2EH

Program Listing
MOV BL, E2H
MOV CL, 100b
ROL BL, CL

7 Determine the contents of register BL after the following instructions have been executed:

2EH Program Listing

MOV BL, E2H
MOV CL, 1000b
ROL BL, CL

8. The number of bytes in a double word are:

4

9. In x86 architecture, ALU stands for which of the following?

Arithmetic Logic Unit

10. The "LOOPNE" instruction is equivalent to which of the following instructions?

DEC CX, JNE

11. The instruction MOV CX, DADD is what addressing mode?

Immediate

12. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The third instruction operator is:

INT

12. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The forth instruction operator is:

JGE

12. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The fifth instruction operator is:

CMP

13. Which of the following DOS Debug instructions would set a break point at memory location 010C?

G = 100 10C

14. How many bytes are there in this short sequence of code? B400CD164CCD21

7

15. How many address lines would be required to address 128 MB directly?

27

16. What are the contents of CX after this program has been run:

D800h

17. 1010 0110 in 2's complement equals ____ in base 10.

-90

18. What is the hexadecimal encoding for adding AX with BX and storing the result in AX?

01D8

19. What is 11.4375₁₀ in binary?

001011.01110

20. If CX is 0000 what will CX be after a "LOOP" instruction?

FFFF

21. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?

7DF2

22. Given:

AX=FFEE BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO NC
1D72:010F 7D08 JGE 0115

How many bytes will the processor jump if the conditions for a jump were met?

7 if not an option pick 8

23. What command in DEBUG would be used to execute interrupts?

P

24. What is the advantage of C Language over Assembly Language?

C is transportable to other microprocessor architectures

25. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

OF = 1, CF = 0

26. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?

36

27. Given:

AX=0353 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC
1D72:010D 7DF6 JNL 0116

-10

28. AND'ing 1FH and 20H will result in which of the following?

0

29. The letters "NO" labeled on relays and PLCs means which of the following?

Normally open

13A7:0110 CD 20 32 20 54 48 39 73-20 69 73 20 74 68 65 20
13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9
13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0115, what is the size of the buffer in decimal?

72

30. A microprocessor with a 31-bit address bus could access how much memory?

2 GB

31. For the instruction sequence below, determine the contents of the register AL after this program is executed:

51H

Memory Location	Contents
1103	24
1102	D8
1101	00
1100	21

Program Listing
MOV AL, 83h
ADD AL, 68h
DAA

Program Listing
MOV AL, 73h
ADD AL, 78h
DAA

32. Given:

AX=**FF00** BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

-256

33. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:

32, 32, 13, 10, 36

What is -96.2697 converted to single precision FP?

C2 c0 8A 16

What is C2 Co 8a 16 Hex single precision FP converted to Decimal

-96.2697

Ieee converter

<http://www.h-schmidt.net/FloatConverter/IEEE754.html>

S2010 MT2

1. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 99?

7

2. If CX is 0003, what will CX be after a “LOOP” instruction?

0002

3. IN the Propeller microcontroller, the command “waitcnt(clkfreq*5 + cnt)” would cause the processor to do which of the following?

A 5 second delay

4. What is the number 1011.0101_2 in decimal?

11.31

5. This section of memory represents a stack. What type of program is this?

BEEF : FFD0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00

BEEF : FFE0 00 01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F

BEEF : FFF0 11 22 33 44 55 66 77 88-99 AA BB CC DD EE FF

COM program

6. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack?

BDCA

7. Determine the contents of register BL after the following instruction have been executed:

2EH

Program Listing
MOV BL, 2E
MOV CL, 10
ROL BL, CL

8. What Hex values must be sent to address the key pad rows on the PPE board?

1, 2, 4, 8

9. What is the number 32.4375_{10} in binary?

100000.01110

10. In MASM, with a “MOV CX, 18h” instruction, and a “LOOP” instruction, in decimal how many times will the program loop?

24

11. The acronym PWM used for motor control, is defined as which of the following?

Pulse Width Modulation

12. In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?

Bit 7 of port D is set to input

12. In the PIC18 with TRISD = 0b11111111, what is the configuration of the Port D?

Bit 8 of port D is set to input

13. Given the short code, what is the value in AX after the program is run?

0500

Program Listing
MOV BX, 0500
Push BX
MOV AX, 0100
POP AX

13. Given the short code, what is the value in AX after the program is run?

0100

Program Listing
MOV BX, 0100
Push BX
MOV AX, 0500
POP AX

14. What flag(s) does the “LOOPNZ” instruction look at to determine whether to loop or not?

ZF

15. How many nibbles are in double precision IEEE floating point format number?

16

16. How many nibbles are in extended precision IEEE floating point format numbers (ttl10h)?

20

17. What type of program is this?

AX=0000 BX=0000 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1476 ES=1576 SS=1376 CS=1D72 IP=0015 NV UP EI PL NZ NA PO NC

1376:0015 0100 ADD [BI + SI], AL

DS : 0000=CD

EXE

18. If the SP is F00F, what is the SP value after a “PUSH CX” instruction?

F00D

19. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

A0

20. What is the numeric sequence of the key pad columns on the PPE board?

37, 2F, 1F

21. What is -130 decimal in 2's complement (8bits)?

01111110

22. Which of the following is a valid x86 command for multiplying a number?

MUL BX

23. The number of bits in single precision IEEE floating point format are:

32

24. You are trying to rebuild a HELLO project program in MASM and you get the following error: “LINK : warning L4021: no stack segment”.

What would be the reason for such an error?

No project template for COM was selected

25. A “pull down” resistor is used in digital circuits to do what?

To keep the signal line “tied” low until the line is active (goes high)

26. A “POP” instruction:

increments the SP

27. AND'ing 10H and 2FH will result in which of the following?

0

28. In the Propeller microcontroller, the command “dira[4..9] := %111111” would cause the processor to do which of the following?

Sets the Propeller pins P4 through P9 as output pins

29. What commands in MASM-CodeView would be used to step through a program line by line?

T (F8)

30. If the SP is F00F, what will the SP value be after a “POP SP” instruction?

F011

31. On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh?

0

32. In the Propeller microcontroller, the term “Method” is(are) which of the following?

A block of executable commands that has variables, can receive parameters, and returns a value.

33. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal?

MOV CX, 30H or MOV CX, 48

34. A “pull up” resistor is used in digital circuits to do what?

To keep the signal “tied” high until the line is active (goes low)

EEE-174 MT1

1. What is the hexadecimal encoding for "JNL" for a jump back 8 bytes?

0F F8

2. In X86 architecture, ALU stands for which of the following?

Arithmetic Logic Unit

3. The number of nibbles in a word are:

16/4 = 4

4. The instruction MOV BX, [2BAD] is what addressing mode?

Direct

5. What is the hexadecimal encoding for adding DX with BX and storing the result in BX?

01CB

6. A microprocessor with a 32-bit address bus could access how much memory?

$2^4=16\text{MB}$, $2^8=256\text{KB}$, $2^{32}=4\text{GB}$

7. You add 7+6 through a 4 bit integer unit. The state of the OF and CF flags after the add will be:

01111

+0110

0|1101

OF=1, the sign bit has changed

CF=1, there is a carry on of bit 7

8. Which of the following DOS Debug instructions would be used change the IP register to 100?

RIP

Convert 129.C hexadecimal into decimal

297.75 *note after decimal divide by 16

8.1 What is the RS232C, specification voltage range for the Logic 0 output?

+3v to +25v.

8.1 What is the RS232C, specification voltage range for the Logic 1 output?

-3v to -25v.

9. What are the contents of CX after this program has been run:

MOV DX, 11h
MOV CX, [5512]
MOV BX, 5511h
SUB DX, [BX]

0000h

Memory location	Contents
5514	24
5513	D8
5512	00
5511	21

***What are the contents of DX after this program has been run:

FFF0h

9. What are the contents of BX after this program?

What are the contents of BX after this program?

Program	Memory location	Contents
MOV BX, 2024h	2026	F2
MOV CX, 4eh	2025	59
DEC BX	2024	39
AND CX, [BX]	2023	4E

Select one:

☐ a. 2023h

☐ b. F239h

☐ c. 2420h

☐ d. 2024h

☒ e. 394Eh

Assuming DS = 1000h, the instruction sequence in the listing below takes the last byte in the transfer from memory at:

Assuming DS = 1000h, the instruction sequence in the listing below takes the last byte in the transfer from memory at:

STD
MOV CX, 500H
MOV DX, 100H
MOV SI, 250H
AI: LODSB
OUT DX, AL
LOOP AI

Select one:

☒ a. 10500h

☐ b. 10250h

☐ c. 1750h

☐ d. 10750h

☐ e. 0FD50h

A microprocessor with a 26-bit address bus could access how much memory?
64MB

10. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO

CY

1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

-48

11. How many bytes are there in this short sequence of code? B815B400CD16CD20

8

12. In using INT 10h to set the video code to 640x350, what value must be in the AH register?

AX
AH|AL
00

00h

13. Moore's law has accurately predicted the growth rate in the number of transistors per die for the _____ last 25 years. What is that rate?

Doubling every 18-24 months

14. INT 21h, Function 09h requires three things set up before calling in order to correctly print a string,

Hello_msg. They are:

DS=SEG Hello_msg, DX=OFFSET Hello_msg, Hello_msg terminated with 24h

15. Given

13A7:0110 CD 20 32 20 54 68 69 73-20 69 73 20 74 68 65 20

13A7:0120 66 69 72 73 74 20 4D 69-64 75 65 72 6D 0D 24 D9

13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0112, what is the size of the buffer in decimal?

50

An input buffer is at memory location 0118, what is the size of the buffer in decimal?

32 if 0118 is 50 then answer is 80

16. Here is a short sequence of code: 7413CD16EB157D213C04EBF0A3C6. All of the instructions are two bytes long. The third instruction operator is: EB15

JMP

17. What is the hexadecimal encoding for loading DX with a word (value) from memory location 0820h?

8B162008

18. Given:

AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D OV UP EI PL NZ NA PO NC

1D72: 010D 7D09 JGE 0116

How many bytes will the processor jump if the condition for a jump were met?

9

19. F6 in 2's complement equals _____ in base 10.

-10

21. Determine the contents of register BH after the following instructions have executed:

70h MOV [0202], AX

22. Given:

AX=2247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC

1D72: 0106 EB0F JMP 0118

What will be the IP value be after a "t" command is executed in DOS Debug?

0118h

23. Which of the following DOS Debug instructions would set a break point at memory location 010E?

G=100 10E

24. Given:

0B0E: 0200 57 65 6C 63 6F 6D 65 20-74 6F 20 41 73 73 65 6D

0B0E:0210 62 6C 79 20 4C 61 6E 67-75 61 67 65 00 00 00 00

An ASCII message begins at memory location 0200, what is the message?

Welcome to Assembly Language

25. Given:

AX=FF47 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 IP=0104 NV UP EI NG NZ NA PE NC

1D72:0104 7002 JO 0118

What will the IP value be after a "t" command is executed in DOS Debug?

0106h

26. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO CY

1D72:0109 7D06 JNL 0118

What will the IP value be after a "t" command is executed in DOS Debug?

0110

27. The instruction MOV BX, 2BAD is what addressing mode?

Immediate

28. What is the hexadecimal encoding for "JNL" for a jump back 10 bytes?

7DF4

29. How many address lines would be required to address 64MB directly?

26 /2^26=67mb/

30. The number of nibbles in a double word are: 8

31. In using INT 10h to move the screen cursor to return on the same line, what value must be in the AX register?

0E0Dh

32. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D OV U EI PL NZ NA PO NC

1d72: 010D 7D09 JNLE 0118

What will the IP value be after a "t" command is executed in DOS Debug?

010Fh

33. Which of the following DOS Debug instructions would be used change the AX register?

RAX

34. How many Bytes are there in this short sequence of code? B815B400CD168A3CCD20

10

35. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY

1D72:0111 EB08 JMP 0119

What will the IP value be after a "t" command is executed in DOS Debug?

0119h

36. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI NG NZ NA PO CY
1D72: 010E 7D06 JNLE 0118

What will the IP value be after a "t" command is executed in DOS Debug?

0118

37. How many bytes decimal will the program jump for JNB instruction, given the following?

AX=0000 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1376 ES=1376 SS=1376 CS=1376 IP=0100 NV UP EI PL NZ NA PO NC
1376:0100 73E0 JNB 00E2

-32

38. Given:

AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D OV UP EI PL NZ NA PO NC
1D72: 010D 7F09 JNLE 0118

What is the decimal value of the signed number in the AX register?

-0032

39. How many address lines would be required to address 512 MB directly?

29 /2^9=29/

40. What is the hexadecimal encoding for adding BX with CX and storing the result in BX?

03CB or 03D9 -- 02CB or 02D9 - these are the only options

41. The binary number, 0111 1110, represents what values; in Hex, and as a BCD number?

7E, 7 invalid

42. In using INT 10h to set the video mode to 640 X 200, what value must be in the AX register?

0006h

43. what is the hexadecimal encoding for loading AH with a word from memory location 0520h?

8B262005 (choose 8B162005 if that is only option)

44. What is the hexadecimal encoding for "JGE" for a jump back 10 bytes?

7DF6

45. Given:

13A7:0110 CD 20 48 20 54 68 69 73-20 69 73 20 74 68 65 20
13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9
13A7:0120 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0112, what is the size of the buffer in bytes in decimal:

72

46. Given:

AX=FF47 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC
1D72:0104 7002 JNO 0118

What will the IP value be after a "t" command is executed in DOS Debug?

0118

47. Determine the contents of register AH after the following instruction have executed:

MOV BX,BA70H

MOV AX,47E7H

MOV BL, 0FH

47H

AND AL,BL

MOV [0202], AX

48. In using INT 10h to move the screen cursor to return to the beginning of the line, what value must be in the AX register?

0E0Dh

49. Given:

1376:0110 48 61 76 65 20 61 20 67-72 65 61 74 20 53 70 72

1376:0120 69 6E 67 20 52 65 63 65-73 73 21 00 00 00 00 00

An ASCII message begins at memory location 0110, what is the message?

Have a great Spring Recess!

50. You add 1+8 through a 4 bit integer unit. The state of the OF and CF flags after the ad will be:

OF=0, CF=0

Page 16

51. What is the advantage of Assembly Language over C Language?

The Assembler creates much faster executable code

52. In The X86 lab 3 Hello MASM program in the original code, what is the address of the byte used to start the number in the sequence "Hello World 0"?

020E

53. Which of the following DOS Debug instructions would set a break point at memory location 010C?

G = 100 10C

54. If CX is 0000 what will CX be after a "LOOP" instruction?

FFFF

55. Given:

AX=0353 BX=0534 CX=0000 DX=0180

DS= 1D72 IP=0109

OV UP EI PL NZ NA PO CY

1D72:0109 7D06

(OV=1,NV=1 ZR=1, NZ=0)

010B

56. How many core does the propeller microcontroller have?

8

57. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?

7DF2 (marked right) though maybe 7DF4

58. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. instructions are a word long. The third instruction operator is:

INT

59. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?

24

60. The ASCII codes for space , space, carriage return, line feed, end of string in decimal are:
32,32,13,10,36
61. A "NOP" instruction in a program will:
Perform a No Operation
62. How many address lines would be required to address 64MB directly?
26
63. What command in DEBUG would be used to execute interrupts?
P
64. What high level language is the propeller programmed in?
Spin
65. Which of the following DOS Debug instruction would be used to change the IP register to 010C?
RIP
66. The acronym PWM used in the Parrallax Propeller and MicroChip PIC18, is defined as:
Pulse Width Modulation
67. Which command would you use to execute another core in the propeller microcontroller?
Cognew
68. Given: IP=0111 NV UP EI NG NZ NA PO CY
1D72:0111 JMP 0119
What will the IP value be after a "t" command is executed in DOS Debug?
0119h
69. The instruction MOV CX, DADD is what addressing mode?
Immediate
69. The instruction MOV CX, [DADD] is what addressing mode?
Direct
70. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?
A0
71. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do with of the following?
Sets the Propeller pins P4 through P9 as output pins
72. In the Propeller microcontroller, the command "waitcnt(clkfreq*10 + cnt)" would cause the processor to do with of the following?
Create 10 second delay
73. The "LOOPNE" instruction is equivalent to which of the following instructions?
DEC CX, JNE
74. On the Arduino platform, what is the programming language used?
C

1. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string, Hello_msg.
They are:

- a. **DS = SEG Hello_msg, DX=OFFSET Hello_msg, Hello_msg terminated with 36**
Or the other option in hex is below
 - b. **DS = SEG Hello_msg, DX=OFFSET Hello_msg, Hello_msg terminated with 24h**
2. Moore's Law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate?
 - a. **Doubling every 18 – 24 months**
3. Given:


```
AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06      JNL    0118
```

What will the IP value be after a "t" command is executed in DOS Debug?

- a. **010B**
4. A "NOP" instruction will:
 - a. **Perform a No OPeration**
5. Given:


```
AX=F247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC
1D72:0106 EB0F      JMP    0118
```

What will the IP value be after a "t" command is executed in DOS Debug?

- a. **0118h**
6. In x86 architecture, BIU stands for which of the following?
 - a. **Bus Interface Unit**
7. 7. Determine the contents of register BL after the following instructions have been executed:
 - a. **2EH**

Program Listing
MOV BL, E2H
MOV CL, 100b
ROL BL, CL

8. 8. The number of bytes in a double word are:
 - a. **4**
9. In x86 architecture, ALU stands for which of the following?
 - a. **Arithmetic Logic Unit**
10. The "LOOPNE" instruction is equivalent to which of the following instructions?
 - a. **DEC CX, JNE**
11. The instruction MOV CX, DADD is what addressing mode?
 - a. **Immediate**
12. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The third instruction operator is:
 - a. **INT**
13. Which of the following DOS Debug instructions would set a break point at memory location 010C?
 - a. **G = 100 10C**
14. How many bytes are there in this short sequence of code? B400CD164CCD21
 - a. **7**
15. How many address lines would be required to address 128 MB directly?
 - a. **27**
16. What are the contents of CX after this program has been run:
 - a. **D800h**

Memory Location	Contents
1103	24
1102	D8
1101	00
1100	21

17. 1010 0110 in 2's complement equals ____ in base 10.
a. -90
18. What is the hexadecimal encoding for adding AX with BX and storing the result in AX?
a. 01D8
19. What is 11.4375₁₀ in binary?
a. 001011.01110
20. If CX is 0000 what will CX be after a "LOOP" instruction?
a. FFFF

21. Given:

AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO NC
 1D72:010F 7D08 JGE 0115

How many bytes will the processor jump if the conditions for a jump were met?

- a. 8**
22. What command in DEBUG would be used to execute interrupts?
a. P
23. What is the advantage of C Language over Assembly Language?
a. C is transportable to other microprocessor architectures
24. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:
a. OF = 1, CF = 0
25. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?
a. 36
26. Given:

AX=0353 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC
 1D72:010D 7DF6 JNL 0116

a. -10

27. AND'ing 1FH and 20H will result in which of the following?

a. 0

28. Given:

13A7:0110 CD 20 32 20 54 48 39 73-20 69 73 20 74 68 65 20
 13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9
 13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0115, what is the size of the buffer in decimal?

a. 72

29. A microprocessor with a 31-bit address bus could access how much memory?

a. 2 GB

30. For the instruction sequence below, determine the contents of the register AL after this program is executed:

a. 51H

MOV AL, 73h
 ADD AL, 78h
 DAA

31. Given:

AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
 1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

a. -256

32. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:

a. 32, 32, 13, 10, 36

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33. What is the advantage of assembly language over C language?

a. The assembler creates much faster executable code

34. How many lines would be required to address 128 mb directly?

a. 27

35. In the x86 lab part 3 Hello MASM program in the original code, what is the address of the byte used to start the number in the sequence "Hello World 0"?

a. 020E

36. Given

```

W e l c o m e _ t o _ A s s e m
0B0E:0200 57 65 6C 63 6F 6D 65 20 74 6F 20 41 73 73 65 6D
0B0E:0210 62 6C 79 20 4C 61 6E 67 75 61 67 65 00 00 00 00
b l y _ L a n g u a g e

```

An Ascii message begins at memory location 0200, what is the message?

a. Welcome to Assembly Language

37. Which of the following DOS Debug instructions would be used to change the IP register to 0110?

a. RIP

38. Moore's law has accurately predicted the growth rate in the number of transistors per ide for the last 40 years. What is the rate?

a. Doubling every 18 – 24 months

39. Which of the following DOS debug instructions would set a break point at memory location 010C?

a. G = 1000 10C

40. AND'ing 1FH and 02H will result in which of the following?

a. 02

41. If CX is 0000 what will CX be after a "LOOP" instruction?

a. FFFF

42. The number of bits in a word are:

a. 16

43. In x86 architecture, ALU stands for which of the following?

a. Arithmetic Logical Unit

44. Given:

```

AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JGE 0118

```

What will the IP value be after a "t" command is executed in DOS DEBUG?

a. 010B

45. What is the number 1010.0101₂ in decimal?

a. 10.31

46. How many cores does the propeller microcontroller have?

- a. 8
47. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?
- a. 7DF2
48. What command in debug would be used to step through a program line by line?
- a. T
49. Here is a short sequence of code: 74 13 EB A3 CD 16 7D 21 3C 04 EB F0 EB 15. All of the instructions are a word long The third instruction operator is:
- a. INT
50. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?
- a. 24

51. Given:

```
AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC
1D72:010D 7DF6 JNL 0116
```

How many bytes in decimal will the processor jump if the conditions for a jump were met?

- a. 10
52. Determine the contents of the register BL after the following instructions have been executed:

Program Listing
MOV BL, E2H
MOV CL, 08H
ROL BL, CL

- a. E2H
53. The ASCII codes for space, space, carriage return, line feed, end of string in **hex** are:
- a. 20, 20, 0D, 0A, 24
54. AND'ing 10H and 2FH will result in which of the following?
- a. 0
55. A "NOP" instruction in a program will
- a. Perform a No Operation
56. Given:

```
AX=FFF0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JGE 0118
```

What is the signed decimal value of the number in the AX register?

- a. -16
57. How many address lines would be required to address 64 MB directly?
- a. 26
58. What command in debug would be used to execute interrupts?
- a. P
59. What high level language is the propeller programmed in?
- a. Spin
60. Which of the following DOS debug instructions would be used to change the IP register to 010C?
- a. RIP
61. The acronym PWM used in Parallax Propeller and MicroChip PIC18, is defined as:
- a. Pulse width modulation
62. Which command would you use to execute another core in the propeller microcontroller?
- a. Cognew
63. Given:

```
AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY
1D72:0111 EB08 JMP 0119
```

What will the IP value be after the "t" command is executed in DOS debug?

- a. 0119h
64. The instruction MOV CD, DADD is what addressing mode?

a. Immediate

65. How many byte in decimal will the processor jump if the conditions for a jump are met?

a. 24

66. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do which of the following?

a. Sets the Propeller pins P4 through P9 as output pins

67. What are the contents of DX after this program has been run:

a. FFF0h.

```
MOV DX, 11h
MOV CX, [5512]
MOV BX, 5511h
SUB DX, [BX]
AND BX, FFFF
```

Memory location	Contents
5514	24
5513	D8
5512	00
5511	21
5510	00

68. The number of nibbles in a word are:

a. 4

What are the TTL logic level voltages for a logic 0 and logic 1?
0V to +5V

What is 152.875 converted to double precision fp?

40 63 06 00 00 00 00

What is 152.1875 converted to single precision fp?

Question 123
Answer saved
Marked out of 1
Flag question

What is 152.1875 Converted to single precision FP?

Select one:
☐ a. 3D A0 00 00
☐ b. C1 1C 80 00
☒ c. 43 18 30 00
☐ d. C2 C0 8A 3D
☐ e. 40 63 06 00
☐ f. 41 1C 00 00

69. In the Propeller microcontroller, the command "waitcnt(clkfreq*10 + cnt)" would cause the processor to do which of the following?

a. Create 10 second delay

70. The "LOOPNE" instruction is equivalent to which of the following instructions?

a. DEC CX JNE

71. Given

```
AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI NG NZ NA PO NC
1D72:010D EB07 JMP 0114
```

How many bytes will the processor jump if the conditions for a jump were met?

a. 7

72. On the Arduino platform, what is the programming language used?

a. C

73. What is the hexadecimal encoding for loading AX with a word (value) from memory location 0820h?

a. A12008

74. Which of the following would be used to set the TRISA register to control the direction of PIC18 Port to input?

a. 1

75. The acronym ADC in microcontrollers stands for which of the following?

a. Analog to Digital converter

76. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

- a. $OF = 1, CF = 0$

Compare and contrast the Harvard architecture with the von Neumann

Question 99

Answer saved

Marked out of 1

Flag question

Compare and contrast the Harvard architecture with the von Neumann architecture.

Select one:

- ☐ a. The von Neumann architecture uses big endian addressing.
- ☐ b. The Harvard architecture uses programmed I/O.
- ☐ c. The von Neumann architecture uses separate program and data memory.
- ☒ d. The Harvard type uses a separate program and data memory
- ☐ e. The Harvard architecture uses big endian addressing.

von Neumann vs. Harvard

- von Neumann
 - Same memory holds data, instructions.
 - A single set of address/data buses between CPU and memory
- Harvard
 - Separate memories for data and instructions.
 - Two sets of address/data buses between CPU and memory

Chenyang Lu

CSE 467S

4

S2010 MT2

1. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 127?
a. 7
2. If CX is 0003, what will CX be after a "LOOP" instruction?
a. 0002
3. IN the Propeller microcontroller, the command "waitcnt(clkfreq*5 + cnt)" would cause the processor to do which of the following?
a. A 5 second delay
4. What is the number 1011.0101_2 in decimal?
a. 11.31
5. This section of memory represents a stack. What type of program is this?
BEEF : FFD0 00 00 00 00 00 00 00-00 00 00 00 00 00 00
BEEF : FFE0 00 01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F
BEEF : FFF0 11 22 33 44 55 66 77 88-99 AA BB CC DD EE FF
a. COM program

6. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack?
 - a. **BDCA**
7. Determine the contents of register BL after the following instruction have been executed:
 - a. **2EH**

Program Listing
MOV BL, 2E
MOV CL, 10
ROL BL, CL
8. What Hex values must be sent to address the key pad rows on the PPE board?
 - a. **1, 2, 4, 8**
9. What is the number 32.4375_{10} in binary?
 - a. **100000.01110**
10. In MASM, with a “MOV CX, 18h” instruction, and a “LOOP” instruction, in decimal how many times will the program loop?
 - a. **24**
11. The acronym PWM used for motor control, is defined as which of the following?
 - a. **Pulse Width Modulation**
12. Given the short code, what is the value in AX after the program is run?
 - a. **0500**

Program Listing
MOV BX, 0500
Push BX
MOV AX, 0100
POP AX
13. What flag(s) does the “LOOPNZ” instruction look at to determine whether to loop or not?
 - a. **ZF**
14. How many nibbles are in double precision IEEE floating point format number?
 - a. **16**
15. What type of program is this?

AX=0000 BX=0000 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1476 ES=1576 SS=1376 CS=1D72 IP=0015 NV UP EI PL NZ NA PO NC
1376:0015 0100 ADD [BI + SI], AL DS : 0000=CD

 - a. **EXE**
16. If the SP is F00F, what is the SP value after a “PUSH CX” instruction?
 - a. **F00D**
17. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?
 - a. **A0**
18. What is the numeric sequence of the key pad columns on the PPE board?
 - a. **37, 2F, 1F**
19. What is -130 decimal in 2’s complement (8bits)?
 - a. **01111110**
20. Which of the following is a valid x86 command for multiplying a number?
 - a. **MUL BX**
21. The number of bits in single precision IEEE floating point format are:
 - a. **32**
22. You are trying to rebuild a HELLO project program in MASM and you get the following error:

"LINK : warning L4021: no stack segment".

What would be the reason for such an error?

a. No project template for COM was selected

23. A "pull down" resistor is used in digital circuits to do what?

a. To keep the signal line "tied" low until the line is active (goes high)

24. A "POP" instruction:

a. increments the SP

25. AND'ing 10H and 2FH will result in which of the following?

a. 0

26. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do which of the following?

a. Sets the Propeller pins P4 through P9 as output pins

27. What commands in MASM-CodeView would be used to step through a program line by line?

a. T (F8)

28. If the SP is F00F, what will the SP value be after a "POP SP" instruction?

a. F011

29. On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh?

a. 0

30. In the Propeller microcontroller, the term "Method" is(are) which of the following?

a. A block of executable commands that has variables, can receive parameters, and returns a value.

31. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal?

a. MOV CX, 30H or MOV CX, 48

32. A "pull up" resistor is used in digital circuits to do what?

a. To keep the signal "tied" high until the line is active (goes low)

33. With a PUSHA instruction, what will be the order of the register (registers A ~ D) contents on the stack?

a. ACDB

34. The LOOPNE instruction performs which of the following?

a. Decrements CX, tests the ZF flag, if it is not zero jumps to address specified.

35. What must the value be and in what register, prior to executing a LOOPNE instruction, to discontinue looping?

a. CX =1

36. With a POPA instruction, what will be the order of the registers (registers A~ D) restored from stack?

a. BDCA

37. What flags does the "LOOPNZ" instruction look at to determine whether to loop or not?

a. ZF

38. If the SP is F00F, what is the SP value after a "POP BX" instruction:

a. F011

39. If CX is 0001, what will CX be after a "LOOPNZ" instruction:

a. 0000

40. -11.25 in decimal converted to binary would be:

a. -1011.0100

41. What is 31.4375 in binary?

a. 011111.0111

42. Using debug which command should be used to change the flag setting?

a. RF

43. What is 14.4375 in binary?

a. 001110.01110

EEE 174 Midterm Study Guide

S2010 MT 1

2. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string, Hello_msg. They are:

b. **DS = SEG Hello_msg, DX=OFFSET Hello_msg, Hello_msg terminated with 36**

77. Moore's Law has accurately predicted the growth rate in the number of transistors per die for the last 40 years.

What is the rate?

a. **Doubling every 18 – 24 months**

78. Given:

```
AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06      JNL    0118
```

What will the IP value be after a "t" command is executed in DOS Debug?

a. **010B**

79. A "NOP" instruction will:

a. **Perform a No Operation**

80. Given:

```
AX=F247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC
1D72:0106 EB0F      JMP    0118
```

What will the IP value be after a "t" command is executed in DOS Debug?

a. **0118h**

81. In x86 architecture, BIU stands for which of the following?

a. **Bus Interface Unit**

82. 7. Determine the contents of register BL after the following instructions have been executed:

a. **2EH**

Program Listing
MOV BL, E2H
MOV CL, 100b
ROL BL, CL

83. 8. The number of bytes in a double word are:

a. **4**

84. In x86 architecture, ALU stands for which of the following?

a. **Arithmetic Logic Unit**

85. The "LOOPNE" instruction is equivalent to which of the following instructions?

a. **DEC CX, JNE**

86. The instruction MOV CX, DADD is what addressing mode?

a. **Immediate**

87. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The third instruction operator is:

a. **INT**

88. Which of the following DOS Debug instructions would set a break point at memory location 010C?

a. **G = 100 10C**

89. How many bytes are there in this short sequence of code? B400CD164CCD21

a. **7**

11. How many bytes are there in this short sequence of code? B400CD16CD20

a. **6**

90. How many address lines would be required to address 128 MB directly?

a. **27**

91. What are the contents of CX after this program has been run:

a. **D800h**

Memory Location	Contents
1103	24
1102	D8
1101	00
1100	21

92. 1010 0110 in 2's complement equals ____ in base 10.

a. **-90**

93. What is the hexadecimal encoding for adding AX with BX and storing the result in AX?

a. **01D8**

94. What is 11.4375_{10} in binary?

a. **001011.01110**

95. If CX is 0000 what will CX be after a "LOOP" instruction?

a. **FFFF**

96. Given:

AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO NC
1D72:010F 7D08 JGE 0115

How many bytes will the processor jump if the conditions for a jump were met?

a. **8**

97. What command in DEBUG would be used to execute interrupts?

a. **P**

98. What is the advantage of C Language over Assembly Language?

a. **C is transportable to other microprocessor architectures**

99. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

a. **OF = 1, CF = 0**

100. In MASM, with a "MOV CX, 24h" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?

a. **36**

101. Given:

AX=0353 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC
1D72:010D 7DF6 JNL 0116

a. **-10**

102. AND'ing 1FH and 20H will result in which of the following?

a. **0**

103. Given:

13A7:0110 CD 20 32 20 54 48 39 73-20 69 73 20 74 68 65 20
13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9
13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0115, what is the size of the buffer in decimal?

a. **72**

104. A microprocessor with a 31-bit address bus could access how much memory?

a. **2 GB**

105. For the instruction sequence below, determine the contents of the register AL after this program is executed:
- Program listing
MOV AL, 73h
ADD AL, 78h
DAA
- a. **51H**

106. Given:
AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

- a. **-256**
107. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:
a. **32, 32, 13, 10, 36**

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108. What is the advantage of assembly language over C language?
a. The assembler creates much faster executable code
109. How many lines would be required to address 128 mb directly?
a. 27
110. In the x86 lab part 3 Hello MASM program in the original code, what is the address of the byte used to start the number in the sequence "Hello World 0"?
a. 020E
111. Given
- ```

W e l c o m e _ t o _ A s s e m
0B0E:0200 57 65 6C 63 6F 6D 65 20-74 6F 20 41 73 73 65 6D
0B0E:0210 62 6C 79 20 4C 61 6E 67-75 61 67 65 00 00 00 00
b l y _ L a n g u a g e

```
- An Ascii message begins at memory location 0200, what is the message?
- a. Welcome to Assembly Language
112. Which of the following DOS Debug instructions would be used to change the IP register to 0110?  
a. RIP
113. Moore's law has accurately predicted the growth rate in the number of transistors per ide for the last 40 years. What is the rate?  
a. Doubling every 18 – 24 months
114. Which of the following DOS debug instructions would set a break point at memory location 010C?  
a. G = 1000 10C
115. AND'ing 1FH and 02H will result in which of the following?  
a. 02
116. If CX is 0000 what will CX be after a "LOOP" instruction?  
a. FFFF
117. The number of bits in a word are:  
a. 16
118. In x86 architecture, ALU stands for which of the following?  
a. Arithmetic Logical Unit
119. Given:

AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
1D72:0109 7D06 JGE 0118 1 0

What will the IP value be after a “t” command is executed in DOS DEBUG?

- a. 010B
120. What is the number  $1010.0101_2$  in decimal?
- a. 10.31
121. How many cores does the propeller microcontroller have?
- a. 8
122. What is the hexadecimal encoding for “JGE” for a jump back 12 bytes?
- a. 7DF2
123. What command in debug would be used to step through a program line by line?
- a. T
124. Here is a short sequence of code: 74 13 EB A3 CD 16 7D 21 3C 04 EB F0 EB 15. All of the instructions are a word long The third instruction operator is:
- a. INT
125. In MASM, with a “MOV CX, 24” instruction, and a “LOOP” instruction, in decimal how many times will the program loop?
- a. 24

126. Given:

AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC  
1D72:010D 7DF6 JNL 0116 0 0 0 0

How many bytes in decimal will the processor jump if the conditions for a jump were met?

- a. 10
127. Determine the contents of the register BL after the instructions have been executed:

| Program Listing |
|-----------------|
| MOV BL, E2H     |
| MOV CL, 08H     |
| ROL BL, CL      |

following

- a. E2H
128. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:
- a. 20, 20, 0D, 0A, 24
129. AND’ing 10H and 2FH will result in which of the following?
- a. 0
130. A “NOP” instruction in a program will
- a. Perform a No Operation

131. Given:

AX=FFF0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
1D72:0109 7D06 JGE 0118

What is the signed decimal value of the number in the AX register?

- a. -16
132. How many address lines would be required to address 64 MB directly?
- a. 26
133. What command in debug would be used to execute interrupts?
- a. P
134. What high level language is the propeller programmed in?
- a. Spin
135. Which of the following DOS debug instructions would be used to change the IP register to 010C?
- a. RIP
136. The acronym PWM used in Parallax Propeller and MicroChip PIC18, is defined as:
- a. Pulse width modulation



137. Which command would you use to execute another core in the propeller microcontroller?

- a. Cognew

138. Given:

```
AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY
1D72:0111 EB08 JMP 0119
```

What will the IP value be after the “t” command is executed in DOS debug?

- a. 0119h

139. The instruction MOV CD, DADD is what addressing mode?

- a. Immediate

140. How many byte in decimal will the processor jump if the conditions for a jump are met?

- a. 24

141. In the Propeller microcontroller, the command “dira[4..9] := %111111” would cause the processor to do which of the following?

- a. Sets the Propeller pins P4 through P9 as output pins

142. What are the contents of DX after this

program has been run:

- a. FFF0h.

```
MOV DX, 11h
MOV CX, [5512]
MOV BX, 5511h
SUB DX, [BX]
AND BX, FFFF
```

| Memory location | Contents |
|-----------------|----------|
| 5514            | 24       |
| 5513            | D8       |
| 5512            | 00       |
| 5511            | 21       |
| 5510            | 00       |

143. The number of nibbles in a word are:

- a. 4

144. In the Propeller microcontroller, the command “waitcnt(clkfreq\*10 + cnt)” would cause the processor to do which of the following?

- a. Create 10 second delay

145. The “PPE board E” instruction is equivalent to which of the following instructions?

- a. DEC CX JNE

146. Given

```
AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI NG NZ NA PO NC
1D72:010D EB07 JMP 0114
```

How many bytes will the processor jump if the conditions for a jump were met?

- a. 7

147. On the Arduino platform, what is the programming language used?

- a. C

148. What is the hexadecimal encoding for loading AX with a word (value) from memory location 0820h?

- a. A12008

149. Which of the following would be used to set the TRISA register to control the direction of PIC18 Port to input?

- a. 1

150. The acronym ADC in microcontrollers stands for which of the following?

- a. Analog to Digital converter

151. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

- a. OF = 1, CF = 0

## S2010 MT2

2. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 127?  
**b. 7**
44. If CX is 0003, what will CX be after a "LOOP" instruction?  
**a. 0002**
45. IN the Propeller microcontroller, the command "waitcnt(clkfreq\*5 + cnt)" would cause the processor to do which of the following?  
**a. A 5 second delay**
46. What is the number  $1011.0101_2$  in decimal?  
**a. 11.31**
47. This section of memory represents a stack. What type of program is this?  
BEEF : FFD0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00  
BEEF : FFE0 00 01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F  
BEEF : FFF0 11 22 33 44 55 66 77 88-99 AA BB CC DD EE FF  
**a. COM program**
48. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack?  
**a. BDCA**
49. Determine the contents of register BL after the following instruction have been executed:  
**a. 2EH**
- Program Listing  
MOV BL, 2E  
MOV CL, 10  
ROL BL, CL
50. What Hex values must be sent to address the key pad rows on the PPE board?  
**a. 1, 2, 4, 8**
51. What is the number  $32.4375_{10}$  in binary?  
**a. 100000.01110**
52. In MASM, with a "MOV CX, 18h" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?  
**a. 24**
53. The acronym PWM used for motor control, is defined as which of the following?  
**a. Pulse Width Modulation**
54. In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?  
**a. Bit 7 of port D is set to output**
55. Given the short code, what is the value in AX after the program is run?  
**a. 0500**
- Program Listing  
MOV BX, 0500  
Push BX  
MOV AX, 0100  
POP AX
56. What flag(s) does the "LOOPNZ" instruction look at to determine whether to loop or not?  
**a. ZF**
57. How many nibbles are in double precision IEEE floating point format number?  
**a. 16**

58. What type of program is this?  
 AX=0000 BX=0000 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1476 ES=1576 SS=1376 CS=1D72 IP=0015 NV UP EI PL NZ NA PO NC  
 1376:0015 0100          ADD    [BI + SI], AL                          DS : 0000=CD  
 a. **EXE**
59. If the SP is F00F, what is the SP value after a "PUSH CX" instruction?  
 a. **F00D**
60. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?  
 a. **A0**
61. What is the numeric sequence of the key pad columns on the PPE board?  
 a. **37, 2F, 1F**
62. What is -130 decimal in 2's complement (8bits)?  
 a. **01111110**
63. Which of the following is a valid x86 command for multiplying a number?  
 a. **MUL BX**
64. The number of bits in single precision IEEE floating point format are:  
 a. **32**
65. You are trying to rebuild a HELLO project program in MASM and you get the following error:  
 "LINK : warning L4021: no stack segment".  
 What would be the reason for such an error?  
 a. **No project template for COM was selected**
66. A "pull down" resistor is used in digital circuits to do what?  
 a. **To keep the signal line "tied" low until the line is active (goes high)**
67. A "POP" instruction:  
 a. **increments the SP**
68. AND'ing 10H and 2FH will result in which of the following?  
 a. **0**
69. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do which of the following?  
 a. **Sets the Propeller pins P4 through P9 as output pins**
70. What commands in MASM-CodeView would be used to step through a program line by line?  
 a. **T (F8)**
71. If the SP is F00F, what will the SP value be after a "POP SP" instruction?  
 a. **F011**
72. On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh?  
 a. **0**
73. In the Propeller microcontroller, the term "Method" is(are) which of the following?  
 a. **A block of executable commands that has variables, can receive parameters, and returns a value.**
74. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal?  
 a. **MOV CX, 30H          or          MOV CX, 48**
75. A "pull up" resistor is used in digital circuits to do what?  
 a. **To keep the signal "tied" high until the line is active (goes low)**

## S2010 MT 1

1. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string, Hello\_msg. They are:

**DS = SEG Hello\_msg, DX=OFFSET Hello\_msg, Hello\_msg terminated with 36**

2. Moore's Law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate?

**Doubling every 18 – 24 months**

3. Given:

AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY

1D72:0109 7D06 JNL 0118

What will the IP value be after a "t" command is executed in DOS Debu range of decimal numbers range of decimal numbers g?

**010B**

4. A "NOP" instruction will:

**Perform a No OPeration**

5. Given:

AX=F247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC

1D72:0106 EB0F JMP 0118

What will the IP value be after a "t" command is executed in DOS Debug?

**0118h**

6. In x86 architecture, BIU stands for which of the following?

**Bus Interface Unit**

7. Determine the contents of register BL after the following instructions have been executed:

**2EH**

Program Listing  
MOV BL, E2H  
MOV CL, 100b  
ROL BL, CL

8. The number of bytes in a double word are:

**4**

9. In x86 architecture, ALU stands for which of the following?

**Arithmetic Logic Unit**

10. The "LOOPNE" instruction is equivalent to which of the following instructions?

**DEC CX, JNE**

11. The instruction MOV CX, DADD is what addressing mode?

**Immediate**

12. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The third instruction operator is:

**INT 16**

13. Which of the following DOS Debug instructions would set a break point at memory location 010C?

**G = 100 10C**

14. How many bytes are there in this short sequence of code? B400CD164CCD21

**7**

15. How many address lines would be required to address 128 MB directly?

**27**

| Memory Location | Contents |
|-----------------|----------|
| 1103            | 24       |
| 1102            | D8       |
| 1101            | 00       |
| 1100            | 21       |

16. What are the contents of CX after this program has been run:

**D800h**

17. 1010 0110 in 2's complement equals \_\_\_\_\_ in base 10.

**-90**

18. What is the hexadecimal encoding for adding AX with BX and storing the result in AX?

**01D8**

19. What is 11.4375<sub>10</sub> in binary?

**001011.01110**

20. If CX is 0000 what will CX be after a “LOOP” instruction?

**FFFF**

22. Given:

AX=FFEO BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO NC  
1D72:010F 7D08 JGE 0115

How many bytes will the processor jump if the conditions for a jump were met?

**8**

23. What command in DEBUG would be used to execute interrupts?

**P**

24. What is the advantage of C Language over Assembly Language?

**C is transportable to other microprocessor architectures**

25. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

**OF = 1, CF = 0**

26. In MASM, with a “MOV CX, 24” instruction, and a “LOOP” instruction, in decimal how many times will the program loop?

**36**

27. Given:

AX=0353 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC  
1D72:010D 7DF6 JNL 0116

**-10**

28. AND'ing 1FH and 20H will result in which of the following?

**0**

29. Given:

13A7:0110 CD 20 32 20 54 48 39 73-20 69 73 20 74 68 65 20  
13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9  
13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0115, what is the size of the buffer in decimal?

**72**

30. A microprocessor with a 31-bit address bus could access how much memory?

**2 GB**

31. For the instruction sequence below, determine the contents of the register AL after this program is executed:

**51H**

32. Given:

AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

**-256**

33. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:

**32, 32, 13, 10, 36**

Program Listing  
MOV AL, 73h  
ADD AL, 78h  
DAA

## S2010 MT2

1. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 127?

**7**

2. If CX is 0003, what will CX be after a “LOOP” instruction?

**0002**

3. IN the Propeller microcontroller, the command “waitcnt(clkfreq\*5 + cnt)” would cause the processor to do which of the following?

**A 5 second delay**

4. What is the number  $1011.0101_2$  in decimal?

**11.31**

5. This section of memory represents a stack. What type of program is this?

BEEF : FFD0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00

BEEF : FFE0 00 01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F

BEEF : FFF0 11 22 33 44 55 66 77 88-99 AA BB CC DD EE FF

**COM program**

6. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack?

**BDCA**

7. Determine the contents of register BL after the following instruction have been executed:

**2EH**

Program Listing  
MOV BL, 2E  
MOV CL, 10  
ROL BL, CL

8. What Hex values must be sent to address the key pad rows on the PPE board?

**1, 2, 4, 8**

9. What is the number  $32.4375_{10}$  in binary?

**100000.01110**

10. In MASM, with a “MOV CX, 18h” instruction, and a “LOOP” instruction, in decimal how many times will the program loop?

**24**

11. The acronym PWM used for motor control, is defined as which of the following?

**Pulse Width Modulation**

12. In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?

**Bit 7 of port D is set to output**

13. Given the short code, what is the value in AX after the program is run?

**0500**

Program Listing  
MOV BX, 0500  
Push BX  
MOV AX, 0100  
POP AX

14. What flag(s) does the “LOOPNZ” instruction look at to determine whether to loop or not?

**ZF**

15. How many nibbles are in double precision IEEE floating point format number?

**16**

17. What type of program is this?

AX=0000 BX=0000 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1476 ES=1576 SS=1376 CS=1D72 IP=0015 NV UP EI PL NZ NA PO NC

1376:0015 0100 ADD [BI + SI], AL DS : 0000=CD

**EXE**

18. If the SP is F00F, what is the SP value after a “PUSH CX” instruction?

**F00D**

19. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

**A0**

20. What is the numeric sequence of the key pad columns on the PPE board?

**37, 2F, 1F**

21. What is -130 decimal in 2's complement (8bits)?

**01111110**

22. Which of the following is a valid x86 command for multiplying a number?

## MUL BX

23. The number of bits in single precision IEEE floating point format are:

**32**

24. You are trying to rebuild a HELLO project program in MASM and you get the following error: "LINK : warning L4021: no stack segment".

What would be the reason for such an error?

**No project template for COM was selected**

25. A "pull down" resistor is used in digital circuits to do what?

**To keep the signal line "tied" low until the line is active (goes high)**

26. A "POP" instruction:

**increments the SP**

27. AND'ing 10H and 2FH will result in which of the following?

**0**

28. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do which of the following?

**Sets the Propeller pins P4 through P9 as output pins**

29. What commands in MASM-CodeView would be used to step through a program line by line?

**T (F8)**

30. If the SP is F00F, what will the SP value be after a "POP SP" instruction?

**F011**

31. On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh?

**0**

32. In the Propeller microcontroller, the term "Method" is(are) which of the following?

**A block of executable commands that has variables, can receive parameters, and returns a value.**

33. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal?

**MOV CX, 30H or MOV CX, 48**

34. A "pull up" resistor is used in digital circuits to do what?

**To keep the signal "tied" high until the line is active (goes low)**

How many bit(s) is/are required to represent a range of decimal numbers from 0 to 9?

a) 4

A "POP" instruction:

a) increments the Stack Pointer, SP

The instruction MOV CX, SI is what addressing mode?

a) Register Addressing - direct

The instruction MOV CX, [SI] is what addressing mode?

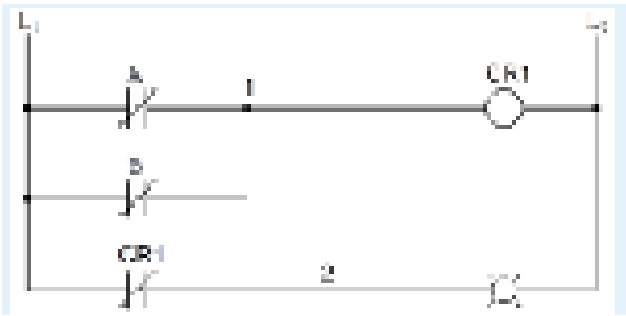
a) Register Indirect

Which of the following will cause a program with a LOOP instruction to loop 48 times (decimal)?

**a) CX = 30h**

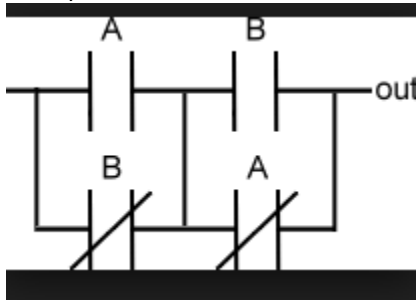
A "PUSH" instruction:

a) Decrements the SP/increments the IP



The Ladder Logic diagram would represent which of the following?

a) NOR



XNOR

If you want to use a INT software interrupt function to print a string out to the screen, what is the function code, start pointer, termination character, and interrupt you need to use?

Select one:

a) ah = 09h, ds:dx, "\$", 21h

You are trying to rebuild a HELLO project program in MASM and you get the following error: "LINK: fatal error L1089: HELLO.lrf: cannot open response file".

What would be the reason for such an error?

Select one:

a) No source file is identified (no.asm file)

The instruction MOV SI, 4DAD is what addressing mode?

Select one:

a) Immediate

How many nibble are in double precision IEEE floating point format numbers (64bit)?

Select one:

a) 16

What flag(s) does the "LOOPNE" instruction look at to determine whether to loop or not?

a) ZF

What is the numeric sequence of the key pad columns on the PPE board?

Select one:



a) 37,2F,1F

18 hexadecimal would be what value in decimal?

a) 24

In the PIC 18 with TRISD = 0b01000000, what is the configuration of the Port D?

Select one:

a) Bit 6 of port D is set to input

In the PIC 18 with TRISD = 0b10000000, what is the configuration of the Port D?

Select one:

a) Bit 7 of port D is set to input

What register(s) does the "LOOPNE" instruction look at to determine how many times to loop?

Select one:

a) CX

If CX is 0003, what will CX be after a "LOOPNZ" instruction?

Select one:

a) 0002

What type of program is this?

```
AX=0000 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1476 ES=1576 SS=1676 CS=1376 IP=0015 MV UP EI PL NZ NA PO NC
1376:0015 0100 ADD [BX+SI],AL DS:0000=CD
```

Select one:

a) EXE program

Determine the content of register BL after the following instruction have been executed:

a) E2H

```
MOV BL, 2EH
MOV CL, 0100b
ROL BL, CL
```

## OTHER

A "pull down" resistor is used in digital circuits to do what?

b) To keep the signal line "tied" low until the line is active (goes high)

A “pull up” resistor is used in digital circuits to do what?

- c) To keep the signal “tied” high until the line is active (goes low)

Double precision IEEE FP standard uses \_\_\_\_\_ nibbles to represent data:

- a) 16

How many bytes are in double precision IEEE floating point format numbers?

- b) 8

You are trying to rebuild a Hello project program in MASM and you get the following error: “LINK : warning L4021: no stack segment”.

- c) No project template for COM was selected

How many double words are in double IEEE floating point format numbers?

- d) 16

In x86 architecture, BIU stands for which of the following?

- e) Bus Interface Unit

Moore’s law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is that rate?

- b) Doubling every 18 – 24 months

NMI stands for what?

- a) Non Maskable Interrupt – it means it cannot be blocked

The letters “NC” labeled on relays and PLCs means which of the following?

- b) Normally Closed

The number of bits in single precision IEEE floating point format are:

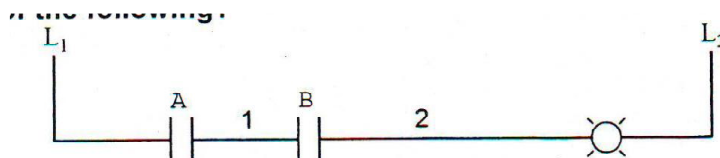
- c) 32

The number of bytes in extended precision IEEE floating point format are:

- b) 10

The Ladder Logic diagram would represent which of the following?

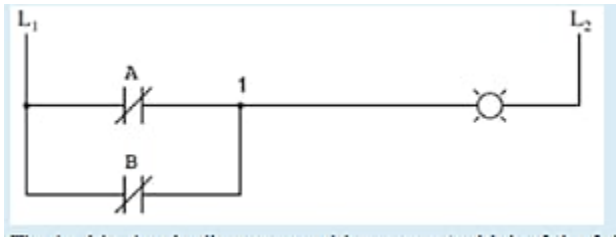
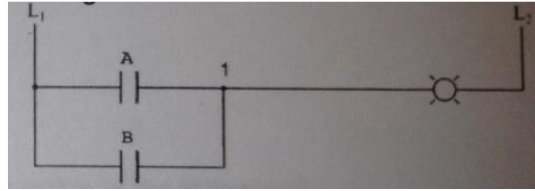
- a) XOR
- b) OR
- c) NAND
- d) AND



e) OPEN CIRCUIT

The Ladder Logic diagram would represent which of the following?

- a) XOR
- b) OR
- c) NAND
- d) AND
- e) OPEN CIRCUIT



NOR

What is the decimal value of C1 1C 00 00 in IEEE single precision FP format?

- d) -9.75

What is the advantage of Assembly Language over C Language?

- a) The assembler creates much faster executable code

What is(are) the advantage(s) of C Language over Assembly Language?

- a) C is transportable to other microprocessor architectures

What is the decimal value of C5 5A 57 00 in IEEE single precision FP format?

- b) -3493.4375

PPE

On the PPE board, what number(s) on the key pad is(are) processed for an output port value of 08h and an input port value of 2Fh?

- a) 0

On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 01h and an input port value of 1Fh?

- b) 3

On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 02h and an input port value of 2fh?

- e) 5

On the PPE board, what number(s) on the key pad is(are) pressed for an output port of 04h and an input port value of 2Fh?

c) 8

What Hex values must be sent to address the key pad rows on the PPE board?

c) 1,2,4,8

On the raspberry pi platform, what is the programming language used?

Various open source languages

What is the numeric sequence of the key pad columns on the PPE board?

d) 37, 2F, 1F

What is the numeric sequence to address the key pad rows on the PPE board used in the lab?

a) 1,2,4,8

### Arduino

Which of the following instruction would be used to set the LED to light on the Arduino Platform?

e) LED = 1

On the Arduino platform, what is the programming language used?

d) C

### Microchip PICKIT

How many bits does the PIC 18 microcontroller used in the PICit 3 Debug Express have?

b) 16

In the PIC18 with TRISD = 0b10000000, what is the configuration of the Port D?

a) Bit 7 of port D is set to input

In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

b) A0

In the PIC18 with TRISD = 0b11110000 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

e) 0A

In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?

c) Bit 7 of port D is set to output

Which of the following would be used to set the TRISA register to control the direction of the PIC18 Port to input?

d) 1

A "POPstruction:  
Increments the SP

With a "POPAX" instruction, what will the order of the accumulator, base, count, and data registers restored from the stack?

ax

DAS used for BCD operation stand for which of the following?  
Decimal adjust for subtraction

What is -1011.0101 in decimal?  
-11.31

what is -32.75 in binary?  
-100000.11000

what command in debug would be used to change the IP value?  
RIP

What is -130 decimal in 2's complement (8 bits)?  
01111110

what is the decimal value of C5 5A 57 00 in IEEE single precision FP format?  
-3493.4375

What is -34 decimal in 2's complement?  
1101 1110

EEE 174 Midterm Study Guide  
S2010 MT 1

1. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string, Hello\_msg. They are:
  - **DS = SEG Hello\_msg, DX=OFFSET Hello\_msg, Hello\_msg terminated with 36**
152. Moore's Law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate?
  - **Doubling every 18 – 24 months**
153. Given:  
AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
1D72:0109 7D06 JNL 0118

What will the IP value be after a “t” command is executed in DOS Debug?

- **010B**

154. A “NOP” instruction will:

- **Perform a No Operation**

155. Given:

AX=F247 BX=0000 CX=0000 DX=0000 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0106 NV UP EI NG NZ NA PE NC  
1D72:0106 EB0F JMP 0118

What will the IP value be after a “t” command is executed in DOS Debug?

- **0118h**

156. In x86 architecture, BIU stands for which of the following?

- **Bus Interface Unit**

157. 7. Determine the contents of register BL after the following instructions have been executed:

- **2EH**

Program Listing  
MOV BL, E2H  
MOV CL, 100b  
ROL BL, CL

158. 8. The number of bytes in a double word are:

- **4**

159. In x86 architecture, ALU stands for which of the following?

- **Arithmetic Logic Unit**

160. The “LOOPNE” instruction is equivalent to which of the following instructions?

- **DEC CX, JNE**

161. The instruction MOV CX, DADD is what addressing mode?

- **Immediate**

162. Here is a short sequence of code: 7413EBA3CD167D213C04EBF0EB15. All of the instructions are a word long. The third instruction operator is:

- **INT**

163. Which of the following DOS Debug instructions would set a break point at memory location 010C?

- **G = 100 10C**

164. How many bytes are there in this short sequence of code? B400CD164CCD21

- **7**

165. How many address lines would be required to address 128 MB directly?

- **27**

166. What are the contents of CX after this program has been run:

- **D800h**

| Memory Location | Contents |
|-----------------|----------|
| 1103            | 24       |
| 1102            | D8       |
| 1101            | 00       |
| 1100            | 21       |

167. 1010 0110 in 2’s complement equals\_\_\_\_\_ in base 10.

- **-90**

168. What is the hexadecimal encoding for adding AX with BX and storing the result in AX?

- **01D8**

169. What is 11.4375<sub>10</sub> in binary?

- **001011.01110**

170. If CX is 0000 what will CX be after a “LOOP” instruction?

- **FFFF**

171. Given:

AX=FFEO BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010E OV UP EI PL NZ NA PO NC  
1D72:010F 7D08 JGE 0115

How many bytes will the processor jump if the conditions for a jump were met?

. **8**

172. What command in DEBUG would be used to execute interrupts?

. **P**

173. What is the advantage of C Language over Assembly Language?

. **C is transportable to other microprocessor architectures**

174. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

. **OF = 1, CF = 0**

175. In MASM, with a "MOV CX, 36" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?

. **36**

176. Given:

AX=0353 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC  
1D72:010D 7DF6 JNL 0116

. **-10**

177. AND'ing 1FH and 20H will result in which of the following?

. **0**

178. Given:

13A7:0110 CD 20 32 20 54 48 39 73-20 69 73 20 74 68 65 20  
13A7:0120 66 69 72 73 74 20 4D 69-64 74 65 72 6D 0D 24 D9  
13A7:0130 00 C6 00 00 00 00 00 00-00 00 00 00 00 00 00

An input buffer is at memory location 0115, what is the size of the buffer in decimal?

. **72**

An input buffer is at memory location 0114, what is the size of the buffer in decimal?

a. **84**

179. A microprocessor with a 31-bit address bus could access how much memory?

. **2 GB**

180. For the instruction sequence below, determine the contents of the register AL after this program is executed:

MOV AL, 73h  
ADD AL, 78h  
DAA

. **51H**

181. Given:

AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

. **-256**

182. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:

. **32, 32, 13, 10, 36**

183. What is the advantage of assembly language over C language?
- . The assembler creates much faster executable code
184. How many lines would be required to address 128 mb directly?
- . 27
185. In the x86 lab part 3 Hello MASM program in the original code, what is the address of the byte used to start the number in the sequence "Hello World 0"?
- . 020E
186. Given
- ```

W e l c o m e _ t o _ A s s e m
0B0E:0200 57 65 6C 63 6F 6D 65 20-74 6F 20 41 73 73 65 6D
0B0E:0210 62 6C 79 20 4C 61 6E 67-75 61 67 65 00 00 00 00
b l y _ L a n g u a g e

```
- An Ascii message begins at memory location 0200, what is the message?
- . Welcome to Assembly Language
187. Which of the following DOS Debug instructions would be used to change the IP register to 0110?
- . RIP
188. Moore's law has accurately predicted the growth rate in the number of transistors per ide for the last 40 years. What is the rate?
- . Doubling every 18 – 24 months
189. Which of the following DOS debug instructions would set a break point at memory location 010C?
- . G = 1000 10C
190. AND'ing 1FH and 02H will result in which of the following?
- . 02
191. If CX is 0000 what will CX be after a "LOOP" instruction?
- . FFFF
192. The number of bits in a word are:
- . 16
193. The number of bits in a double word are:
- . 32
194. In x86 architecture, ALU stands for which of the following?
- . Arithmetic Logical Unit
195. Given:
- ```

AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JGE 0118 1 0 0

```
- What will the IP value be after a "t" command is executed in DOS DEBUG?
- . 010B
196. What is the number  $1010.0101_2$  in decimal?
- . 10.31
197. How many cores does the propeller microcontroller have?
- . 8
198. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?
- . 7DF2
199. What command in debug would be used to step through a program line by line?
- . T
200. Here is a short sequence of code: 74 13 EB A3 CD 16 7D 21 3C 04 EB F0 EB 15. All of the instructions are a word long The third instruction operator is:
- . INT



201. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?

- 24

202. Given:

```
AX=FF00 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC
1D72:010D 7DF6 JNL 0116
```

How many bytes in decimal will the processor jump if the conditions for a jump were met?

- 10

203. Determine the contents of the register BL after the instructions have been executed:

| Program Listing |
|-----------------|
| MOV BL, E2H     |
| MOV CL, 08H     |
| ROL BL, CL      |

following

- E2H

204. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:

- 20, 20, 0D, 0A, 24

205. AND'ing 10H and 2FH will result in which of the following?

- 0

206. A "NOP" instruction in a program will

- Perform a No Operation

207. Given:

```
AX=FFF0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY
1D72:0109 7D06 JGE 0118
```

What is the signed decimal value of the number in the AX register?

- 16

208. How many address lines would be required to address 64 MB directly?

- 26

209. What command in debug would be used to execute interrupts?

- P

210. What high level language is the propeller programmed in?

- Spin

211. Which of the following DOS debug instructions would be used to change the IP register to 010C?

- RIP

212. The acronym PWM used in Parallax Propeller and MicroChip PIC18, is defined as:

- Pulse width modulation

213. Which command would you use to execute another core in the propeller microcontroller?

- Cognew

214. Given:

```
AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY
1D72:0111 EB08 JMP 0119
```

What will the IP value be after the "t" command is executed in DOS debug?

- 0119h

215. The instruction MOV CD, DADD is what addressing mode?

- Immediate

216. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

- F0

217. How many byte in decimal will the processor jump if the conditions for a jump are met?

- 24

218. In the Propeller microcontroller, the command "dira[4..9] := %11111" would cause the processor to do which of the following?

- Sets the Propeller pins P4 through P9 as output pins

219. What are the contents of DX after this program has been run:

- FFF0h.

```
MOV DX, 11h
MOV CX, [5512]
MOV BX, 5511h
SUB DX, [BX]
AND BX, FFFF
```

| Memory location | Contents |
|-----------------|----------|
| 5514            | 24       |
| 5513            | D8       |
| 5512            | 00       |
| 5511            | 21       |
| 5510            | 00       |

220. The number of nibbles in a word are:

- 4

221. In the Propeller microcontroller, the command “waitcnt(clkfreq\*10 + cnt)” would cause the processor to do which of the following?

- Create 10 second delay

222. The “LOOPNE” instruction is equivalent to which of the following instructions?

- DEC CX JNE

223. Given

```
AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000
DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI NG NZ NA PO NC
1D72:010D EB07 JMP 0114
```

How many bytes will the processor jump if the conditions for a jump were met?

- 7

224. On the Arduino platform, what is the programming language used?

- C

225. What is the hexadecimal encoding for loading AX with a word (value) from memory location 0820h?

- A12008

226. Which of the following would be used to set the TRISA register to control the direction of PIC18 Port to input?

- 1

227. The acronym ADC in microcontrollers stands for which of the following?

- Analog to Digital converter

228. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be:

- OF = 1, CF = 0

229. What is the advantage of Assembly Language over C Language?

- **The Assembler creates much faster executable code**

230. How many address lines would be required to address 128 MB directly?

- **27**

231. In the x86 lab part Hello MASM program in the original code, what is the address of the byte used to start number in the sequence “Hello World 0”?

- **020E**

232. Given:

0B0E : 0200 57 65 6C 63 F 6D 65 20-74 6F 20 41 73 73 65 6D

0B0E: 0210 62 6C 79 20 4C 61 6E 67-75 61 67 65 00 00 00 00

An ASCII message begins at memory location 0200, what is the message?

- **Welcome to Assembly Language**

233. Which of the following DOS Debug instructions would be used to change the IP register to 0110?

- **RIP**

234. Moore’s law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate?

- **Doubling every 18-24 months**

235. Which of the following DOS Debug instructions would set a break point at memory location 010C?

- **G = 100 10C**
236. AND'ing 1FH and 02H will result in which of the following?
- **02**
237. If CX is 0000 what will CX be after a "LOOP" instruction?
- **FFFF**
238. The number of bits in a word are:
- **16**
239. In x86 architecture, ALU stands for which of the following?
- **Arithmetic Logic Unit**
240. Given:  
 AX=0353 BX=0534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 0V UP EI PL NZ NA PO CY  
 1D72:0109 7D06 JGE 0118  
 What will the IP value be after a "t" command is executed in DOS Debug?
- **010B**
241. What is the number,  $1010.0101_2$  in decimal?
- **10.31**
242. How many cores does the propeller microcontroller have?
- **8**
243. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?
- **7DF2**
244. What command in DEBUG would be used to step through a program line by line?
- **T**
245. Here is a short sequence of code: 74 13 EB A3 CD 16 7D 21 3C 04 EB F0 EB 15. All of the instructions are a word long. The third instruction operator is:
- **INT**
246. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?
- **24**
247. Given:  
 AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI PL NZ NA PO NC  
 1D72:010D 7DF6 JNI 0116  
 How many bytes in decimal will the processor jump if the conditions for a jump were met?
- **10**
248. Determine the contents of register BL after the following instructions have been executed:
- **E2H**
249. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:
- **32, 32, 13, 10, 36**
250. AND'ing 10H and 2FH will result in which of the following?
- **0**
251. A "NOP" instruction in a program will:
- **Perform a No Operation**
252. Given:  
 AX=FFF0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000 DS=1D72 ES=1D72 SS=1D72  
 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY  
 1D72:0109 7D06 JGE 0118  
 What is the signed decimal value of the number in the AX register?
- **-16**
253. How many address lines would be required to address 64 MB directly?
- **26**

254. What command in DEBUG would be used to execute interrupts?
- **P**
255. What high level language is the propeller programmed in?
- **Spin**
256. Which of the following DOS Debug Instructions would be used to change the IP register to 010C?
- **RIP**
257. The acronym PWM used in the Parallax Propeller and MicroChip PIC18, is defined as:
- **Pulse Width Modulation**
258. Which command would you use to execute another core in the propeller microcontroller?
- **Cognew**
259. Given:  
 AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY  
 1D72:0111 ED08 JMP 0119  
 What will the IP value be after a “t” command is executed in DOS Debug?
- **0119h**
2. The instruction MOV CX, DADD is what addressing mode?
- **Immediate**
260. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?
- **A0**
261. Given:  
 AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010F NV UP EI NG NZ NA PO NC  
 ID72:010F 7D18 JGE 0128  
 How many bytes in decimal will the processor jump if the conditions for a jump are met?
- **24**
262. In the Propeller microcontroller, the command “dira[4..9] := %111111” would cause the processor to do which of the following?
- **Sets the Propeller pins P4 through P9 as output pins**
263. What are the contexts of DX after this program has been run:

|                | Memory Location | Contents |
|----------------|-----------------|----------|
| MOV DX, 11h    | 5514            | 24       |
| MOV CX, [5512] | 5513            | D8       |
| MOV BX, 5511h  | 5512            | 00       |
| SUB DX, [BX]   | 5511            | 21       |
| AND BX, FFFF   | 5510            | 00       |

- **FFF0h**
264. The number of nibbles in a word are:
- **4**
265. In the Propeller microcontroller, the command “waitcnt(clkfreq\*10 + cnt)” would cause the processor to do which of the following?
- **Create 10 second delay**
266. The “LOOPNE” instruction is equivalent to which of the following instructions?
- **DEC CX, JNE**
267. Given:  
 AX=FFE0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D NV UP EI NG NZ NA PO NC

1D72:010D EB07                      JMP                      0114

How many bytes will the processor jump if the conditions for a jump were met?

- **7**

268. On the Arduino platform, what is the programming language used?

- **C**

269. What is the hexadecimal encoding for loading AX with a word (value) from the memory location 0820h?

- **A12008**

270. Which of the following would be used to set the TRISA register to control the direction of the PIC18 Port to input?

- **1**

271. The acronym ADC in microcontrollers stands for which of the following?

- **Analog to Digital Converter**

272. In adding 5+5 through a 4 bit integer unit. The state of the OF and the CF flags after the add instruction would be:

- **OF=1, CF=0**

273. Given:

0B0E:0200    57 65 6C 63 6F 6D 65 20-74 6F 20 74 68 65 20 66

0B0E:0210    69 72 73 74 20 64 61 79-20 6F 66 20 74 68 65 20

0B0E:0220    72 65 73 74 20 6F 66 20-79 6F 75 72 20 6C 69 66

0B0E:0230    65 00 00 00 00 00 00 00 00 00 00 00 00 00 00

An ASCII message begins at memory location 0200, what is the message?

- **Welcome to the first day of the rest of your life**

274. The instruction MOV DX, BADD is what addressing mode?

- **Immediate**

275. Which of the following is the hexadecimal encoding for adding BX with CX and storing the result in CX?

- **03CB**

276. What is the advantage of Assembly Language over C Language?

- **The Assembler creates much faster executable code**

277. What is  $18.4375_{10}$  in binary?

- **010010.01110**

278. For the instruction sequence below, determine the contents of the register AL after this program is executed:

- **51H**

279. In x86 architecture, ALU stands for which of the following?

- **Arithmetic Logic Unit**

280. A microprocessor with a 33-bit address bus could access how much memory?

- **8 GB**

281. What is the hexadecimal encoding for "JGE" for a jump back 12 bytes?

- **7DF2**

282. Given:

AX=FF00 BX=3534 CX=0000 DX=0180 SP=FF00 BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010F NV UP EI NG NZ NA PO NC

1D72:010F    7D18                      JGE                      0128

How many bytes in decimal will the processor jump if the conditions for a jump are met?

- **24**

283. Moore's law has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is that rate?

- **Doubling every 18-24 months**

284. The number of bytes in a word are:

- **2**

285. Determine the contents of register BL after the following instructions have been executed:
- **2EH**
286. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 127?
- **7**
287. What high level language is the propeller programmed in?
- **Spin**
288. In the Propeller microcontroller, the command “dira[4..9] := %000000” would cause the processor to do which of the following?
- **Sets the Propeller pins P4 through P9 as input pins**
289. Which command would you use to execute another core in the propeller microcontroller?
- **Cognew**
290. How many cores does the propeller microcontroller have?
- **8**
291. In the Propeller microcontroller, the command “waitcnt(clkfreq\*10 + cnt)” would cause the processor to do which of the following?
- **Create 10 second delay**
292. The acronym ADC in microcontrollers stands for which of the following?
- **Analog to Digital Converter**
293. The acronym PWM used in the Parallax Propeller and Microchip PIC18, is defined as:
- **Pulse Width Modulation**
294. How many bits does the PIC18 microcontroller use in the PICkit 3 Debug Express have?
- **8**
295. Which of the following would be used to set the TRISA register to control the direction of the PIC18 Port to input?
- **1**
296. In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?
- **Bit 7 of port D is set to output**
297. In the PIC 18 with TRISD = 0b11110000 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?
- **0A**
298. On the Arduino platform, what is the programming language used?
- **C**
299. Given:  
 AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
 DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0111 NV UP EI NG NZ NA PO CY  
 1D72:0111 EB08 JMP 0119  
 What will the IP value be after a “t” command is executed in DOS Debug?
- **0119h**
300. How many bytes are there in this short sequence of code? B4 00 CD 16 4C CD 21 CD 20
- **9**
- The world’s first microprocessor was developed in 1972 by?**  
**Intel**  
**RISC stands for**  
**Reduce instruction set computer**
301. In x86 architecture, BIU stands for which of the following?
- **Bus Interface Unit**
302. Here is a short sequence of code: 74 13 EB A3 CD 16 7D 21 3C 04 EB F0 EB 15. All of the instructions are a word long. The fifth instruction operator is:
- **CMP**
303. The ASCII codes for space, space, carriage return, line feed, end of string in decimal are:
- **32, 32, 13, 10, 36**

304. A "NOP" instruction in a program will:

- **Perform a No Operation**

305. Given:

AX=FFF0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=0109 OV UP EI PL NZ NA PO CY

1D72:0109 7D06 JNL 0118

What is the signed decimal value of the number in the AX register?

- **-16**

306. Which of the following DOS Debug instructions would set a break point at memory location 010C?

- **G = 100 10C**

307. In adding 5+7 through a 4 bit integer unit, the state of the OF and CF flags after the add instruction would be:

- **OF = 0, CF = 0, ZF=0**

308. Given:

AX=FFD0 BX=3534 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000

DS=1D72 ES=1D72 SS=1D72 CS=1D72 IP=010D OV UP EI NG ZR NA PO NC

1D72:010D 7509 JNZ 0116

What will the IP value be after a "t" command is executed in DOS Debug?

- **010Fh**

309. What are the contents of DX after this program has been run:

- **FFF0h**

310. Which of the following DOS Debug instructions would be used to change the IP register to 010C?

- **RIP**

311. What is the number,  $1010.0101_2$  in decimal?

- **10.31**

312. What command in DEBUG would be used to step through a program line by line?

- **T**

313. AND'ing 1FH and 02H will result in which of the following?

- **02**

314. How many address lines would be required to address 64 MB directly?

- **26**

If you want to use a INT software interrupt function to print a string out to the screen....

Ah = 09h, ds:dx, "\$", 21h

## S2010 MT2

315. How many bit(s) is/are required to represent a range of decimal numbers from 0 to 127?  
**. 7**
316. If CX is 0003, what will CX be after a "LOOP" instruction?  
**a. 0002**  
If CX is 0002, what will CX be after a "LOOP" instruction?  
**b. 0001**
317. IN the Propeller microcontroller, the command "waitcnt(clkfreq\*5 + cnt)" would cause the processor to do which of the following?  
**c. A 5 second delay**
318. What is the number  $1011.0101_2$  in decimal?  
**d. 11.31**
319. This section of memory represents a stack. What type of program is this?  
BEEF : FFD0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00  
BEEF : FFE0 00 01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F  
BEEF : FFF0 11 22 33 44 55 66 77 88-99 AA BB CC DD EE FF  
**e. COM program**
320. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack?  
**f. BDCA**
321. Determine the contents of register BL after the following instruction have been executed.  
**g. 2EH**
- Program Listing  
MOV BL, 2E  
MOV CL, 10  
ROL BL, CL
322. What Hex values must be sent to address the key pad rows on the PPE board?  
**h. 1, 2, 4, 8**
323. What is the number  $32.4375_{10}$  in binary?  
**i. 100000.01110**
324. In MASM, with a "MOV CX, 18h" instruction, and a "LOOP" instruction, in decimal how many times will the program loop?  
**j. 24**
325. The acronym PWM used for motor control, is defined as which of the following?  
**k. Pulse Width Modulation**
326. In the PIC18 with TRISD = 0b01111111, what is the configuration of the Port D?  
**l. Bit 7 of port D is set to output**
327. Given the short code, what is the value in AX after the program is run?  
**m. 0500**
- Program Listing  
MOV BX, 0500  
Push BX  
MOV AX, 0100  
POP AX
328. What flag(s) does the "LOOPNZ" instruction look at to determine whether to loop or not?  
**n. ZF**
329. How many nibbles are in double precision IEEE floating point format number?



**o. 16**

330. What type of program is this?

AX=0000 BX=0000 CX=0000 DX=0180 SP=FFEE BP=0000 SI=0000 DI=0000  
DS=1476 ES=1576 SS=1376 CS=1D72 IP=0015 NV UP EI PL NZ NA PO NC  
1376:0015 0100            ADD    [BI + SI], AL                            DS : 0000=CD

**p. EXE**

331. If the SP is F00F, what is the SP value after a "PUSH CX" instruction?

**q. F00D**

332. In the PIC18 with TRISD = 0b00001111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs?

**r. A0**

333. What is the numeric sequence of the key pad columns on the PPE board?

**s. 37, 2F, 1F**

334. What is -130 decimal in 2's complement (8bits)?

**t. 01111110**

335. Which of the following is a valid x86 command for multiplying a number?

**u. MUL BX**

336. The number of bits in single precision IEEE floating point format are:

**v. 32**

337. You are trying to rebuild a HELLO project program in MASM and you get the following error:

"LINK : warning L4021: no stack segment".

What would be the reason for such an error?

**w. No project template for COM was selected**

338. A "pull down" resistor is used in digital circuits to do what?

**x. To keep the signal line "tied" low until the line is active (goes high)**

339. A "POP" instruction:

**y. increments the SP**

340. AND'ing 10H and 2FH will result in which of the following?

**z. 0**

341. In the Propeller microcontroller, the command "dira[4..9] := %111111" would cause the processor to do which of the following?

**aa. Sets the Propeller pins P4 through P9 as output pins**

342. What commands in MASM-CodeView would be used to step through a program line by line?

**bb. T (F8)**

343. If the SP is F00F, what will the SP value be after a "POP SP" instruction?

**cc. F011**

344. On the PPE board, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh?

**dd. 0**

345. In the Propeller microcontroller, the term "Method" is(are) which of the following?

**ee. A block of executable commands that has variables, can receive parameters, and returns a value.**

346. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal?

**ff. MOV CX, 30H            or            MOV CX, 48**

347. A "pull up" resistor is used in digital circuits to do what?

**gg. To keep the signal "tied" high until the line is active (goes low)**

76. 1010 0110 in 2's complement equals in base 10 → **-90**

77.

78. A microprocessor with a 32-bit address bus could access how much memory → **4GB**

79. A microprocessor with a 24-bit address bus could access how much memory → **16MB**

- 80.
81. A6 in 2's complement equals in base 10 → -90
- 82.
83. A "POP" instruction: → Increments the SP
84. A "PUSH" instruction → decrements the SP
85. A "NOP" instruction in a program will → Perform a No Operation
86. AND'ing 1FH and 02H will result in which of the following → 02
87. AND'ing 10H and 2FH will result in which of the following → 0
88. A "pull down" resistor is used in digital circuits to do what? → To keep the signal line "tied" low until the line is active (goes high)
- 89.
90. A "pull up" resistor is used in digital circuits to do what → To keep signal "tied" high until the line is active (goes low)
91. Determine the contents of register BL after the following instructions have been executed: → E2H
92. MOV BL, E2H
93. MOV CL, 1000b
94. Mov CL, SI is what addressing mode **REGISTER**
95. ROL BL, CL ; rotate BL by 8 to left = same value
- 96.
97. For the instruction sequence below, determine the contents of the registers AL after this program → 51H
98. MOV AL, 73h ; 0111 0011
99. ADD AL, 78h ; 0111 1000
100. DAA
101. if low nibble of AL > 9 or AF = 1 then:
102. AL = AL + 6
103. AF = 1
104. if AL > 9Fh or CF = 1 then:
105. AL = AL + 60h
106. CF = 1
- 107.
108. For the instruction sequence below, determine the contents of the register AL after this program is executed → 51H
- MOV AL, 83h
- ADD, AL, 68h
109. DAA
- 110.
111. Given the short code, what is the value in AX after the program is run → 0100
- Mov BX, 0100

BX

PUSH

MOV AX, 0500

POP AX
- 112.
113. Given the short code, what is the value in AX after the program is run → 0001
- MOV BX, 0001
114. PUSH BX
115. MOV AX, 0500
116. POP AX
- 117.
118. GIVEN: IP = 0106 Flags: NV UP EI NG NZ NA PE NC Instruction: JMP 011F
119. What will the IP value be after "t" command is executed in DOS Debug? → 011FH (Unconditional Jump)
- 120.
121. GIVEN: IP = 0109 Flags: OV UP EI PL NZ NA PO CY Instruction: JGE 0118
122. What will the IP value be after a "t" command is executed in DOS Debug → 010B
123. (0109 + 0010 add two bytes)
- 124.
125. GIVEN: IP=FFE0 Flags: OV UP EI PL NZ NA PO NC Instruction: JGE 0116: ID72:010D 7D09
126. How many bytes will the processor jump if the condition for a jump were met? → 9
- 127.
- 128.
129. GIVEN: AX= FFF0 IP = 0109 FALGS: OV UP EI PL NZ NA PO CY ID72: 010F 7D18 Instruction: JGE
0118. What is the signed decimal value of the number in the AX register? → -16

130. Convert the number 7D18 into decimal.
- 131.
132. GIVEN: IP= 010F Flags: NV UP EI NG NZ NA PO NC Instruction: JNL 0115.
133. How many bytes in decimal will the processor jump if the conditions for a jump were met → 24
- 134.
135. GIVEN: 57 65 6C 63 6F 6D 65 20-74 6F 20 41 73 73 65 6D
136. 62 6C 79 20 4C 61 6E 67-75 61 67 65 00 00 00 00
137. An ASCII message begins at memory location 0200, what is the message? → Welcome to Assembly Language
- 138.
139. Here is a short sequence of code: 7413 EBA3 CD16 7D21 3C04 EBF0 EB15. All of the instructions are a word long. The third instruction operator is → INT
- 140.
141. Here is a short sequence of code: 7413 A3EB CD16 7D21 3C04 EBF0 EB15. All of the instructions are a word long. The fourth instruction operator is → JGE
- 142.
143. Here is a short sequence of code: B400 CD16 3C4A 7404 BC6A 7513. All of the instructions are two bytes long. The sixth instruction operator is → JNZ
- 144.
- 145.
146. How many cores does the propeller microcontroller have → 8
- 147.
148. How many bits(s) is/are required to represent a range of decimal numbers from 0 to 15 → 4
149. How many bits(s) is/are required to represent a range of decimal numbers from 0 to 63 → 6
150. How many bits(s) is/are required to represent a range of decimal numbers from 0 to 127 → 7
151. How many bits(s) is/are required to represent a range of decimal numbers from 0 to 255 → 8
152. How many bytes are there in this short sequence of code B4 00 CD 16 4C CD 20 → 7
153. How many nibbles are there in this short sequence of code B4 00 CD 16 3C 4A 74 04 3C 6A 75 13 → 24
154. How many bytes are in double precision IEEE floating point format numbers → 8
155. How many nibbles are in double precision IEEE floating point format numbers → 16
156. How many address lines would be required to address 128 MB directly → 27 ( $128 \times 1048576 = 134217728$  and  $2^{27} = 134217728$ )
157. How many address lines would be required to address 64 MB directly → 26 ( $64 \times 1048576 = 67108864$  and  $2^{26} = 67108864$ )
158. If CX is 0000, what will CX be after a "LOOP" instruction → FFFF
159. If CX is 0003, what will CX be after a "LOOPNZ" instruction → 0002
160. If the SP is F00F, what is the SP value after a "PUSH CX" instruction → F00D
161. If the SP is F00F, what is the SP value after a "POP CX" instruction → F011
162. If the SP is F00F, what is the SP value after a "POP SP" instruction → F011
163. In adding 5+5 through a 4 bit integer unit. The state of the OF and CF flags after the add instruction would be → OF = 1, CF = 0
164. In x86 architecture, BIU stands for which of the following → Bus Interface Unit
165. In x86 architecture, ALU stands for which of the following → Arithmetic Logic Unit
166. In the x86 lab part 3 Hello MASM program in the original code, what is the address of the byte used to start the number in the sequence "Hello World 0"? → 020E
167. In MASM, with a "MOV CX, 24h" instruction, and a "LOOP" instruction, how many times will the program loop in decimal → 36
168. In MASM, with a "MOV CX, 24" instruction, and a "LOOP" instruction, how many times will the program loop in decimal → 24
169. In MASM, with a "MOV CX, 12h" instruction, and a "LOOP" instruction, how many times will the program loop in decimal → 18
170. In the Hello MASM lab in the original code, what is the address of the string to start the message "Hello World 0" → 0200
171. In the PIC18 with TRISD = 0b10000000, what is the configuration of the Port D → Bit 7 of port D is set to input

172. In the PIC18 with TRISD = 0b01111111 and LATD = 0xAA, what value will be on Port D and shown on the LEDs → **Bit 7 of port D is set to output** (because the first bit is zero = output)
173. In the PIC18 with TRISD = 0b00001111, what is the configuration of the Port D → **A0 (First 4 are outputs and last four are inputs)**
174. In the PIC18 with TRISD = 0b11110000 and LATD = 0xAA, what value will be on Port D and shown on the LEDs → **0A**
175. In the Propeller microcontroller, the command "dira[9..4] := %000000" would cause the processor to do which of the following → **Sets the propeller pin P4 through P9 as output pins**
176. In the Propeller microcontroller, the command "dira[9..4] := %111111" would cause the processor to do which of the following → **Sets the propeller pin P4 through P9 as output pins**
177. In the propeller microcontroller, the command "waitcnt(clkfreq\*3 + cnt)" would cause the processor to do which of the following → **A 3 second delay**
178. In the propeller microcontroller, the command "waitcnt(clkfreq\*2 + cnt)" would cause the processor to do which of the following → **A 2 second delay**
179. In the Propeller microcontroller, the term "Method" is (are) which of the following → **A block of executable commands that has variables, can receive parameters, and returns a value.**
180. Int 10h uses what function code to write a character to the screen and advance the cursor by one character position → **0Eh**
181. Int 21h, Function 09h requires three things set up before calling in order to correctly print a string:
182. **DS=SEG Hello\_msg, DX=OFFSET Hello\_msg, Hello\_msg terminated with 24h.**
- 183.
184. Ladder Logic is used in? → **PLCs**
185. **Moore's law** has accurately predicted the growth rate in the number of transistors per die for the last 40 years. What is the rate? → **Doubling every 18-24 months**
186. On the Arduino platform what is the program language used → **C**
- 187.
188. On the **PPE board**, what number(s) on the key pad is(are) pressed for an output port value of 08h and an input port value of 2Fh → **0**
189. On the **PPE board**, what number(s) on the key pad is(are) pressed for an output port value of 04h and an input port value of 2Fh → **8**
190. The "LOOPNZ" instruction is equivalent to which of the following instructions → **DEC CX, JNE**
- 191.
192. The acronym PWM used for motor control, is defined as which of the following → **Pulse Width Modulation**
193. The acronym PLC, is defined as which of the following? → **Programmable Logic Controller**
- 194.
195. The ASCII codes for space, space, carriage return, line feed, end of string in **decimal** are → **32, 32, 13, 10, 36**
196. The ASCII codes for space, space, carriage return, line feed, end of string in **hexadecimal** are:→ **20, 20, 0D, 0A, 24**
197. The binary number, **1011 0101**, represents what values as a unsigned binary, 8 bit signed binary, odd parity ASCII, and BCD number (in that order) → **181, -76, 5, invlaid5**
198. The binary number, **1000 0101**, represents what values as a unsigned binary, 8 bit signed binary, odd parity ASCII, and BCD number (in that order) → **133, -123, ENQ, 85**
199. The instruction MOV CX, DADD is what addressing mode → **Immediate**
200. The instruction MOV CX, [DADD] is what addressing mode → **Direct**
201. The number of bits in single precision IEEE floating point format are → **32**
- 202.
- 203.
- 204.
205. This section of memory represents a stack. What type of program is this → **EXE PROGRAM**  
**BEEF:00D0** 00 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 **BEEF:00E0** 00  
01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F **BEEF:00F0** 11 22 33 44 55 66  
77 88-99 AA BB CC DD EE FF
- 206.
207. This section of memory represents a stack. What type of program is this → **COM PROGRAM**  
**BEEF:FFD0** 00 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 **BEEF:FFE0** 00

01 02 03 04 05 06 07-08 09 0A 0B 0C 0D 0E 0F  
77 88-99 AA BB CC DD EE FF

BEEF:FFF0 11 22 33 44 55 66

208.

209.

210. Using MASM, which of the following will cause a program with a LOOP instruction to loop 48 times in decimal → **MOV CX, 48**

211. Which command would you use to execute another core in the propeller microcontroller → **Cognew**

212. What command in DEBUG would be used to change the code segment → **RCS**

213. What command in DEBUG would be used to change the IP value → **RIP**

214. What command in DEBUG would be used to execute interrupts → **P**

215. What command in MASM-CodeView would be used to step through a program line by line → **T(F8)**

216. What flag(s) does the "LOOPNZ" instruction look at to determine whether to loop or not → **ZF**

217. What flag(s) does the "LOOPNE" instruction look at to determine whether to loop or not → **ZF**

218. What Hex values must be sent to address the key pad rows on the PPE board → **1,2,4,8**

219. What is the advantage of C Language over Assembly Language → **C is transportable to other microprocessor architectures**

220. What is the hexadecimal encoding for "JGE" for a jump back 10 bytes → **7DF2**

221. What is the hexadecimal encoding for adding AX with BX and storing the result in AX → **01D8**

222. ADD AX, BX 000 00W 11 reg1 reg2

223. What is the hexadecimal encoding for adding BX with DX and storing the result in BX → **01D3**

224.

225. What is -130 decimal in 2's complement (8bits) → **01111110**

226. What is -32.75 in a base two number system → **-100000.110000**

227. What is 16.4375 in binary → **010000.01110**

228. What is the binary value of decimal 12.875 → **1100.1110**

229.

230. What is 16.4375 in binary → **010000.01110**

231.

232. What is number, 1011.0101 (2) in decimal? → **11.31**

What is the hexadecimal encoding for lading AX with a word (value from memory location 0820h

**Question 121**  
Answer saved  
Marked out of 1  
Flag question

What is the hexadecimal encoding for loading AX with a word (value) from memory location 0820h?

Select one:

- ☐ a. 8A262008
- ☒ b. 8B162008 ← correct
- ☐ c. 8B162008
- ☐ d. A10820
- ☐ e. A00820

233.

234. What is the **numeric sequence** of the key pad columns on the PPE board → **37,2F,1F**

235. What is the decimal value of C5 5A 57 00 in IEEE single precision FP format → **-3493.4375**

236. What of the following instruction would be used to set the LED to light on the Arudino platform → **digitalWrite(ledPin, HIGH);**

237.

238. What type of program is this → **EXE**

IP = **0115**, 1376:0115 0100      ADD [BX+SI], AL      DS:0000=CD

239.

240. What type of program is this → **COM**

IP = **0100**, 1376:0100 0100      ADD [BX+SI], AL      DS:0000=CD

241.

242. Which of the following DOS Debug instructions would set a break point at memory location 010C → **G= 100 10C**

- 243.
244. Which of the following would be used to set the **TRISA** register to control the direction of **PIC18** port to **input** → **1** and for **output** its → **0**
- 245.
246. Which of the following DOS Debug instructions would be used to change the IP register to 110 → **RAX = 0110**
247. What is 458752.00 Converted to double precision FP?  
 . 41 1c 00 00 00 00
248. Which of the following will cause a program with a LOOP instruction to loop 48 times (decimal) → **CX=30h**
- 249.
250. Which of the following is a valid x86 command for multiplying a number → **MUL BX**
251. Which of the following is **not a valid** command for a number into a register in MASM → **MOV AX, BADH**
252. With a POP BX instruction, what will be order off the accumulator, base, count, and data registers restored from the stack → **BX**
253. With a POPA instruction, what will be the order of the accumulator, base, count, and data registers restored from the stack → **BDCA**
254. You are trying to rebuild a HELLO program project in MASM and you get the following error: "ERROR 4 line 1". What is the cause of the error? → **Not known—this error by itself isn't a problem, press the enter key to clear the error.**
- 255.
256. You are typing to rebuild a HELLO project program in MASM and you get the following error: "LINK : warning L4021: no stack segment". What would be the reason for the such an error → **No project template for COM was selected.**
- 257.
258. You are typing to rebuild a HELLO project program in MASM and you get the following error: "LINK : fatal error L1089: HELLO.lrf: cannot open response file". What would be the reason for the such an error → **No source file is identified(no .asm file)**
- 259.
- 260.
- 261.
- 262.
- 263.
264. **PPE Row Column Scan decoding**
- |    |    |    |    |    |    |    |    |                           |
|----|----|----|----|----|----|----|----|---------------------------|
| D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |                           |
| S7 | S6 | S5 | S4 | S3 | S2 |    |    |                           |
| ^  | ^  | ^  |    |    |    |    |    |                           |
| 3  | 2  | 1  |    |    |    |    |    |                           |
| 0  | 0  | 0  | 0  | 1  | 0  | 0  | 0  | = 08h                     |
| 0  | 0  | 0  | 1  | 0  | 0  | 0  | 0  | = 10h                     |
| 0  | 0  | 1  | 0  | 0  | 0  | 0  | 0  | = 20h                     |
|    |    |    |    |    |    |    |    |                           |
| 0  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | = 3Fh -> Nothing pressed  |
| 0  | 0  | 1  | 1  | 0  | 1  | 1  | 1  | = 37h -> Number 1 pressed |
| 0  | 0  | 1  | 0  | 1  | 1  | 1  | 1  | = 2Fh -> Number 2 pressed |
| 0  | 0  | 0  | 1  | 1  | 1  | 1  | 1  | = 1Fh -> Number 3 pressed |
- 265.

## Flags

The complete set of possible flag mnemonics in Debug (ordered from left to right) are as follows:

**Set**

OV = Overflow  
 DN = Direction Down  
 EI = Interrupts Enabled  
 NG = Sign Flag negative  
 ZR = Zero  
 AC = Auxiliary Carry  
 PO = Odd Parity  
 CY = Carry

**Clear**

NV = No Overflow  
 UP = Direction Up  
 DI = Interrupts Disabled  
 PL = Sign Flag positive  
 NZ = Not Zero  
 NA = No Auxiliary Carry  
 PE = Even Parity  
 NC = No Carry

**Jump information**

| Mnemonic                   | Condition                                             |
|----------------------------|-------------------------------------------------------|
| <i>Signed Operations</i>   |                                                       |
| JG/JNLE                    | Greater/not less or equal $((SF \oplus OF) + ZF) = 0$ |
| JGE/JNL                    | Greater or equal/not less $(SF \oplus OF) = 0$        |
| JL/JNGE                    | Less/not greater or equal $(SF \oplus OF) = 1$        |
| JLE/JNG                    | Less or equal/not greater $((SF \oplus OF) + ZF) = 1$ |
| JO                         | Overflow $(OF = 1)$                                   |
| JS                         | Sign $(SF = 1)$                                       |
| JNO                        | Not overflow $(OF = 0)$                               |
| JNS                        | Not sign $(SF = 0)$                                   |
| <i>Unsigned Operations</i> |                                                       |
| JA/JNBE                    | Above/not below or equal $(CF \oplus ZF) = 0$         |
| JAЕ/JNB                    | Above or equal/not below $(CF = 0)$                   |
| JB/JNAE                    | Below/not above or equal $(CF = 1)$                   |
| JBE/JNA                    | Below or equal/not above $(CF \oplus ZF) = 1$         |
| <i>Either</i>              |                                                       |
| JC                         | Carry $(CF = 1)$                                      |
| JE/JZ                      | Equal/zero $(ZF = 1)$                                 |
| JP/JPE                     | Parity/parity even $(PF = 1)$                         |
| JNC                        | Not carry $(CF = 0)$                                  |
| JNE/JNZ                    | Not equal/not zero $(ZF = 0)$                         |
| JNP/JPO                    | Not parity/parity odd $(PF = 0)$                      |

Jumps always start with 7...

**Jcc – Jump if Condition is met, (see conditional jump instructions table Ch4 Uffenbeck)**

8-bit displacement

|              |             |
|--------------|-------------|
| 0111 t t t n | byte offset |
|--------------|-------------|

Full displacement

|           |              |             |
|-----------|--------------|-------------|
| 0000 1111 | 1000 t t t n | word offset |
|-----------|--------------|-------------|

| Hex | t | t | t | n | Flag Test          | unsigned | signed   | Other |
|-----|---|---|---|---|--------------------|----------|----------|-------|
| 0   | 0 | 0 | 0 | 0 | OF = 1             |          | JO       |       |
| 1   | 0 | 0 | 0 | 1 | OF = 0             |          | JNO      |       |
| 2   | 0 | 0 | 1 | 0 | CF = 1             | JB, JNAE |          |       |
| 3   | 0 | 0 | 1 | 1 | CF = 0             | JNB, JAE |          |       |
| 4   | 0 | 1 | 0 | 0 | ZF = 1             | JE, JZ   | JE, JZ   |       |
| 5   | 0 | 1 | 0 | 1 | ZF = 0             | JNE, JNZ | JNE, JNZ |       |
| 6   | 0 | 1 | 1 | 0 | CF = 1 or ZF = 1   | JBE, JNA |          |       |
| 7   | 0 | 1 | 1 | 1 | CF = 0 and ZF = 0  | JNBE, JA |          |       |
| 8   | 1 | 0 | 0 | 0 | SF = 1             |          | JS       |       |
| 9   | 1 | 0 | 0 | 1 | SF = 0             |          | JNS      |       |
| A   | 1 | 0 | 1 | 0 | PF = 1             |          |          | JP    |
| B   | 1 | 0 | 1 | 1 | PF = 0             |          |          | JNP   |
| C   | 1 | 1 | 0 | 0 | SF ≠ OF            |          | JNGE, JL |       |
| D   | 1 | 1 | 0 | 1 | SF = OF            |          | JGE, JNL |       |
| E   | 1 | 1 | 1 | 0 | ZF = 1 or SF ≠ OF  |          | JNG, JLE |       |
| F   | 1 | 1 | 1 | 1 | ZF = 0 and SF = OF |          | JG, JNLE |       |

Examples:

JMP unconditional jump (same argument)

1110 1011: byte displacement (EB \_\_)

1110 1001: word displacement (E9 \_\_)

JCXZ jump if CX = 0

1110 0011: byte displacement (E3 \_\_)

(to work with ECX use address size prefix)

LOOP

1110 0010: byte displacement (E2 \_\_)

*Include auto-decrement of the CX register.**Jump if CX is not zero after decrement.*8bit-> +127 forward to -128 bytes  
backward, expressed in Hex

Link to a converter

<http://www.exploringbinary.com/floating-point-converter/>