

ARM[®] Cortex[®]-M0 32-bit Microcontroller

NuMicro[®] Family NUC131 Series BSP Revision History

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Revision 3.00.003 (Released 2017-10-24)

- 1. Fixed CLK_SysTickDelay() bug.
- 2. Fixed UA_LIN_CTL[4] bit field name is "MUTE_EN" not "WAKE_EN" in UA_LIN_CTL constants definitions.
- 3. Fixed bug of PWM_MASK_OUTPUT() that mask function can't be disabled.
- 4. Fixed bug for return value error of PWM_ConfigOutputChannel().
- 5. Fixed UART SelectLINMode() clear enable bit setting bug.
- 6. Fixed PLL clock source selection bug in CLK_SetCoreClock()
- 7. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().
- 8. Fixed u32idmask for Standard ID issue in CAN_SetRxMsgObjAndMsk() function.
- 9. Modified to disable debug message when enabling semihost without NuLink connection.
- 10. Added ADC MeasureAVDD sample code
- 11. Added new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick()
- 12. Added CLK_SysTickLongDelay() for long delay
- 13. Added CAN_SetRxMsgObjAndMsk() function declaration.

Revision 3.00.002 (Released 2015-01-16)

- 1. Fixed PWM driver bug for output low when duty is 100%
- 2. Fixed BPWM driver bug for output low when duty is 100%
- 3. Fixed CLK driver bug in CLK_SetCoreClock():
- 4. Fixed CLK driver constant definitions error of (B)PWM0/1_MODULE clock source selection.
- 5. Fixed GPIO_ENABLE_DOUT_MASK() and GPIO_DISABLE_DOUT_MASK() bug of GPIO driver.
- 6. Fixed PWM driver bug of PWM MASK OUTPUT() to remove redundant parenthesis.
- 7. Fixed BPWM driver bug of BPWM_MASK_OUTPUT() to remove redundant parenthesis.
- 8. Fixed UART driver clear flag bug in UART_ClearIntFlag().
- 9. Fixed I2C driver module reset bug of I2C_Close().
- 10. Fixed clear RS-485 address byte detection flag bug in UART_RS485_CLEAR_ADDR_FLAG() of UART driver.
- 11. Fixed SYS_IS_SYSTEM_RST() bug, it is "SYS_RSTSRC_RSTS_SYS_Msk" but "SYS_RSTSRC_RSTS_MCU_Msk".
- 12. Fixed clear RS-485 address byte detection flag clear bug in RS485_HANDLE() of UART RS485 Slave sample code.
- 13. Fixed UART RS485 RTS active level to high level active in RS485_9bitModeMaster() of UART RS485 Sample code.
- 14. Fixed NVIC_EnableIRQ() to NVIC_DisableIRQ() after CHIP wake-up in I2C_Wakeup_Slave sample code
- Added PWM_EnableLoadMode() and PWM_DisableLoadMode() functions to PWM driver
- 16. Added PWM_SetBrakePinSource() function to PWM driver
- 17. Added CLK GetPCLKFreq() function to CLK driver
- 18. Added new macro PWM SET DEADZONE CLK SRC() to PWM driver
- 19. Added new macro SYS_IS_LVR_RST() to SYS driver.
- 20. Added non-blocking printf implementation and use predefine compiler option to enable/disable it.



Revision 3.00.001 (Released 2014-06-12)

1. First Release



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