

Distributed Parallelization of xPU Stencil Computations in Julia

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ABSTRACT

We present a straightforward approach for distributed parallelization of stencil-based xPU applications on a regular staggered grid, which is instantiated in the package `ImplicitGlobalGrid.jl`. The approach allows to leverage remote direct memory access and enables close to ideal weak scaling of real-world applications on thousands of GPUs. The communication costs can be easily hidden behind computation.

Keywords

Julia, Distributed Parallelization, xPU, GPU, Supercomputing, Stencil Computations, Staggered Grid

1. Introduction

In light of the high pace of hardware evolution since the dawn of the 21st century, the HPC community has identified the 3 “P”s - (scalable) Performance, (performance) Portability and Productivity - as fundamental requirements for today’s and tomorrow’s software development. The approach and package development presented in this paper responds to each of the 3 “P”s. We present an approach for automatic and architecture-agnostic distributed parallelization of stencil-based xPU applications on a regular staggered grid (with xPU we refer simultaneously to GPU and CPU in this paper).

2. Approach

The here presented approach renders the distributed parallelization of stencil-based xPU applications on a regular staggered grid almost trivial. We have instantiated the approach in the Julia package `ImplicitGlobalGrid.jl`. A highlight in the design of `ImplicitGlobalGrid` is the automatic implicit creation of the global computational grid based on the number of processes the application is run with (and based on the process topology, which can be explicitly chosen by the user or automatically defined). As a consequence, the user only needs to write a code to solve his problem on one xPU (local grid); then, as little as three functions can be enough to transform a single xPU application into a massively scaling multi-xPU application: a first function creates the implicit global staggered grid, a second function performs a halo update on it, and a third function finalizes the global grid. Fig. 1 shows a stencil-based 3-D heat diffusion xPU solver, where distributed parallelization is achieved with these three `ImplicitGlobalGrid` functions (lines 23, 38 and 43) plus some additional functions to query the size of the

```

1  using ImplicitGlobalGrid
2  using ParallelStencil
3  using ParallelStencil.FiniteDifferences3D
4  @init_parallel_stencil(CUDA, Float64, 3)
5
6  @parallel function step!(T2,T,Ci,lam,dt,dx,dy,dz)
7      @inn(T2) = @inn(T) + dt*(
8          lam*@inn(Ci)*(@d2_xi(T)/dx^2 +
9                        @d2_yi(T)/dy^2 +
10                       @d2_zi(T)/dz^2) )
11
12      return
13  end
14
15  function diffusion3D()
16      # Physics
17      lam = 1.0          #Thermal conductivity
18      c0 = 2.0           #Heat capacity
19      lx=ly=lz = 1.0     #Domain length x|y|z
20
21      # Numerics
22      nx=ny=nz = 512     #Nb gridpoints x|y|z
23      nt = 100           #Nb time steps
24      me, = init_global_grid(nx, ny, nz)
25      dx = lx/(nx_g()-1) #Space step in x
26      dy = ly/(ny_g()-1) #Space step in y
27      dz = lz/(nz_g()-1) #Space step in z
28
29      # Initial conditions
30      T = @ones(nx,ny,nz).*1.7 #Temperature
31      T2 = copy(T)             #Temperature (2nd)
32      Ci = @ones(nx,ny,nz)./c0 #1/Heat capacity
33
34      # Time loop
35      dt = min(dx^2,dy^2,dz^2)/lam/maximum(Ci)/6.1
36      for it = 1:nt
37          @hide_communication(16, 2, 2) begin
38              @parallel step!(T2,T,Ci,lam,dt,dx,dy,dz)
39              update_halo!(T2)
40          end
41          T, T2 = T2, T
42      end
43      finalize_global_grid()
44  end
45
46  diffusion3D()

```

Fig. 1. Stencil-based 3-D heat diffusion xPU solver implemented using `ImplicitGlobalGrid` and `ParallelStencil`.

global grid (lines 24-26; note that shared memory parallelization is performed with `ParallelStencil` [3]).

ImplicitGlobalGrid relies on `MPI.jl` [2] to perform halo updates close to hardware limits. For GPU applications, ImplicitGlobalGrid leverages remote direct memory access when CUDA- or ROCm-aware MPI is available and, otherwise, uses highly optimized asynchronous data transfer routines to move the data through the hosts. In addition, pipelining is applied on all stages of the data transfers, improving the effective throughput between GPU and GPU. Low level management of memory, CUDA streams, ROCm queues and signals permits to efficiently reuse send and receive buffers and streams or queues and signals throughout an application without putting the burden of their management to the user. Moreover, all data transfers are performed on non-blocking high-priority streams or queues, allowing to overlap the communication optimally with computation. `ParallelStencil.jl`, e.g., can do so with a simple macro call (Fig. 1, line 36).

ImplicitGlobalGrid is fully interoperable with `MPI.jl`. By default, it creates a Cartesian MPI communicator, which can be easily retrieved together with other MPI variables. Alternatively, an MPI communicator can be passed to ImplicitGlobalGrid for usage. As a result, ImplicitGlobalGrid’s functionality can be seamlessly extended using `MPI.jl`.

The modular design of ImplicitGlobalGrid, which heavily relies on multiple dispatch, enables adding support for other hardware with little development effort. Support for AMD GPUs using the recently matured `AMDGPU.jl` package [5] has already been implemented as a result (Nvidia GPUs are supported using `CUDA.jl` [1]). ImplicitGlobalGrid supports at present distributed parallelization for CUDA- and ROCm-capable GPUs as well as for CPUs.

3. Results

We here report the scaling achieved with the 3-D heat diffusion xPU solver (Fig. 1) on up to 2197 Nvidia Tesla P100 GPUs on the Piz Daint Supercomputer at the Swiss National Supercomputing Centre (Fig. 2). We observe a parallel efficiency of 93% on 2197 GPUs. Moreover, we have employed ImplicitGlobalGrid and `ParallelStencil` for the parallelization of a solver for nonlinear 3-D poro-visco-elastic two-phase flow and have also conducted a weak scaling experiment on Piz Daint (Fig. 3). We observe a parallel efficiency of over 95% on up to 1024 GPUs. As a performance reference, the solver implemented in Julia achieved 90% of the performance of the respective original solver written in CUDA C using MPI.

4. Conclusions

We have shown that ImplicitGlobalGrid enables scalable performance, performance portability and productivity and addresses the 3 “P”s in all of its aspects. In addition, we have demonstrated the effectiveness and wide applicability of our approach within geosciences. Our approach is naturally in no sense limited to geosciences as distributed parallelization based on halo updates is employed in many scientific disciplines. We illustrated this in a recent contribution, where we showcased a quantum fluid dynamics solver using the nonlinear Gross-Pitaevski equation implemented with ImplicitGlobalGrid and `ParallelStencil` [4].

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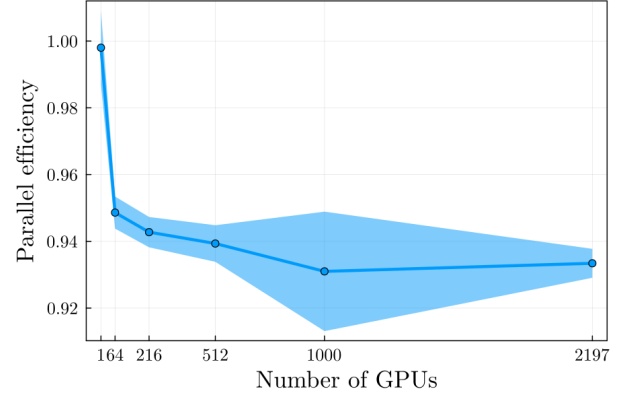


Fig. 2. Parallel weak scaling of the 3-D heat diffusion solver (Fig. 1) from 1 to 2197 Nvidia P100 GPUs on Piz Daint at CSCS. The blue surface visualizes the 95% confidence interval of the reported medians (20 samples). The raw data and plotting script are available in XXX.

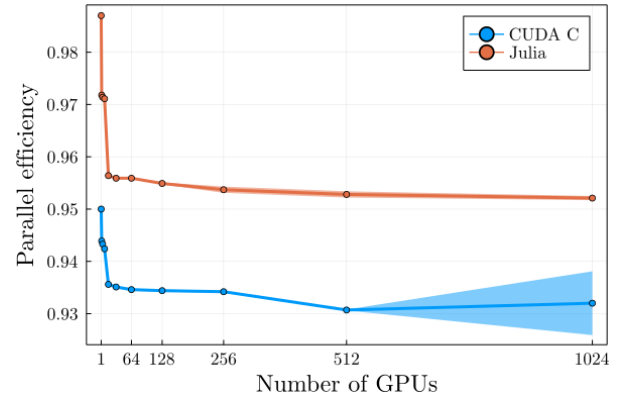


Fig. 3. Parallel weak scaling of the nonlinear 3-D poro-visco-elastic two-phase flow solver from 1 to 1024 Nvidia P100 GPUs on Piz Daint at CSCS (problem size per GPU is 382^3). The blue and orange surfaces visualize the 95% confidence interval of the reported medians (20 samples). The raw data and plotting script are available in XXX.

6. References

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