+5V SUPPLY

+3.3V SUPPLY

GPIO INTERFACE

DEBUG

SPI1

LEVEL SEL

SPI0

DIO-RST

DIO-EN

UART0  
SWIO

+5V SUPPLY

VBUS

LEVEL SEL

LS

+5V

+3.3V

OUT

DIOH

SPI

RST

OUT

LEVEL TRANSLATOR

EN

IN

VREF

OUT

LEVEL TRANSLATOR

EN

IN

VREF

OUT

LEVEL TRANSLATOR

EN

IN

VREF

OUT

LEVEL TRANSLATOR

EN

IN

VREF

OUT

RP2040

LEVEL SEL

SPI0

DIO-RST

DIO-EN

DEBUG

SPI1

ADC

DIOL

SPI

RST

OUT