ENERGIS 10IN Managed PDU

Display Board 1.1.0 Schematic Documentation

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Document status:	Internal - CE Technical File

This Technical File applies exclusively to ENERGIS 10IN Managed PDU, Hardware Revision 1.1.0, and corresponding firmware versions released for this hardware. Firmware versions are identified in the device user interface and are covered by this Technical File insofar as they do not change safety-relevant behavior. The Technical File is maintained in electronic form by the manufacturer and can be made available without undue delay. All documents listed herein are retained for at least 10 years after the last product has been placed on the EU market.

Revision History

A revision is a new edition of the document and affects all sections of this document.

Version	Date	Responsible	Modification
1.0.0	12.12.2025	David Sipos	Initial creation of the document

Contents

1. Introduction

This document provides general technical information related to the electrical schematics used within the ENERGIS 10-inch managed PDU system. It applies to all schematic diagrams associated with the product, including but not limited to the main control circuitry, display/interface connections, and auxiliary module interconnections.

1.1 Purpose

The purpose of this document is to support technical documentation, compliance activities, and internal reference by describing the general characteristics, intended use, and design context of the schematic diagrams. It does not describe firmware behaviour in detail and does not replace PCB layout files, manufacturing data, or test reports referenced elsewhere in the technical file.

This document is applicable to the schematic versions identified in the corresponding design documentation and engineering outputs. Any future revisions may require an update or extension of this document where relevant.

2. General Information

The schematics covered by this document represent the electrical design of internal components of the ENERGIS 10-inch managed PDU and are not intended to be used as standalone products. Each schematic defines the functional relationships between components and subsystems within the overall system architecture and is valid only when implemented in accordance with the product design and enclosure specifications.

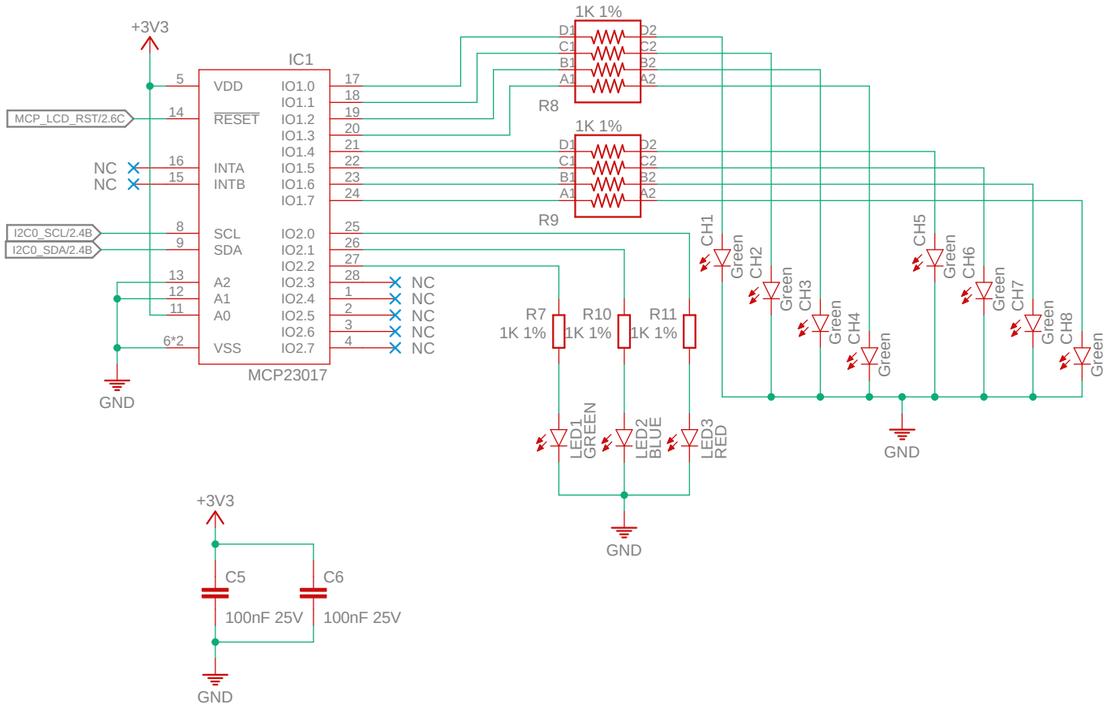
The schematics are developed using standard electronic design principles and documented using industry-standard EDA tools. Component selection and circuit topology are based on performance requirements, availability, and compliance with applicable regulatory standards, including RoHS where applicable. All circuits are intended for indoor use within controlled environmental conditions as specified in the product documentation. The designs do not include provisions for user modification and are not intended for repair or alteration by the end user.

Detailed design data, including schematics, PCB layouts, manufacturing outputs, and validation results, are referenced in the Technical File Index and maintained as part of the overall technical documentation set.

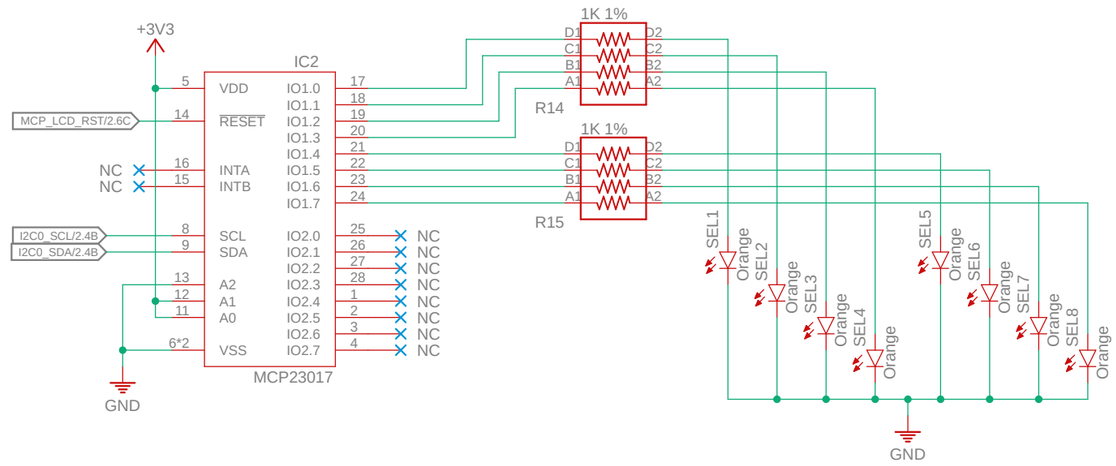
3. ENERGIS_DisplayBoard_1.1.0 Schematics

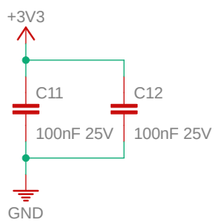
IO DRIVERS

DISPLAY LEDS



SELECTION LEDS





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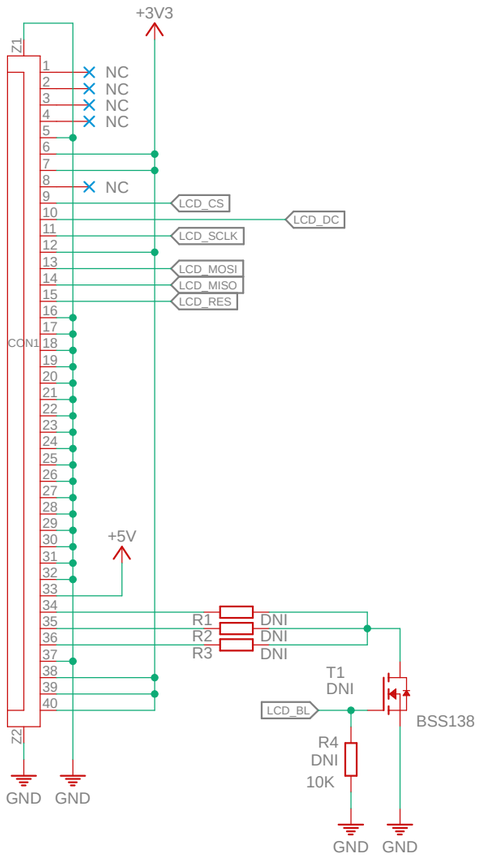
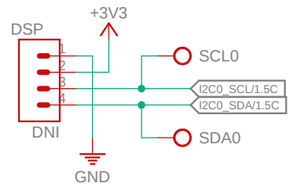
https://github.com/DvidMakesThings/HW_10-In-Rack_PDU		Sheet: 1/3

CONNECTORS

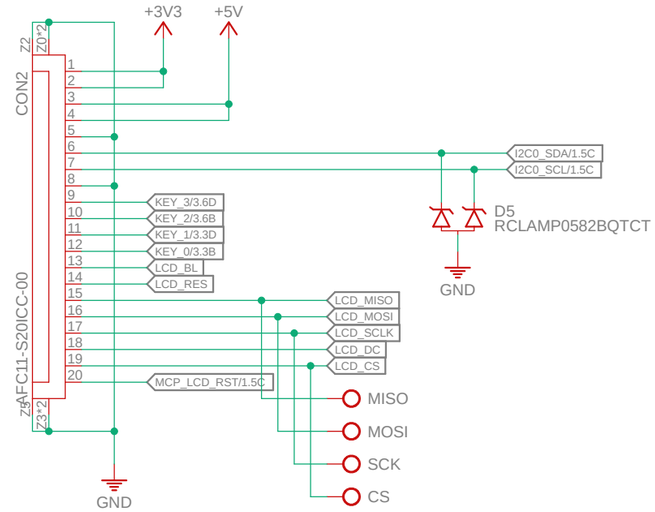
3.5-IN LCD



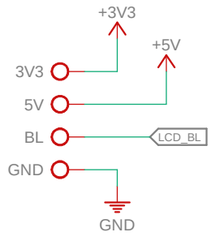
I2C DISPLAY

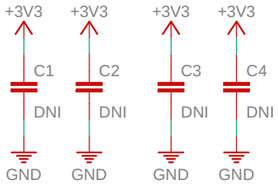


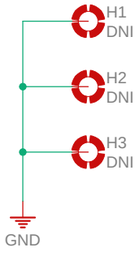
BOARD CONNECTOR



TESTPOINTS







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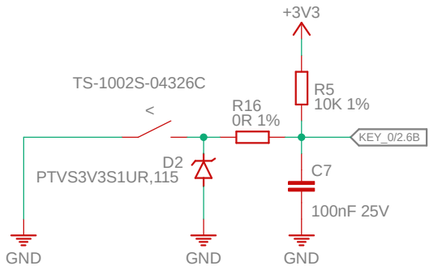
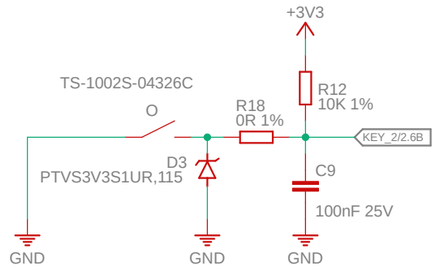
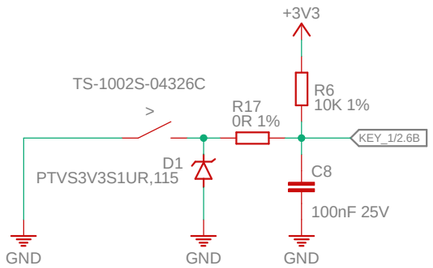
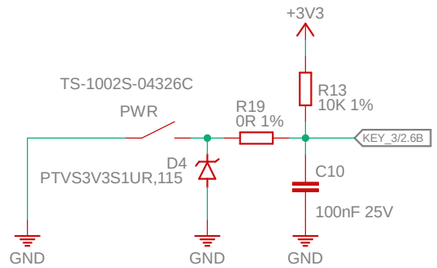
DvidMakesThings	Rev: 1.1.0
https://github.com/DvidMakesThings/HW_10-In-Rack_PDU	Sheet: 2/3

PUSHBUTTONS









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DvidMakesThings		
https://github.com/DvidMakesThings/HW_10-In-Rack_PDU		Sheet: 3/3