

PDNode-600Pro

Rev 1.0.0

Baseboard

Variant: Pro

07.02.2026

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VERSION:

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

PCB TOP VIEW

PCB BOTTOM VIEW

NOTES

Not fitted components are marked as

Comments:	Company: DavidMakesThings	Variant: Pro	Git Hash:
Board Name:	Baseboard	Project Name:	PDNode-600 Pro
Sheet Title:	File Name: PDNode_Baseboard.kicad_sch	Designer: David Sipes	Date: 2026-02-05 Revision: 1.0.0
Sheet Path:	/	Reviewer:	Size: A3 Sheet: 1 of 15

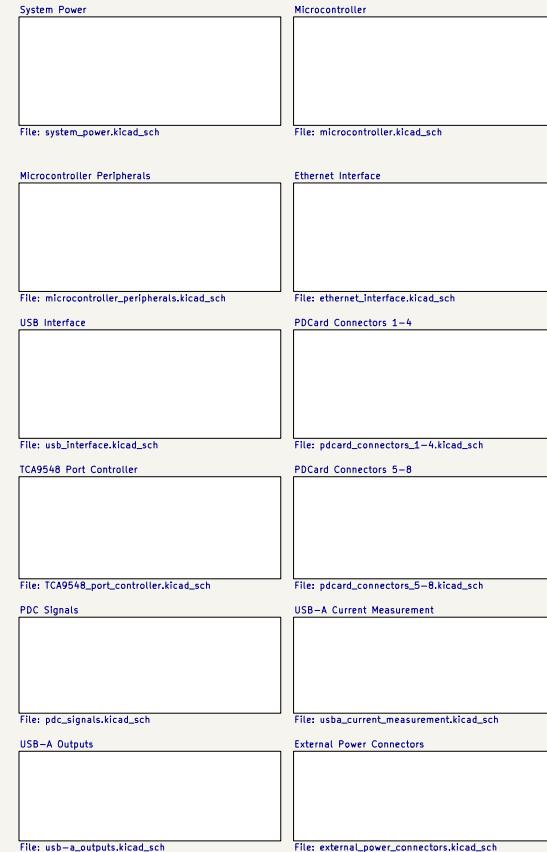
Block Diagram

File: ProjectArchitecture.kicad_sch

Revision History

File: RevisionHistory.kicad_sch

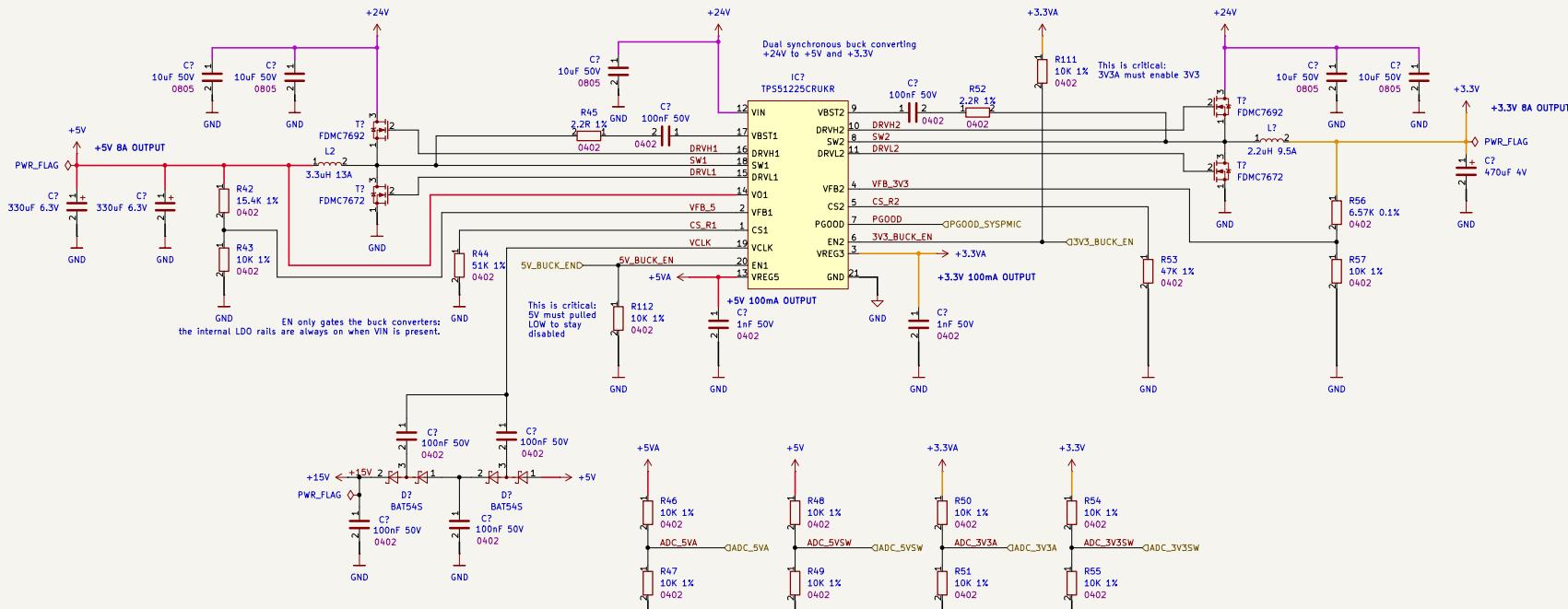
Block Diagram



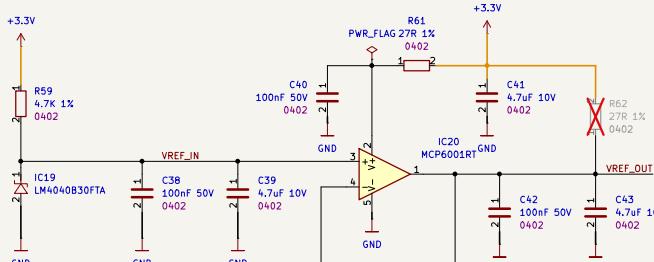
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	Board Name: Baseboard	Project Name: PDNode-600 Pro		
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	Sheet Path: /Block Diagram/	Reviewer:	Size: A3	Sheet: 2 of 15

System Power

+24V TO +3.3V AND +5V CONVERTER



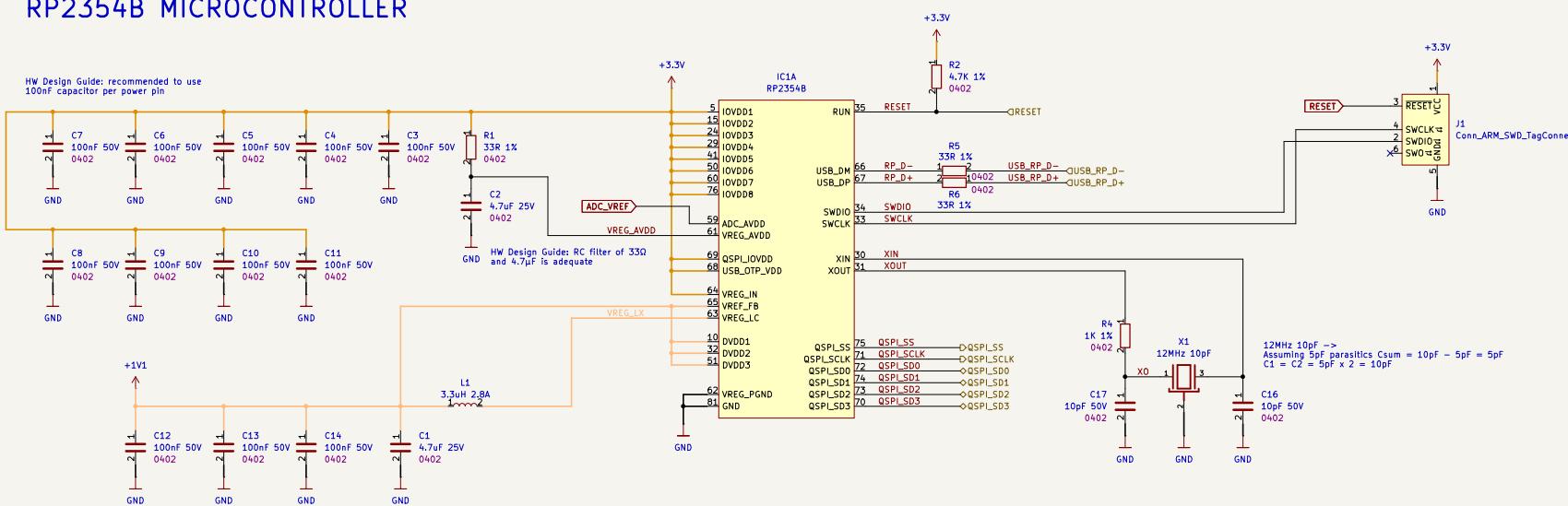
3.0V ADC VOLTAGE REFERENCE



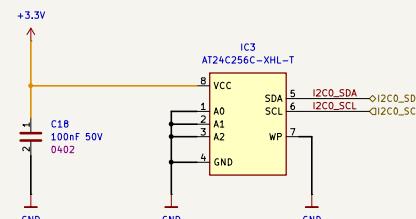
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		DavidMakesThings	Pro	
	Board Name:	Project Name:		
	Baseboard	PDNode-600 Pro		
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Microcontroller Peripherals

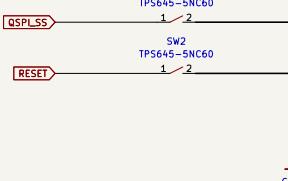
RP2354B MICROCONTROLLER



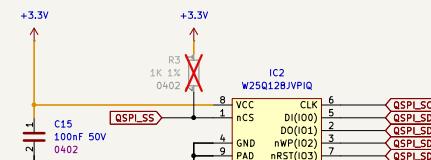
EXTERNAL EEPROM



PUSHBUTTONS



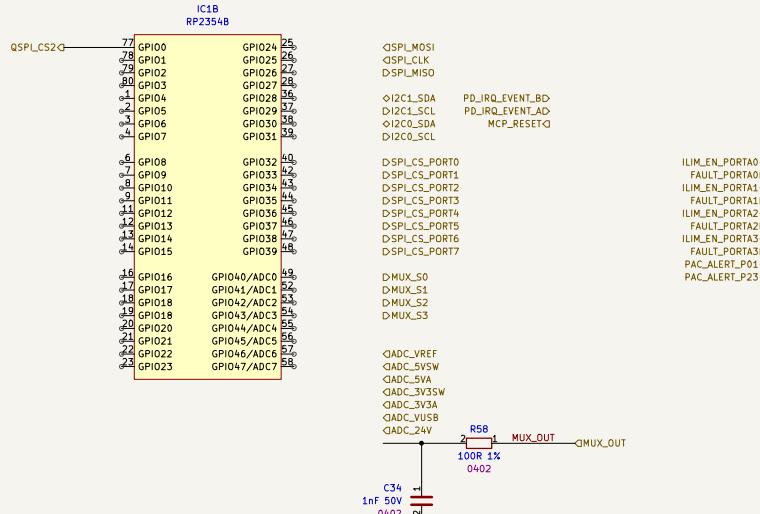
EXTERNAL FLASH



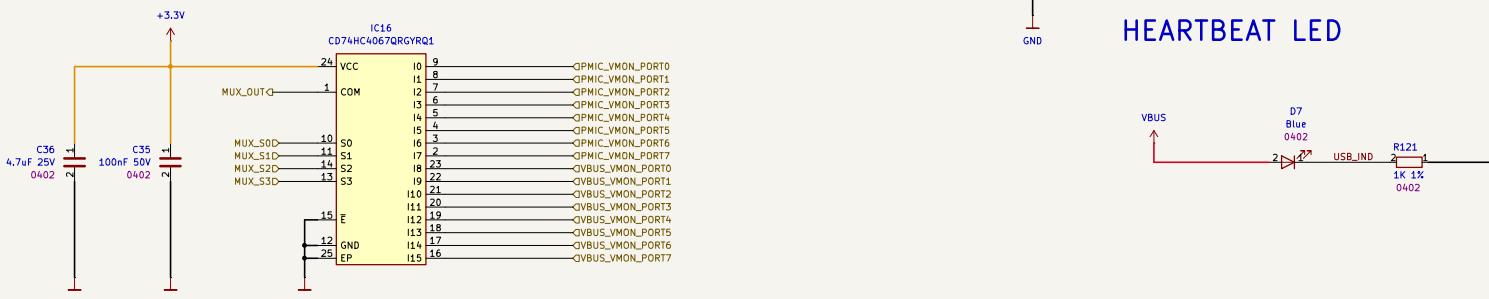
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	Board Name: Baseboard	Project Name: PDNode-600 Pro		
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Microcontroller

RP2354B GPIO ASSIGNMENT



PDC ANALOG SIGNAL MUX

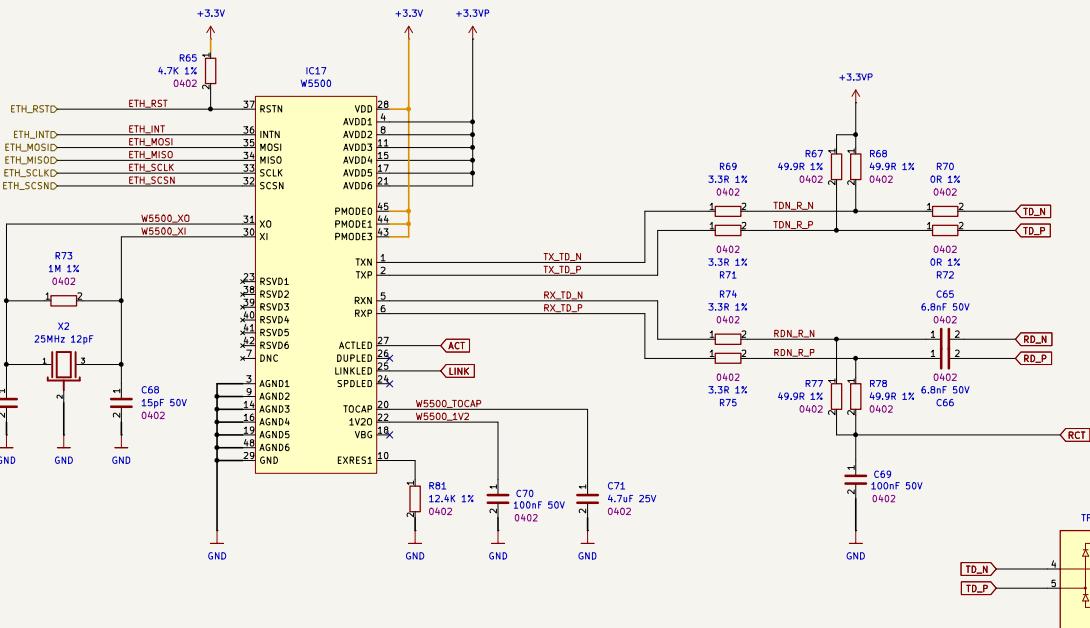


CD74HC4067QRGYRQ1:
 ======
 - Ron 160 Ω max: compared to THE divider (90.9K/10K) Thevenin (-9 kΩ),
 that's a ~1-2% scale up in worst case. If the ADC input were purely
 resistive, there'd be no noise at the ADC input. The mux mostly just charges
 that cap, so the practical impact is smaller.
 - Leakage 800 nA max: into ~9 kΩ source is only ~7 mV worst-case
 equivalent error.
 - Capacitive load (5 pF / 50 pF): 100R + 1 nF kills that nicely.
 Dummy conversion (throwaway sample) after switching helps too.
 $R = 9K + 100R = 9.1K$
 $C = 1nF$
 $t = 2.1 \mu s$
 $\rightarrow 3t = 45\mu s$ after switching the MUX channel is settled

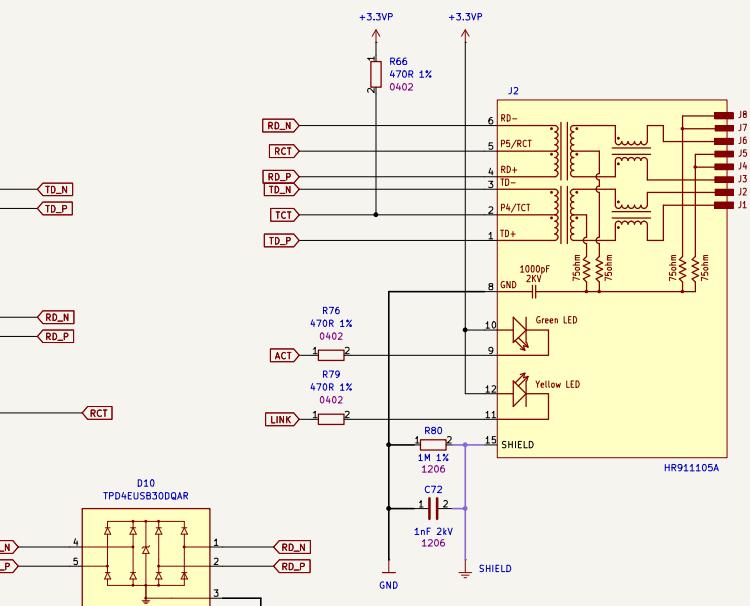
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DvidMakesThings	Pro		
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Sheet Title:	File Name:	Designer:	Date:
Microcontroller	microcontroller.kicad_sch	David Sipes	2026-02-05
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/Block Diagram/Microcontroller/		A3	5 of 15

Ethernet Interface

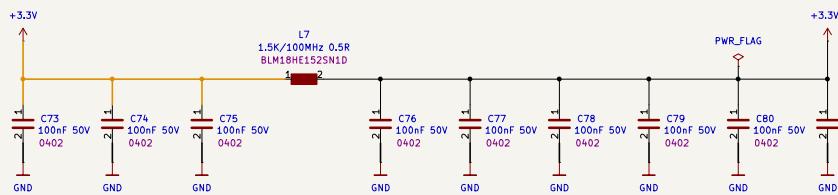
W5500 ETHERNET CONTROLLER WITH PHY



MAGJACK



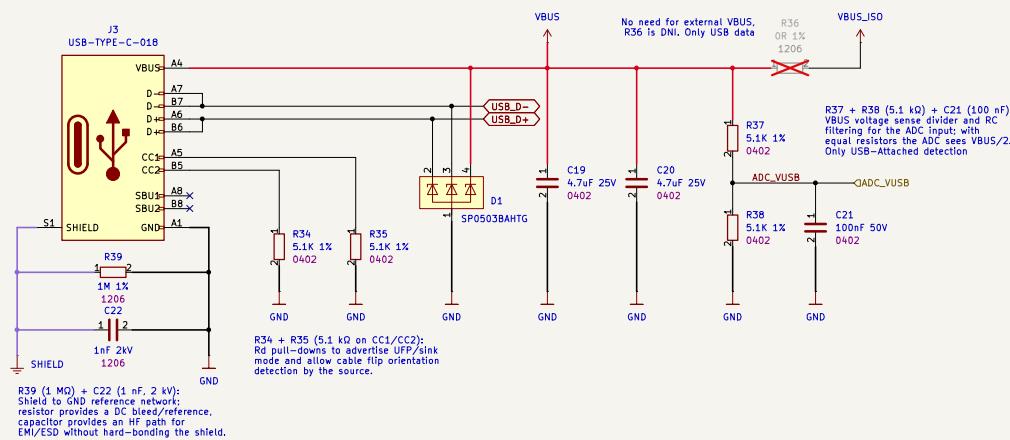
DECOUPLING



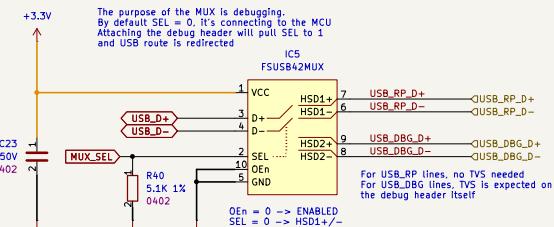
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USB Interface

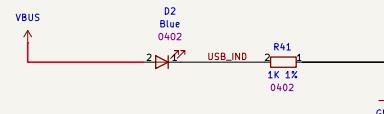
USB-C CONNECTOR



USB DATA-PATH SELECTOR



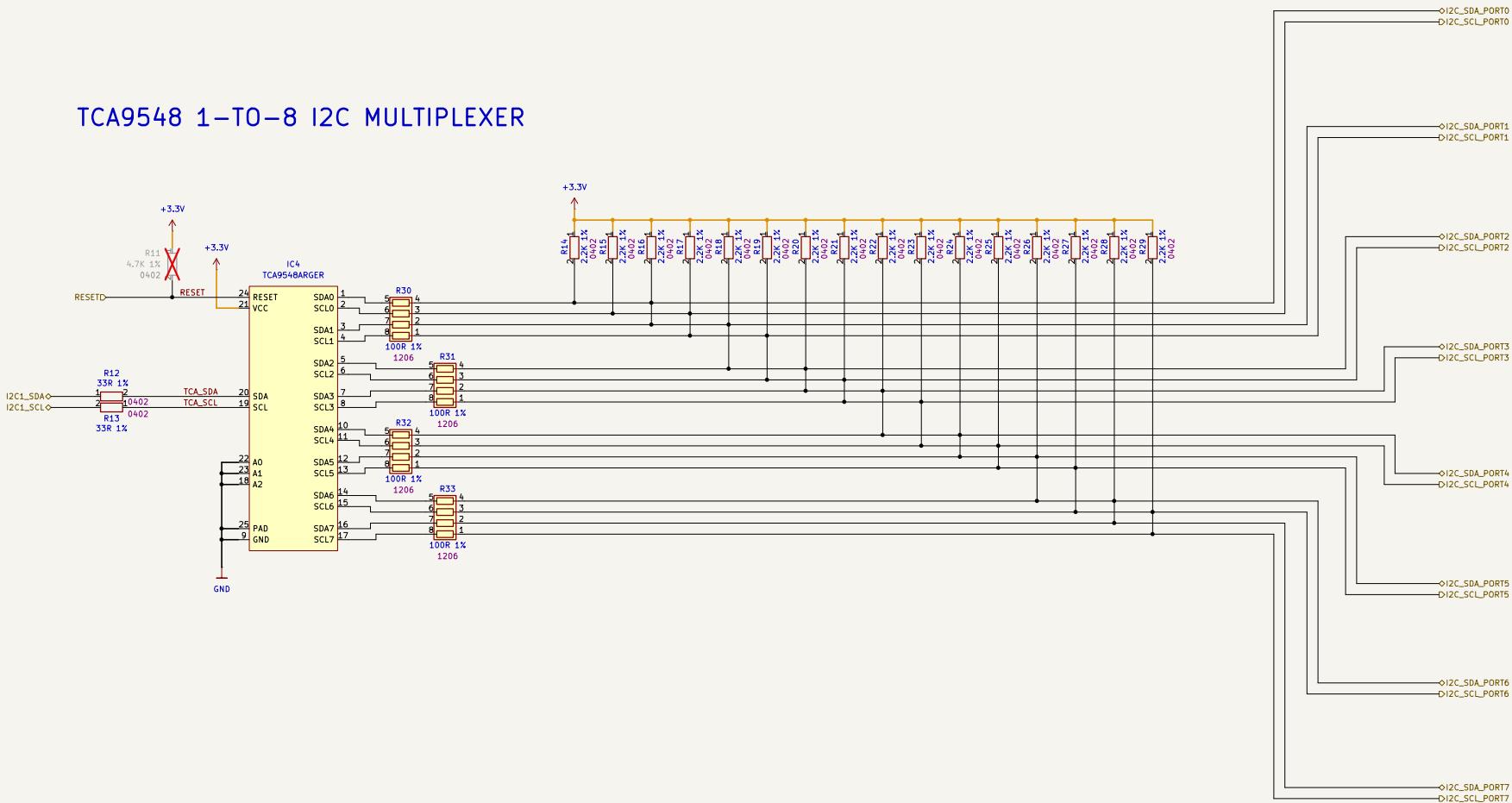
USB INDICATOR LED



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TCA9548 Port Controller

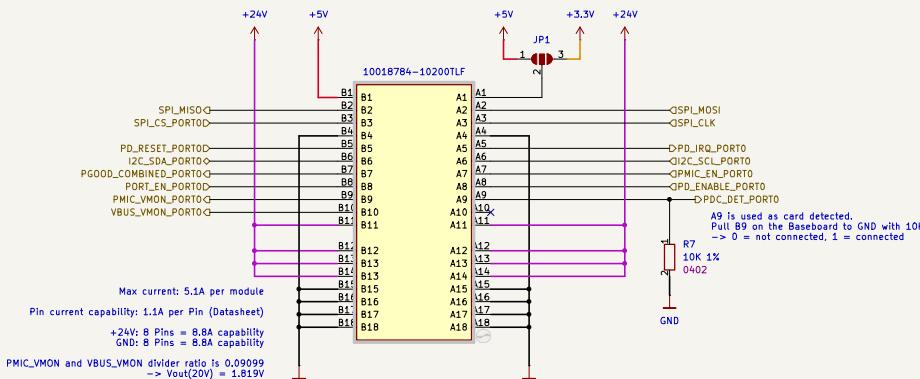
TCA9548 1-TO-8 I2C MULTIPLEXER



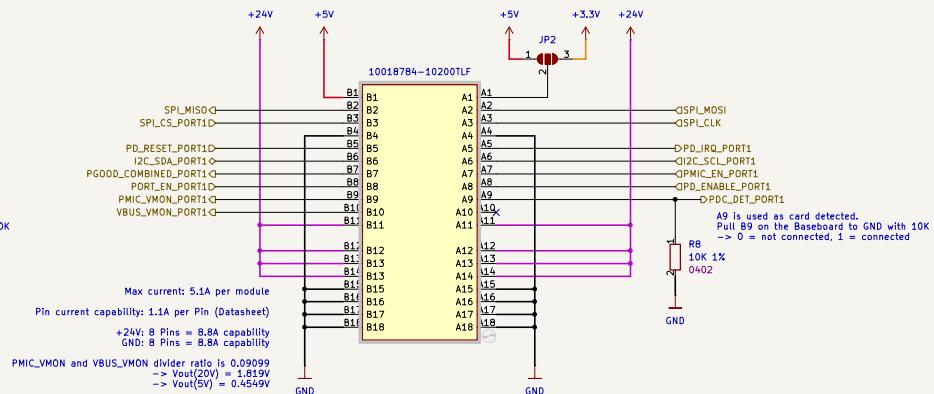
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PDCard Connectors 1-4

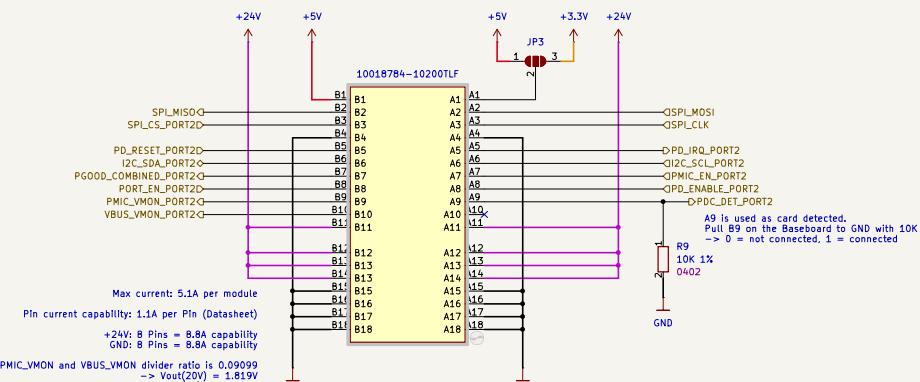
PORT0 PDC CONNECTOR



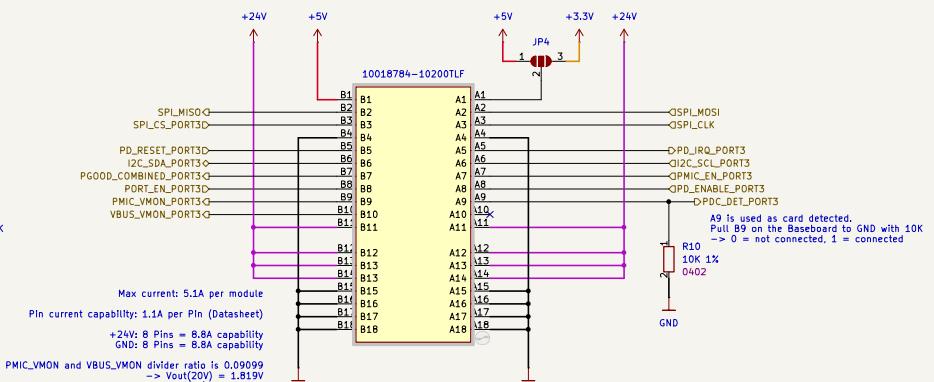
PORT1 PDC CONNECTOR



PORT2 PDC CONNECTOR



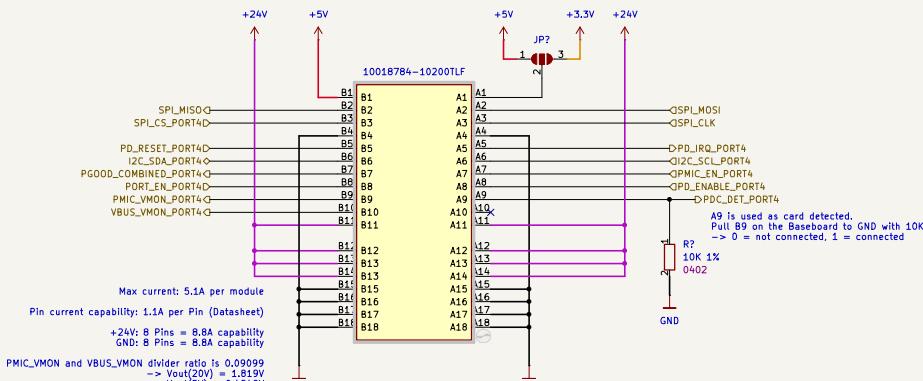
PORT3 PDC CONNECTOR



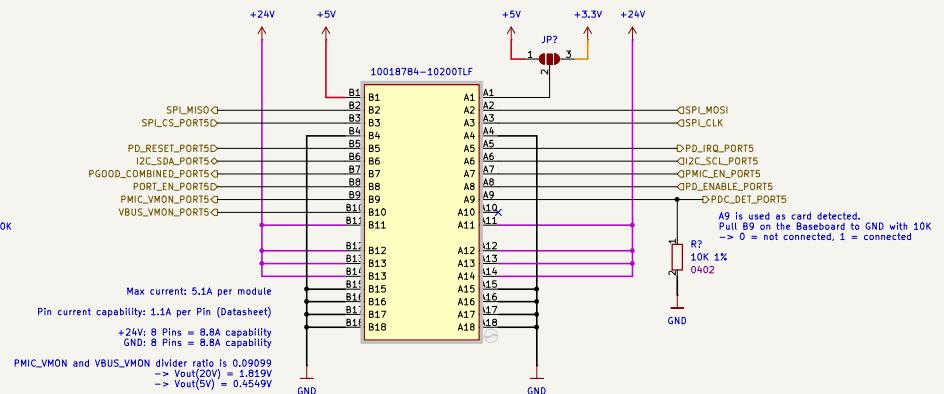
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DavidMakesThings	Pro		
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PDCard Connectors 1-4	pdcard_connectors_1-4.kicad_sch	David Sipes	
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PDCard Connectors 5-8

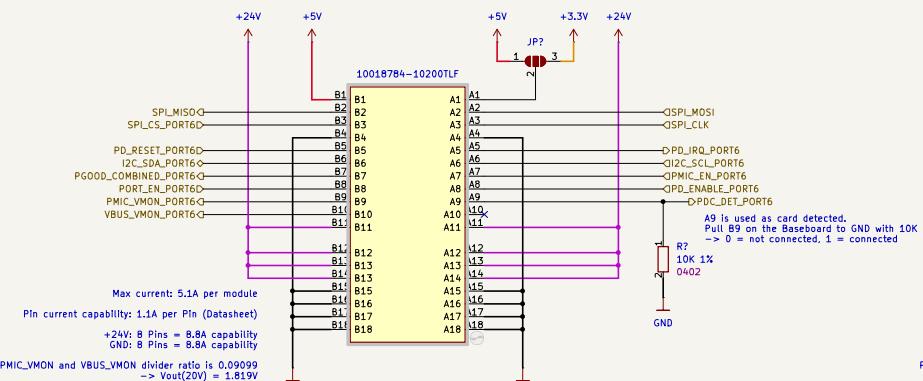
PORT0 PDC CONNECTOR



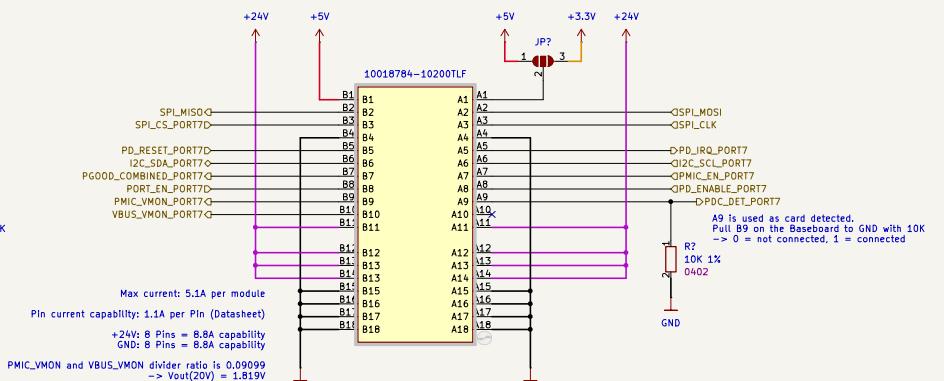
PORT1 PDC CONNECTOR



PORT2 PDC CONNECTOR



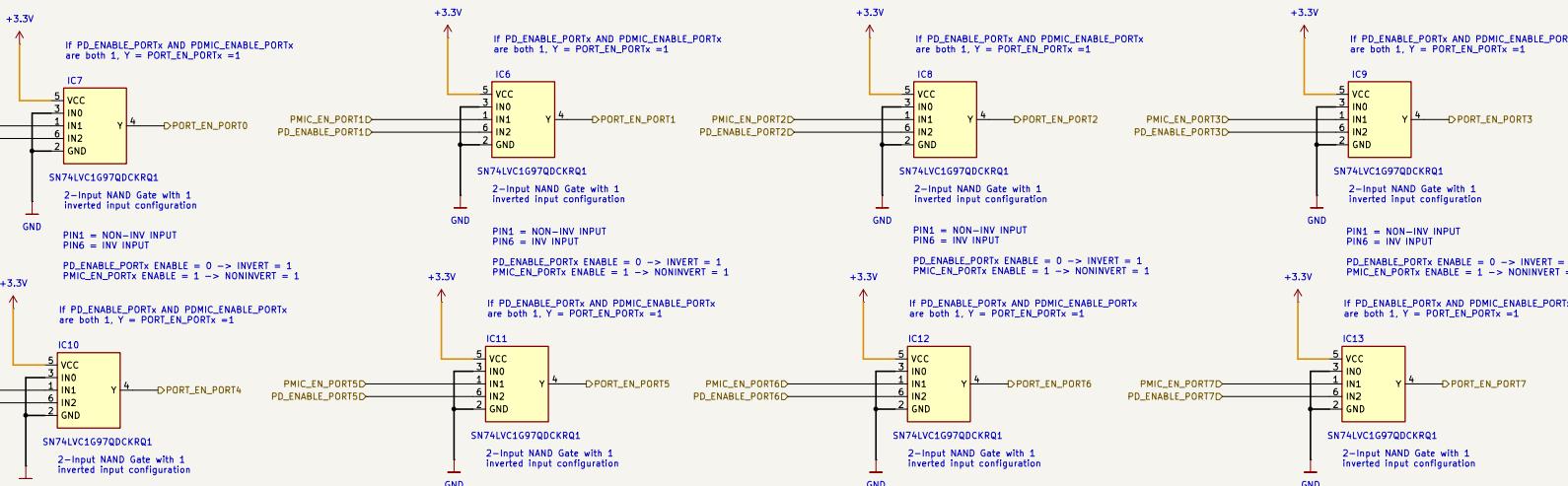
PORT3 PDC CONNECTOR



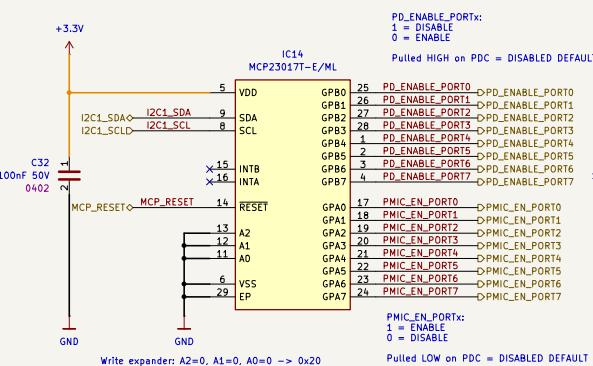
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Board Name:	Baseboard	Project Name:	PDNode-600 Pro
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PDCard Connectors 5-8	pdcard_connectors_5-8.kicad_sch	David Sipes	
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PDC Signals

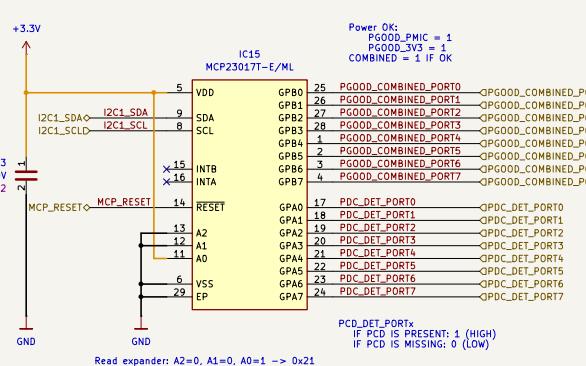
PDC ENABLE LEDS



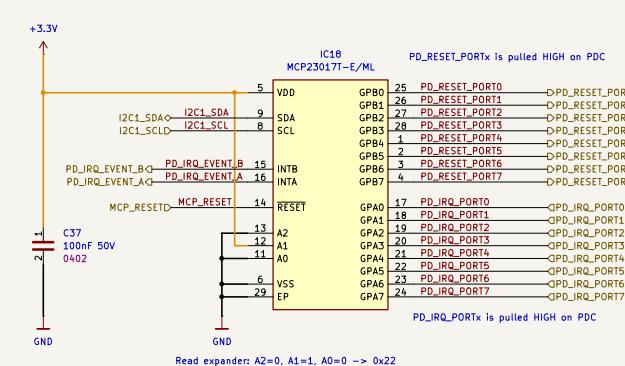
PDC CONTROL SIGNALS (TO WRITE)



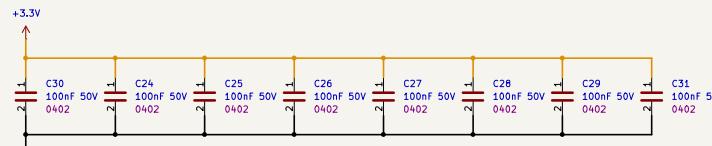
PDC STATUS SIGNALS (TO READ)



PDC RESET AND INTERRUPT



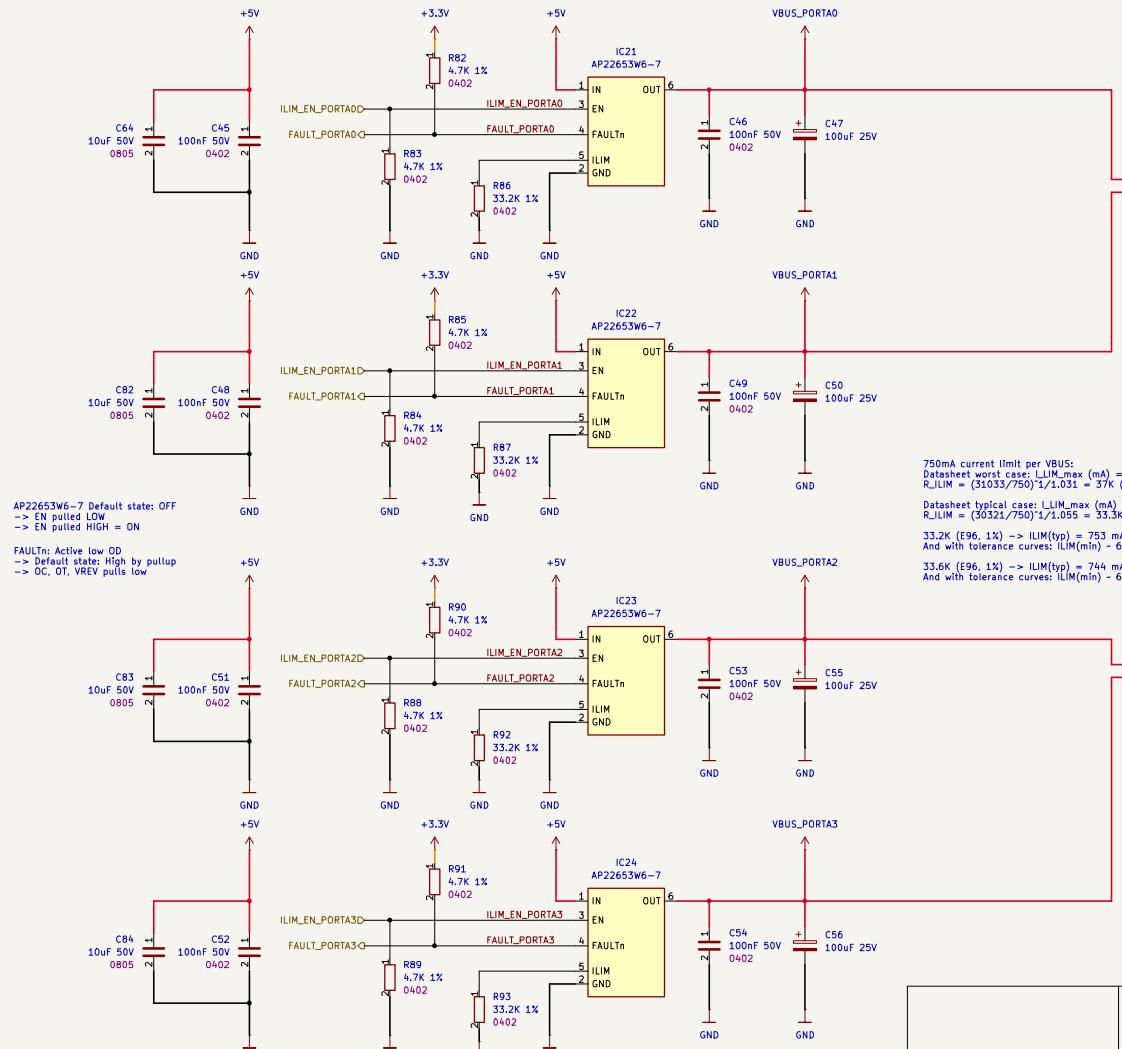
DECOUPLING



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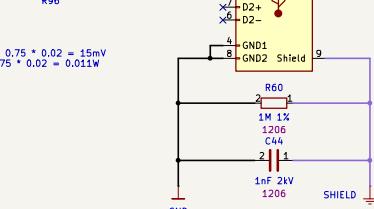
USB-A Outputs

USB CURRENT LIMIT SWITCH

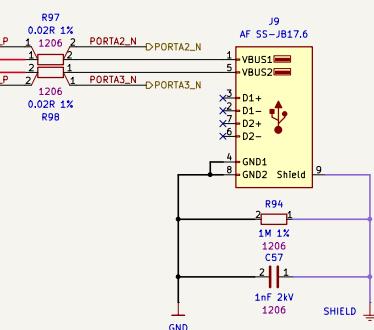


USB-A STACKED OUTPUTS

0.02Ω Shunt:
At 0.75A => $V = 0.75 * 0.02 = 15mV$
 $P = 0.75 * 0.75 * 0.02 = 0.011W$



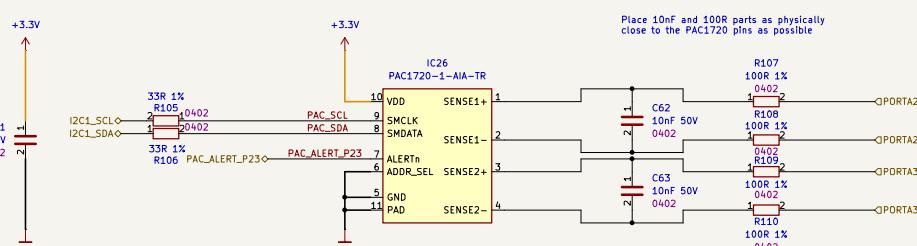
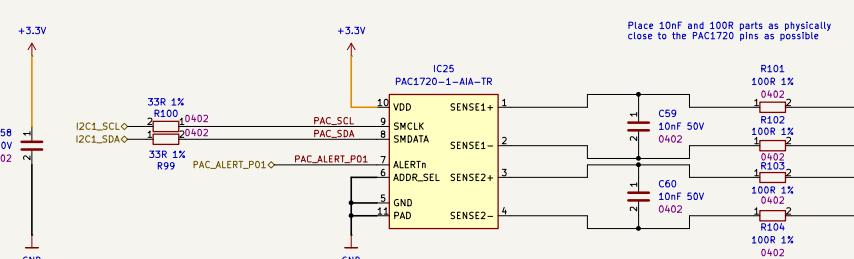
750mA current limit per VBUS:
Datasheet worst case: I_{LIM_max} (mA) = $30321 / R_{JLIM}(K)^{1.055}$
 $R_{JLIM} = (31055/750)^{1/1.055} = 57K$ (750mA max)
Datasheet typical case: I_{LIM_max} (mA) = $30321 / R_{JLIM}(K)^{1.055}$
 $R_{JLIM} = (30321/750)^{1/1.055} = 33.3K$ (750mA max)
33.2K (E96, 1%) => $I_{LIM}(\text{typ}) = 753$ mA
And with tolerance curves: $I_{LIM}(\text{min}) - 671$ mA, $I_{LIM}(\text{max}) - 839$ mA
33.6K (E96, 1%) => $I_{LIM}(\text{typ}) = 744$ mA
And with tolerance curves: $I_{LIM}(\text{min}) - 662$ mA, $I_{LIM}(\text{max}) - 828$ mA



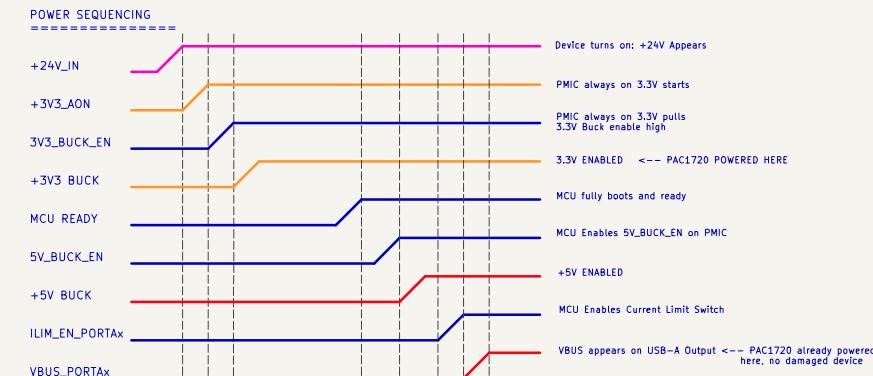
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DvidMakesThings		Pro	
Board Name:	Baseboard	Project Name:	PDNode-600 Pro
Sheet Title:	File Name:	Designer:	Date:
USB-A Outputs	usb-a_outputs.kicad_sch	David Sipes	2026-02-05
Sheet Path:	Reviewer:	Size:	Sheet:
/Block Diagram/USB-A Outputs/		A3	12 of 15

USB-A Current Measurement

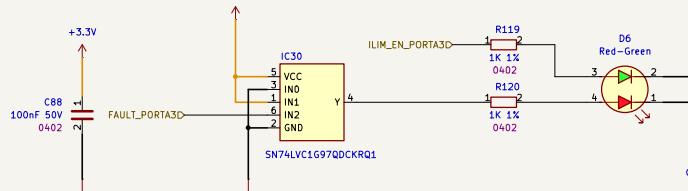
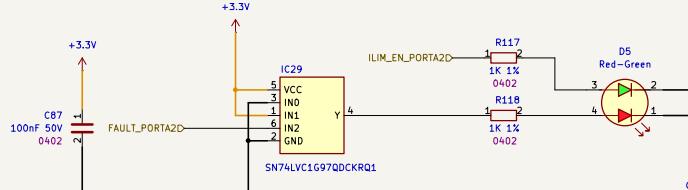
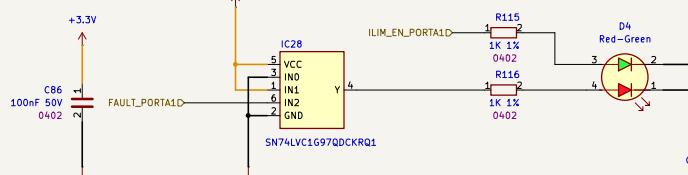
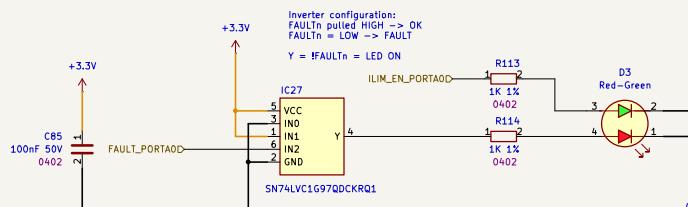
USB CURRENT MEASUREMENT



Biggest landmine with PAC1720:
don't let the SENSE pins see VBUS when VDD is unpowered.
If USB 5V can exist while 3.3V is off, mitigation is needed.
That's the one thing that can turn "happy board" into "dead board."



USB-A CURRENT LIMIT SIGNALS



Comments:	Company:	Variant:	Git Hash:
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Board Name:	Baseboard	Project Name:	PDNODE-600 Pro
Sheet Title:	File Name:	Designer:	Date: 2026-02-05 Revision: 1.0.0
Sheet Path:	Reviewer:	Size:	/Block Diagram/USB-A Current Measurement/

External Power Connectors

1 2 3 4 5 6 7 8

A

B

C

D

E

A

B

C

D

E

	Comments:	Company: DvidMakesThings	Variant: Pro	Git Hash:
	Board Name: Baseboard		Project Name: PDNode-600 Pro	
	Sheet Title: External Power Connectors	File Name: external_power_connectors.kicad_sch	Designer: David Sipes	Date: 2026-02-05 Revision: 1.0.0
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1 2 3 4 5 6 7 8

F

Revision History

DATE	REVISION	RESPONSIBLE	CHANGE
16.01.2026	1.0.0	DMT	INITIAL CREATION
03.02.2026	1.0.1	DMT	24V to 5V and 3.3V PMIC removed for cost reduction

5	Comments:	Company:	DvidMakesThings	Variant:	Pro	Git Hash:
		Board Name:	Baseboard	Project Name:	PDNode-600 Pro	
		Sheet Title:	File Name:	Designer:	Date:	Revision:
		Revision History	Revision History.kicad_sch	David Sipes	2026-02-05	1.0.0
6	Sheet Path:	/Revision History/	Reviewer:	Size:	Sheet:	A3 15 of 15