Ganpat University

Faculty of Engineering & Technology

Computer Science & Engineering

2CSE205: Computer Organization

Name:- Dwij Vatsal Desai

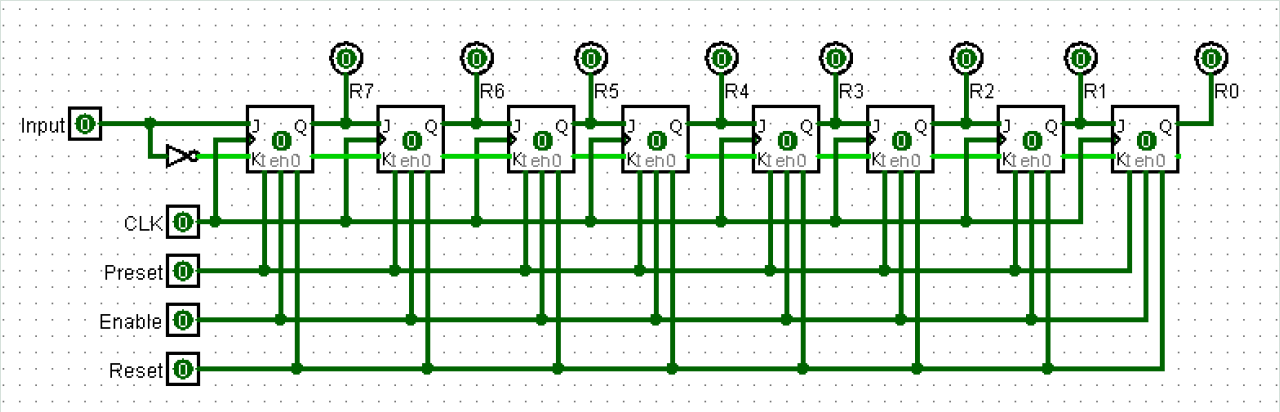
Sem:- 2

Sub: - CO

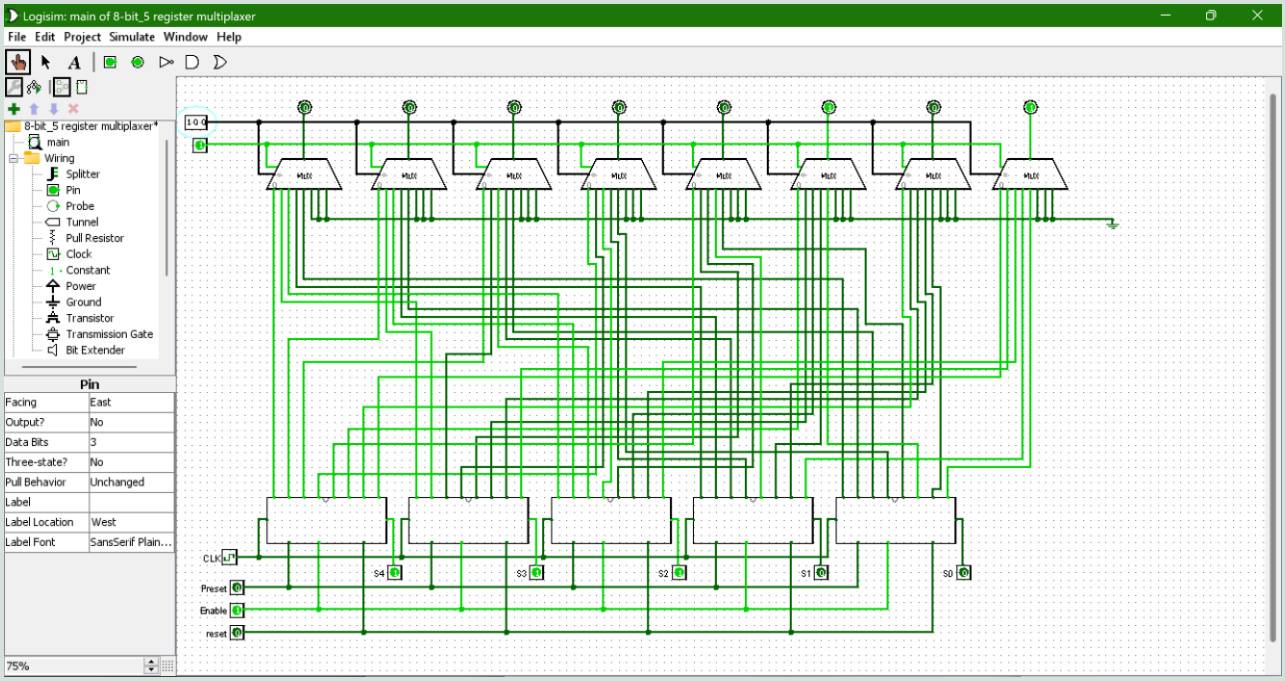
Enrollment No.:- 23162121027

Images:-

→8-Bit register



➔ Final 8-Bit 5 register Bus system


Logisim: main of 8-bit_5 register multiplexer
File Edit Project Simulate Window Help
8-bit_5 register multiplexer*
main
Wiring
Splitter
Pin
Probe
Tunnel
Pull Resistor
Clock
Constant
Power
Ground
Transistor
Transmission Gate
Bit Extender
Pin
Facing East
Output? No
Data Bits 3
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain...
75%
Mux
CLK
Preset
Enable
reset
S4
S3
S2
S1
S0