

# Design Project – PCB Design

Student Name:

Student #:

Student Email:

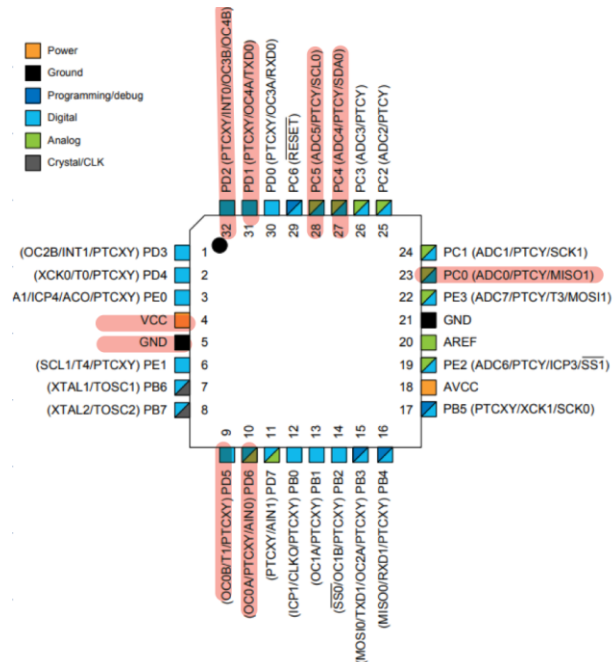
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Primary Github address: [https://github.com/DylanCaz/Submission\\_DA](https://github.com/DylanCaz/Submission_DA)

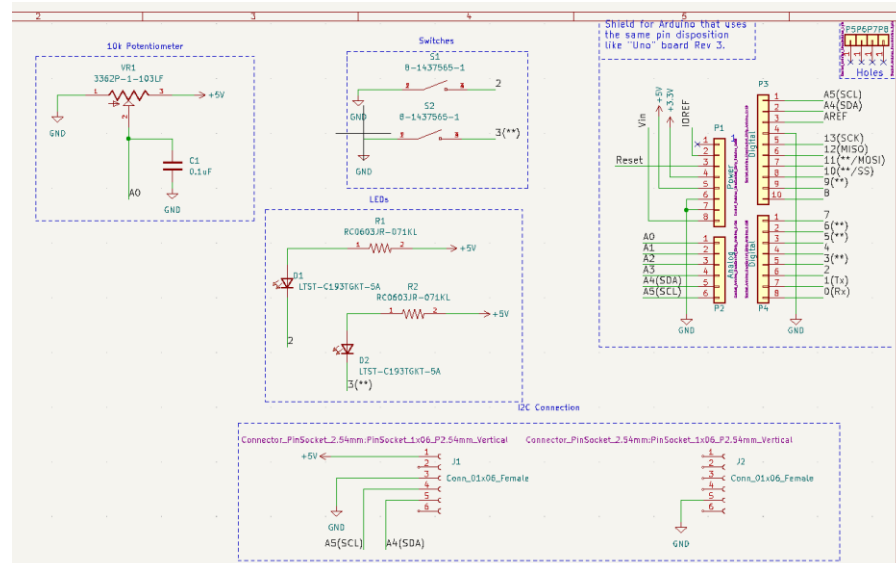
Directory:

[https://github.com/DylanCaz/Submission\\_DA/tree/main/KiCad%20PCB%20Design%20Project](https://github.com/DylanCaz/Submission_DA/tree/main/KiCad%20PCB%20Design%20Project)

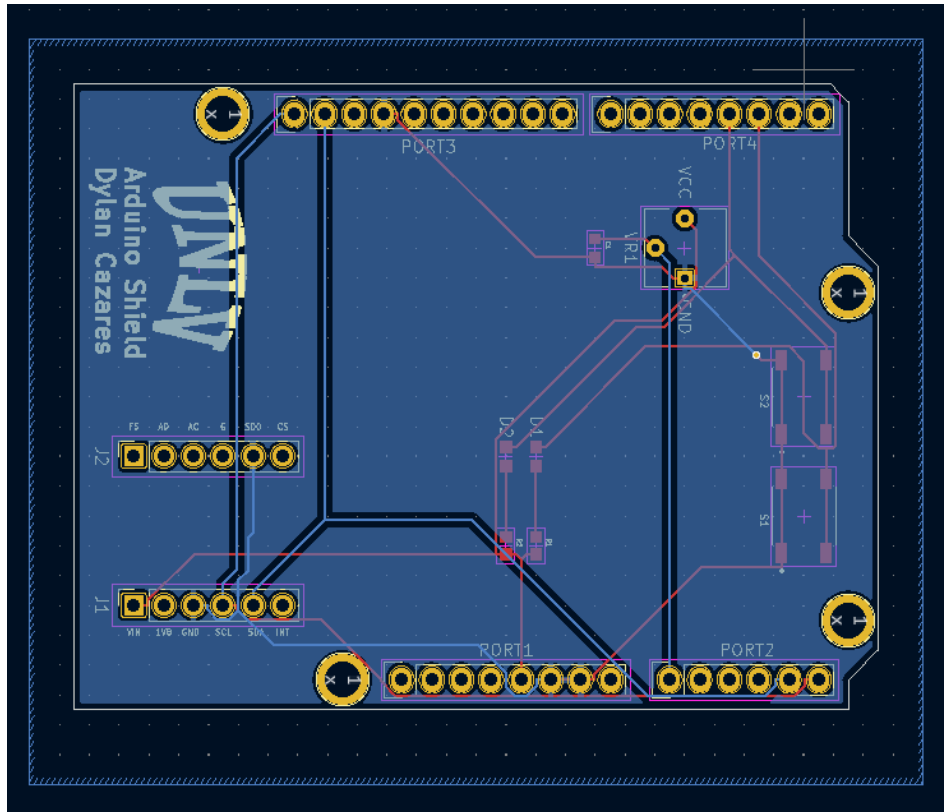
## 1. COMPONENTS LISTS AND CONECTION BLOCK DIAGRAM w/ PINS



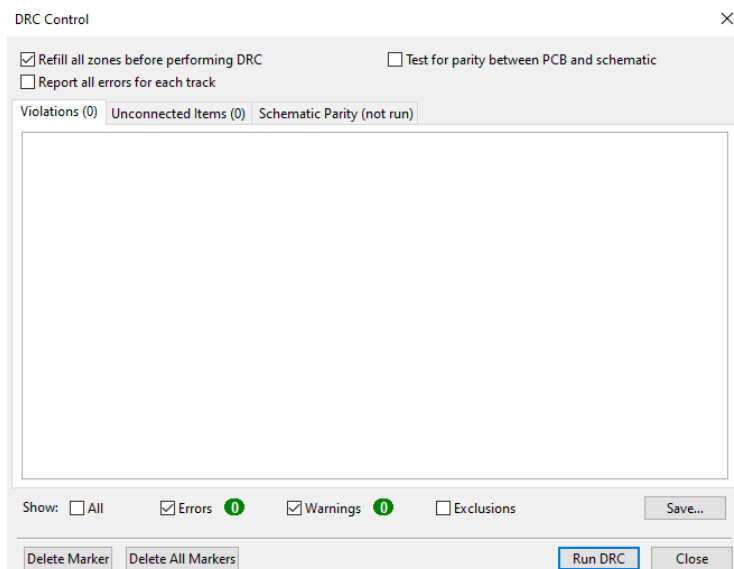
## 2. SCHEMATIC



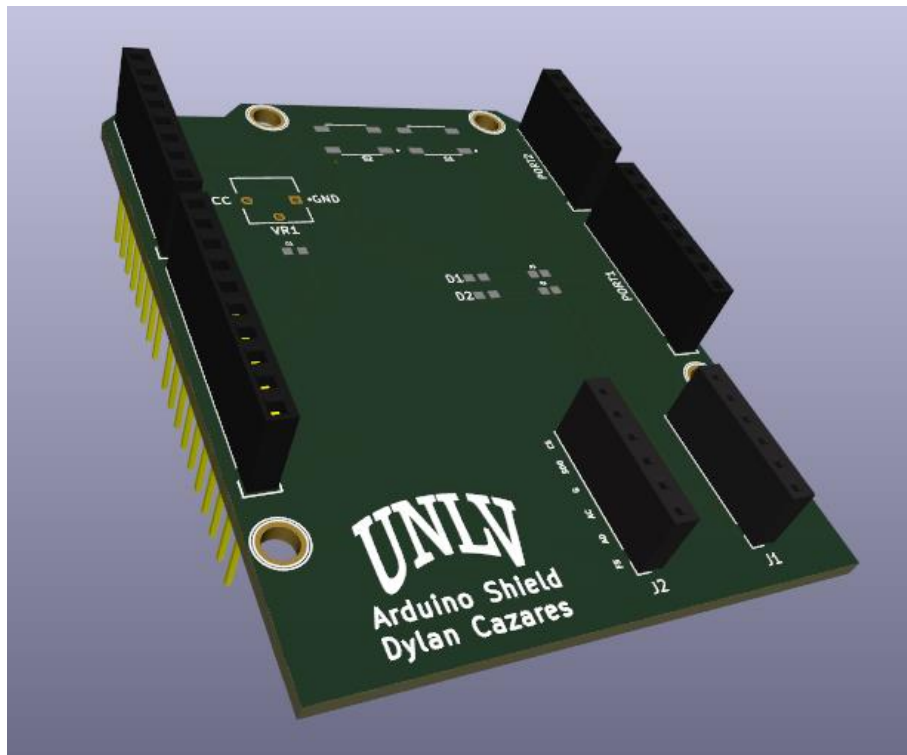
### 3. PCB VIEW



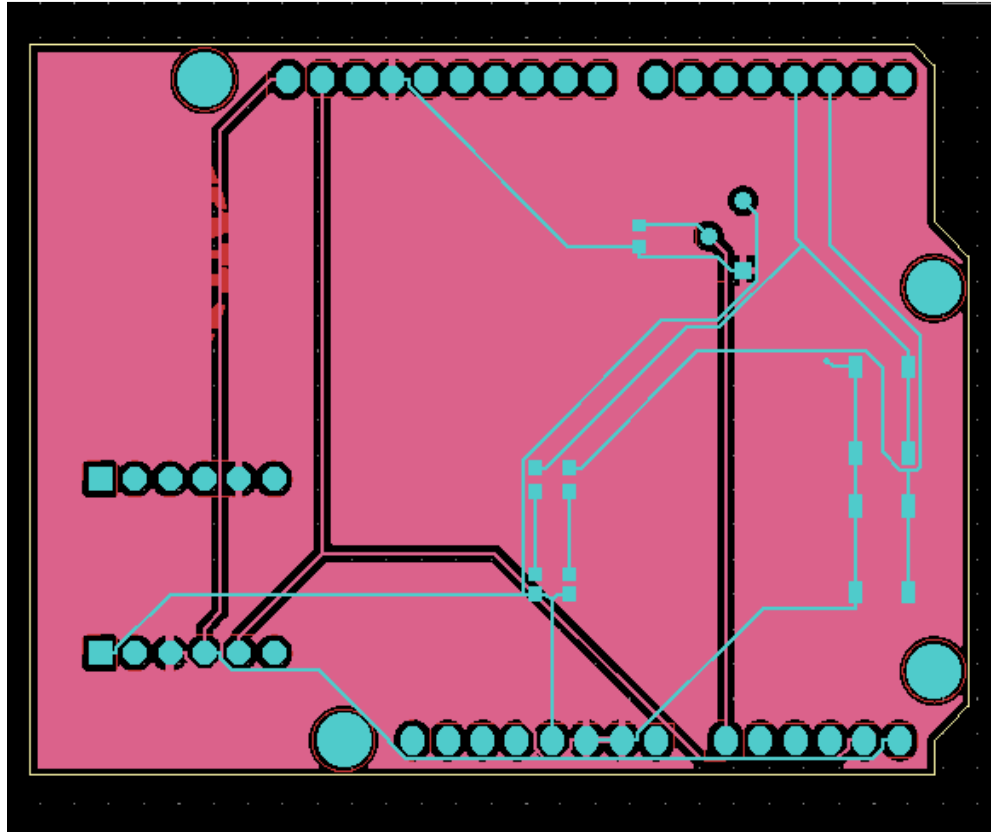
### DRC Checker



#### 4. 3D VIEW



#### 5. GERBER VIEW



**6. GITHUB LINK OF THIS DA**

[https://github.com/DylanCaz/Submission\\_DA/tree/main/KiCad%20PCB%20Design%20Project](https://github.com/DylanCaz/Submission_DA/tree/main/KiCad%20PCB%20Design%20Project)

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<http://studentconduct.unlv.edu/misconduct/policy.html>

*"This assignment submission is my own, original work".*

