

Some Examples of some Port Declarations which can be Matched:

```
input wire [31 : 0] HADDR, // address
```

```
input wire[31:0]HADDR, // address
```

```
output wire[31:0] HRDATA,
```

```
output[15:0]gpio_out0,
```

It can be matched.

```
 #(parameter INCR = 26'd25770)
```

The above, which is not legal syntax, can still be matched, and the parameter name “INCR” will be extracted - the compiler will complain though!

```
 #(parameter INCR = 26'd25770,  
  parameter MAX_NUM_BLKs=5)
```

```
input [7:0][31:0] data_frame,
```

A 2D packed array as an input.

```
input [7:0] [31:0] data_frame,
```

Can still be matched, although the format is 😬.

```
output [0+:8]rdata,
```

```
input[7:0] [31: 0]data_frame ,  
input [ ADDR_WIDTH -1 : 0 ] addr,
```

This can also be matched, and the name of the inputs, “data_frame” and “addr”, will be found.