Class Activity-3

1. **Practice Logic Simplification with Karnaugh Maps:**
   a. $Y(A,B) = AB' + AB + A'B'$
   b. $Y(X,Y,Z) = m0 + m3 + m4 + m6$
   c. Here is a truth table for a specific three-input logic circuit: **Y(A,B,C,)**

A	B	C	Out
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	0

   d. Here is a truth table for a specific four-input logic circuit:

A	B	C	D	Out
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	1