

Class Activity-2

1. Design a combinational logic structure for the below functional expression and write a VHDL code and simulate with the given test cases.

a. $Y = (A'.B.C') + (B'.C') + (A.B.C)$

Test cases= 010, 011, 110, 100, 111

b. $F(W,X,Y,Z) = WXYZ + W'XY'Z + WX'YZ' + W'X'Y'Z'$

Test cases= 1010, 1011, 1101, 1001, 1100

Hint:

- 1. Refer Module: #9**
- 2. Use EDA Playground to simulate the VHDL code**

Good Luck!!!