# **Dylan Grace**

Cell Phone: (219)-363-4774 dylan.grace@valpo.edu

# **EDUCATION**

Valparaiso UniversityValparaiso, INBachelor of Science in Computer EngineeringMay 2023

With a second major in physics Grade Point Average: 3.740/4.000

## **WORK EXPERIENCE**

JJ Babbitt Summer 2021

Manufacturing, Engineering, and Software Development Intern

- Developed custom excel functions using visual basic
- Discovered a mathematical property of mouthpieces that allowed us to obtain perfect coordinates for the facings of mouthpieces
  - o This change brought about consistency and accuracy across the entire facing correct to 0.001"
  - o Implementation provided a 2x-10x improvement to accuracy across the whole facing of the mouthpiece
- Machined the measuring device that implemented our new accuracy standard

### Valparaiso University Hesse Center

2020 – Present

Engineering Tutor

- Assisting students in learning about physics, mathematics, computer engineering, and general engineering subjects.
- Leading weekly help sessions for digital logic and linear circuit theory.

#### RESEARCH

### **QCA Quantum Computing Research**

2019 - Present

- Proved that the method of orthogonal "kink" counting in QCA (Quantum Dot Cellular Automata) generates errors by performing electrostatics calculations on QCA systems
- Presented research at Valparaiso University SOURCE symposium to invalidate the five input QCA majority gate as well as show errors in the coherence vector computational engine inside QCADesigner (the common QCA CAD environment) that was used to validate the five to one majority gate.
- Continuing research in an electrostatics solution as a greedy algorithm for QCA design verification and automatic circuit construction of the most optimized, stable circuit possible from just a truth table.
- Created a C++ desktop application that would allow a user to determine the ground state of QCA systems through an electrostatics calculation.

# **AWARDS**

## IPC Emerging Engineer

2022 – Present

One of 5 students nationally to participate in industry mentorship and to sit on IPC electronics standards committees.

# **ACTIVITIES**

#### **IEEE (Institute of Electrical and Electronics Engineers)**

2019 – Present

Virtual Reality Gloves Project Lead and Founder

#### Valparaiso University Research Society

2021 - Present

Founder and President

#### Sigma Phi Epsilon

2020 – Present

Chaplain

### College of Engineering Organizations Week

Fall 2021

Founder and student director

# IPC

2020 – Present

General Member

# All Parallel CPU Core Design

2020 - Present

Personal Project

#### **SKILLS**

Quartus II,	C, C++, C#,	Python,	Visual Studio,	Unreal Engine,	Quantum Mechanics,	Digital Logic,
OrCAD PSpice,	C++ CLI,	Qiskit,	x86 Assembly,	WinForms,	Computer Architecture,	VHDL
OrCAD Capture,	Visual Basic,	MATLAB,	MIPS Assembly,	MinGW,	Compilers,	ModelSim