RCP CS01.imi Generated by IMITATOR 3.1 "Cheese Artichoke" Build: HEAD/ec224ec Generation time: Fri Jul 23, 2021 10:20:05
 J
 rcv_ack21rcv_idle21rcv_req21

 s1:=0
 s1:=1
 $\begin{array}{ccc}
snd_idle12 & snd_idle12 \\
x1 := 0 & x1 := 0
\end{array}$ snd_idle21 x2 := 0 snd_idle21 x2 := 0Initial 6 clocks | 5 parameters delay > 0& rc_slow_max >= rc_slow_min & rc_slow_min > rc_fast_max fast2 rc_fast_max >= x2 rcv_ack12rcv_idle12rcv_req12 $s\overline{2} := 0$ $s\overline{2} := 1$ $s\overline{2} := 2$ & rc_fast_max >= rc_fast_min & rc_fast_min >= 0 & y21 = 0 slow1 rc_slow_max >= x1 rcv_ack21rcv_idle21rcv_req21 s1 := 0 s1 := 1 s1 := 2bufone21 delay \Rightarrow y21 s21 = 0 s21 = 1 s21 = 2 snd ack21 snd_idle21 snd_req21 S1o2 True S1o3 True S1o4 True bufone12 delay \Rightarrow y12 \Rightarrow s12 = 0 s12 = i s12 = 2 snd ack12 snd_idle12 snd_req12 rc_fast_min
rc_fast_max
rc_fast_max
rc_slow_min
rc_slow_max
delay
s1
sprime12
sprime12
sprime21 & x21 = 0& y12 = 0& x2 = 0& x1 = 0& x12 = 0 $x1 >= rc_slow_min_1 x1 >= rc_st_min_1$ $x2 \ge rc_slow_min_1 x2 \ge rc_st_min_1$ s1 := 2buftwo21 delay >= x21 sprime21 - 0 sprime21 = 1 sprime21 = 2 snd_ack21 snd_idle21 snd_req21 s2 = 2s1 = 1snd_ack12 snd_req12 snd_req21 snd ack21 U almost_root2 7 almost_root1 True rcv ack21 $s\bar{2} := 0$ $s\bar{1} := 0$ almost_child1 True U almost_child2 True

child2 root2 root1

S1oEnd | True