Dylan mondy

## Homework 6

## Microprocessor System Design-ELEN 120 Due 11/6/23 20 points

Problem 1: 10 points

I have configured Timer 2 in a similar manner to the programs provided for part 3 of Lab 5. Except:

The CPU clock and timer clock are set to 24 MHz instead of 4MHz.

The prescaler (TIM PSC) is set to 10000

The timer reload register (TIM ARR) is set to 24

I'm assuming the presentar and reland have values of loods and 24 and are not set in the code

What is the timer 2 interrupt rate?

Using an equation I made in lab = 2xy = 2

for 24 and 10000 (which would

where (= clock , x= presqualar, y= reload, and z= frequency (intermpt rate) 25).

we can calculate the rate.

24 addogo = 20 ps ) (24)(4000)

50 intermpts per second or linterrupt

every 20 ms

Using the same timer with the CPU clock and timer clock set to 40 MHz:

I want the timer interrupt rate to be once per 10 seconds. What values should I use for TIM\_PSC and TIM\_ARR?

For full credit, mathematically show me that your answers are correct.