Circuit Simulation Project

https://esim.fossee.in/circuit-simulation-project

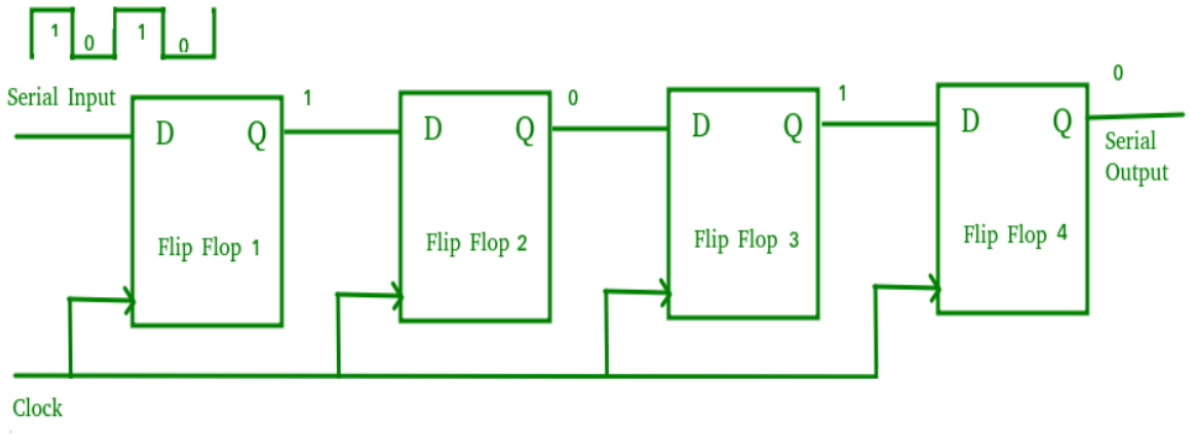
Name of the participant : Dynaneshwari S Jangale

Title of the circuit : Serial In Serial Out shift register

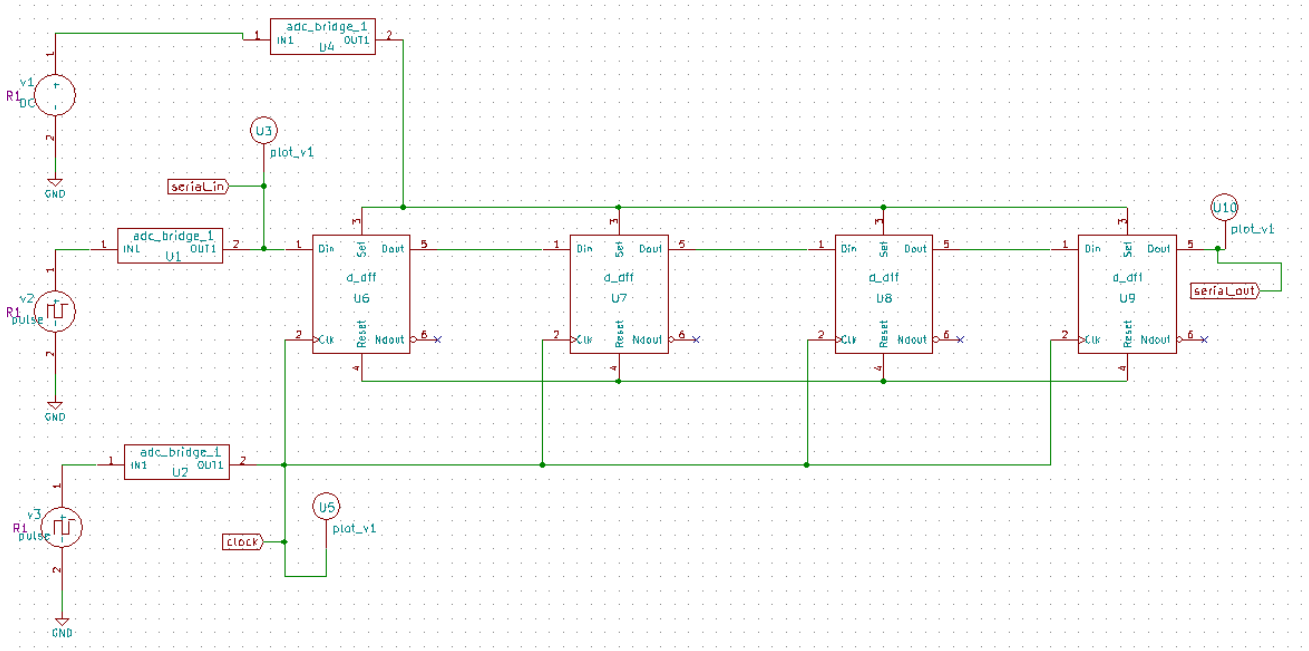
Theory/Description :

The shift register, which allows serial input (one bit after the other through a single data line) and produces a serial output is known as Serial-In Serial-Out shift register. Since there is only one output, the data leaves the shift register one bit at a time in a serial pattern, thus the name Serial-In Serial-Out Shift Register.

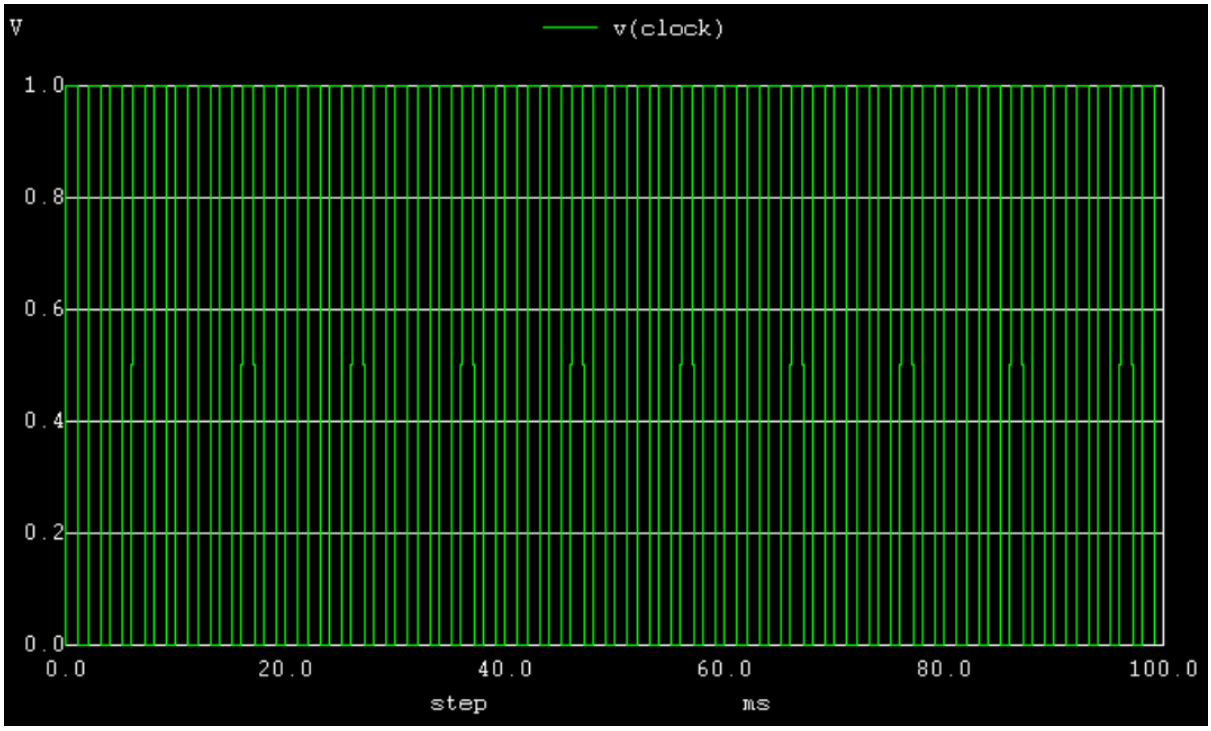
The logic circuit given below shows a serial-in serial-out shift register. The circuit consists of four D flip-flops which are connected in a serial manner. All these flip-flops are synchronous with each other since the same clock signal is applied to each flip flop.

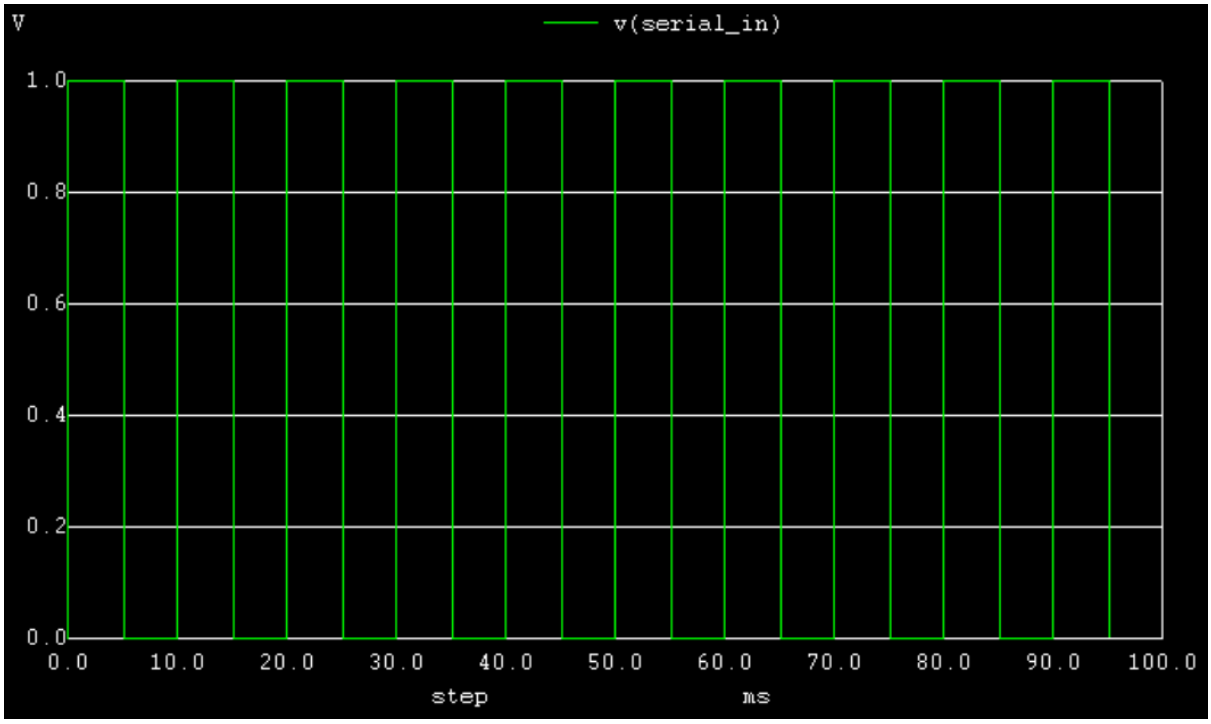


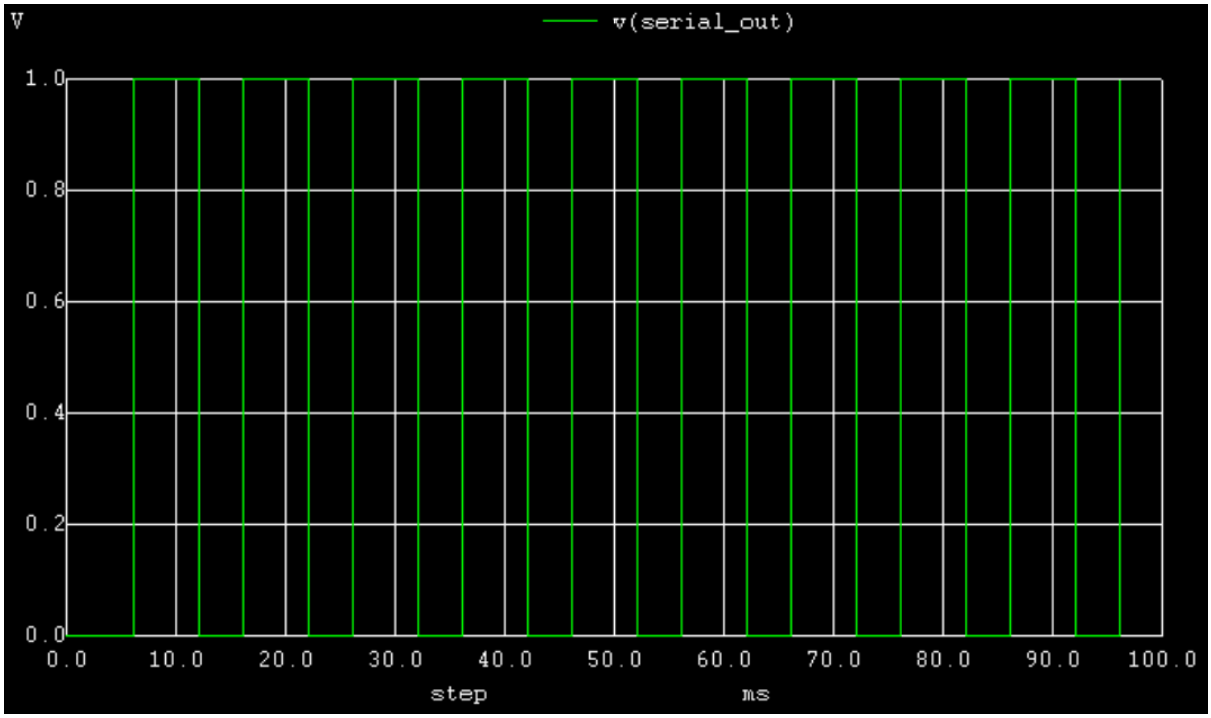
Circuit Diagram(s) :



Results (Input, Output waveforms and/or Multimeter readings) :







Source/Reference(s) : https://www.geeksforgeeks.org/shift-registers-in-digital-logic/