

NO.	ITEM	SOECUFUCATION	NOTES
1-1	Input frequency range	900.0MHz~2150.0MHz	
1-2	One input connector	F Type, Female	
1-3	Nominal input impedance	75 Ohm	
1-4	Tuning circuit	Built in PLL	SP5655
1-5	IF frequency	479.50 MHz center	(TEMIC)
1-6	IF band width	27 MHz nominal	
1-7	Demodulation	Phase locked loop	
1-8	Vedio output polarity	Positive going	
1-9	Operating voltage	+28V(+/-5%)(tuing)	
		+5v (+/-5%)(B+)	
1-10	Operating temprature	-10°C~+60°C	
1-11	Operating humidity	Less than 80% R.H. (at 40°C)	
1-12	Storage temperature	-20°C~+70°C	
1-13	Storage humidity	Less than 95% R.H. (at 40°C)	
1-14	Input level	-60~-30dBm	
2.	Standard test condition	Test for electrical specification	
		shall be preformed at following	
		condition unless otherwise	
		specified.	
2-1	Ambient condition	Temperature 25°C+/-2°C	
		Humidity 65°C+/-5°C	
		If no doubt on test results	
		temperature +5°C~+30°C and	
		humidity 45%~80% R.H could	
		be appllied.	
2-2	Measurement to start	30 minutes after DC power supplied.	
2-3	Power supply	Terminal Supply voltage	
		LNB power	
		+5V +5V(+/-)0.1V	
		+28V +28V(+/-)0.1V	
		SDA specified tuning	
		SCL pulse	

BSS479LAIDF

NO. **ITEM** Specification NOTES 3. Current consumption Terminal MIN. TYP. MAX. 190 240 290 +5V mA +28V 0.5 1.0 3.0 mΑ MAX. Supply voltage Terminal 4. Absolute maximum voltage LNB power DC +25V DC +5.25V +5V +28V DC +30V SDA,SCL 0V to the same voltage as +5V terminal Terminal Max. take off current LNB power 500mA B.B output 0.5mA 5. Electrical specification Under standard test condition test channel:DBS 20 CH input level :-45dBm unless otherwise specified. MIN. TYP. MAX. Condition 5-1. Input VSWR 900MHz~2150MHz 2.0 3.0 AGC 5-2. Noise figure 900.0 MHz fullgain ~2150 MHz 8.0 12.0 dB 5-3. Local leakage at input 900MHz~1750MHz -63 -70 terminal 1750MHz~2150MHz -50 dBm 1 5-4. Tuning voltage curve MHz 1.6 900 950 MHz 2.2 1150 MHz 4.0 5.0 1250 MHz 1450 MHz 7.0 9.2 1650 MHz 12.1 1850 MHz 15.9 2050 MHz 2150 MHz 21.0 26.6

COMTECH TECHNOLOGY CO., LTD.

BSS479LAIDF

NO	ITEM	Specification							NOTES	
		Condition				MIN.	TY	P. N	MAX.	
5-5.	Local oscillator	Tuing voltage			Г		+	\top		
	+B shift	shift with								
		+B +/ -5%					+-1	0		MHz
5-6.	Local oscillator	Tunir	ıg volta	ige						
	temperature drift	shift v	vith							
		-10°C	C~+60°	°C			+-1	0		MHz
5-7.	IF 3dB bandwidth						27			MHz
5-8.		Cent	er Erro	r (f0)		-1	+	+	+1	
			er Volta	` ′		0.14	2.	5	4.88	
		Sens		J		2	3	3	4	
	Window AFT input on P6				_					
		A2 A1 A0			Freque	uency Vo		age		
		1 0 0			Too Low 3		3 ~	13.2V		
		0 1 1			Correct 2.		l .	5 ~ 3 V		
		0 1 0			l .		l	~ 2.25V		
		0	0	1 0				l	5 ~ 1.5V ~ 0.75V	
		U	0			lioo nig	Jo riigii O.		~ 0.75	
5-9.	B.B output									
	characteristics	 Video	o wave	form w	/hi	ite 100°	% pa	l		
	(1) Video output level	l				16MHz	•			
		witho	ut pre-	empha	si	S				
			e to syı	-	$\overline{}$	0.55	0.75		0.95	Vp-p
	(0) 0 : (1 1 7 7
	(2) Gain-frequency				•	uency :			ИHz	
	response	without energy dispasal mod								
		reference freq. 100				KHz IF	BW	27M	lHz	
		Freq.	respo	onse			+-	1	+-3	dB
	(3) Group delay response				— Hz	 z~8MHz				
		l	•	-		asal mo		ion		
		refere	ence fr	eq. 100	OKHz IF BW 27MHz			lz_	ncoc	
		Grou	р				+	-10	+-50	nsec

NO	ITEM	Spec	Specification							
	(4) DG/DP	10 step staircase	10 step staircase 16MHz p-p PAL							
		without energy d								
		positive video ar								
		should be applie								
		DG (APL 50%)		2	2	%				
		DP (APL 50%)		2	5.0	0				
	(5) SN RATIO	Input C/N =14dB	3 (noise	BW 27N	1Hz)					
		white 100% vide	o 16MF	Hz p-p P/	٩L					
		with audio subca	arrier mo	odulation	1					
		3.4MHz p-p DE\	/. @6.5	MHz pos	sitive					
		video amplifier w	vith de-e	emphasi	s should					
		be applied 100H	z~5MH	z unweig	ghted SN					
		FOR:power on re	eset ind	licator						
		SN	34.0	36.0		dB				
	(6) Static threshole			6.0	8.0					
						dB				
7-1.	Signal level out voltage	(V)Signal								
		level out $\frac{4}{3}$								
		2								
				-60 -80		47K OHM				
		IN	NPUT 1	LEVEL	(dBM)	loaded				
7.0	IAOO DOLL					loadod				
<i>1-</i> 2.	I^2C BSU		44	li4i						
	(1) SDA,SCL	Under standard		_	MAN					
	Input voltage	Condition	MIN.	TYP.	MAX.					
		High voltage	3		5	V				
	(2) Addross	Low voltage	0 2 format	<u> </u>	1.5	-				
	(2) Address	C2 (on write data	a ioima	L)						
	(3) SDA,SCL	SDA/SCL are in	ance and							
	Input impedance									
	, , , , , , , , , , , , , , , , , , , ,	5V continually or								
		supply is switche	•							
		L								

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(4) Data format

	MSE				LSB
П					

Address	1	1	0	0	0	MA1	MA0	0	Α	BYTE1
programmable		14	13	12	11	10	9	8		
divider	0	2	2	2	2	2	2	2	Α	BYTE2
Programmable	7	6	5	4	3	2	1	0		
divider	2	2	2	2	2	2	2	2	Α	BYTE3
Charge pump								(0)		
and test	1	СР	T1	T0	1	1	1	os	Α	BYTE4
I/O port control										
bits	P7	P6	P5	P4	P3	P2	P1	P0	Α	BYTE5

Table 1 write data format (MSB is transmitted first)

Address	1	1	0	0	0	MA1	MA0	0	Α	BYTE1
Status byte	POR	FL	12	I 1	10	A2	A1	Α0	Α	BYTE2

Table 2 read data format

A:acknowledge bit.

MA1,MA0:voltage address bits.

CP:charge pump current select.

T1:test mode selection.

T0:charge pump disable.

OS:varactor drive output disable switch.

P7,P6,P5,P4,P3,P2,P1,P0:control output states.

POR:power on reset indicator

FL:phase lock detect flag.

I2,I1,I0:digital information from ports P7,P5 and P4

A2,A1,A0:5 level ADC data from P6.

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