SD:

#define GPIO\_SPI\_SD GPIOB

**#define GPIO\_Pin\_CS GPIO\_Pin\_12**

#define GPIO\_Pin\_SPI\_SD\_SCK GPIO\_Pin\_13

#define GPIO\_Pin\_SPI\_SD\_MISO GPIO\_Pin\_14

#define GPIO\_Pin\_SPI\_SD\_MOSI GPIO\_Pin\_15

DCIM:

/\*Sửa chân lại\*/

PA4 - HREF (HSYNC),

PA6 - PCLK (PIXCLK)

PB6 - D5,

PB7 - VSYNC,

PE5 - D6,

PE6 - D7

PA9 - D0,

PA10 - D1,

PE0 - D2,

PE1 - D3,

PC11 - D4

PA8 - XCLK

I2C :PA8, PC9 // I2C3\_Pinpack 1

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource0, GPIO\_AF\_FSMC); // D2

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource1, GPIO\_AF\_FSMC); // D3

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource4, GPIO\_AF\_FSMC); // NOE -> RD

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource5, GPIO\_AF\_FSMC); // NWE -> WR

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource7, GPIO\_AF\_FSMC); // NE1 -> CS

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource8, GPIO\_AF\_FSMC); // D13

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource9, GPIO\_AF\_FSMC); // D14

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource10, GPIO\_AF\_FSMC); // D15

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource11, GPIO\_AF\_FSMC); // A16 -> RS

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource14, GPIO\_AF\_FSMC); // D0

GPIO\_PinAFConfig(GPIOD, GPIO\_PinSource15, GPIO\_AF\_FSMC); // D1

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource7, GPIO\_AF\_FSMC); // D4

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource8, GPIO\_AF\_FSMC); // D5

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource9, GPIO\_AF\_FSMC); // D6

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource10, GPIO\_AF\_FSMC); // D7

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource11, GPIO\_AF\_FSMC); // D8

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource12, GPIO\_AF\_FSMC); // D9

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource13, GPIO\_AF\_FSMC); // D10

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource14, GPIO\_AF\_FSMC); // D11

GPIO\_PinAFConfig(GPIOE, GPIO\_PinSource15, GPIO\_AF\_FSMC); // D12

RTC:I2C2: B10,B11

BH1750:I2C1: B8,B9

DHT: A10

BT:USART6: C6,C7