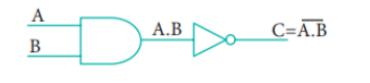


1 NAND Gate

The NAND (NOT AND) Gate is an inverted AND Gate. It reads as $C = \text{NOT of } A \text{ AND } B$. The NAND Gate gives a false output (result of 0) only when both inputs are true (1).



1.1 NB

The NAND Gate is a universal gate because it can be used to form any other kind of gate.

