Solved Problems On Rectifiers

BASIC ELECTRONICS SOLVED PROBLEMS BY SASMITA JANUARY 9, 2020

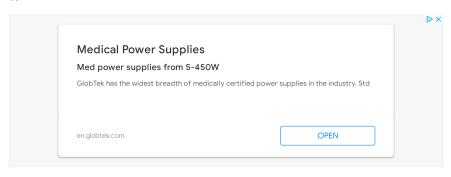


- Q1. The applied input a.c. power to a half-wave rectifier is 100 watts. The d.c. output power obtained is 40 watts.
- (i) What is the rectification efficiency?
- (ii) What happens to remaining 60 watts?



Solution:

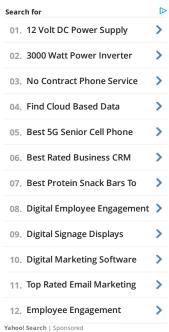
(i)



Rectification efficiency = $\frac{\text{d.c. output power}}{\text{a.c. input power}} =$

(ii) 40% efficiency of rectification does not mean that 60% of power is lost in the rectifier circuit. In fact, a crystal diode consumes little power due to its small internal resistance. The 100 W a.c. power is contained as 50 watts in positive half-cycles and 50 watts in negative half-cycles. The 50 watts in the negative half-cycles are not supplied at all. Only 50 watts in the positive half-cycles are converted into 40 watts.

$$\therefore \qquad \text{Power efficiency} = \frac{40}{50} \times 100 = 80\%$$



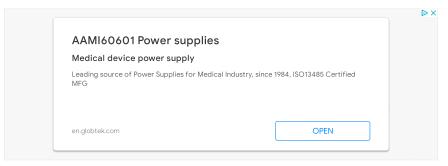
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Although 100 watts of a.c. power was supplied, the half-wave rectifier accepted only 50 watts and converted it into 40 watts d.c. power. Therefore, it is appropriate to say that efficiency of rectification is 40% and not 80% which is power efficiency.

Q2. An a.c. supply of 230 V is applied to a half-wave rectifier circuit through a transformer of turn ratio 10:1. Find (i) the output d.c. voltage and (ii) the peak inverse voltage. Assume the diode to be ideal.

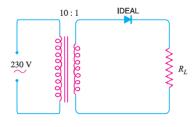


Fig. 1

Solution:

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Primary to secondary turns is

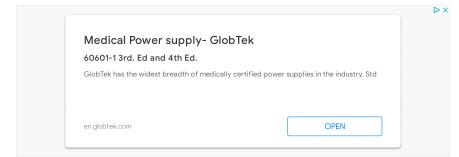
$$\frac{N_1}{N_2} = 10$$

R.M.S. primary voltage

$$= 230 \text{ V}$$

:. Max. primary voltage is

$$V_{pm} = (\sqrt{2}) \times \text{r.m.s.}$$
 primary voltage
= $(\sqrt{2}) \times 230 = 325.3 \text{ V}$



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$$V_{sm} = V_{pm} \times \frac{N_2}{N_1} = 325.3 \times \frac{1}{10} = 32.53 \text{ V}$$

(i)

$$I_{dc.} = \frac{I_m}{\pi}$$

 $V_{dc} = \frac{I_m}{\pi} \times R_L = \frac{V_{sm}}{\pi} = \frac{32.53}{\pi} = 10.36 \text{ V}$

(ii) During the negative half-cycle of a.c. supply, the diode is reverse biased and hence conducts no current. Therefore, the maximum secondary voltage appears across the diode.

.. Peak inverse voltage = 32.53 V

Q3. A crystal diode having internal resistance rf = 20Ω is used for half-wave rectification. If the applied voltage v = $50 \sin \omega$ t and load resistance RL= 800Ω , find :

(i) Im, Idc, Irms (ii) a.c. power input and d.c. power output (iii) d.c. output voltage (iv) efficiency of rectification.

Solution:

$$v = 50 \sin \omega t$$

$$\therefore \text{ Maximum voltage, } V_m = 50 \text{ V}$$

(i)

$$I_m = \frac{V_m}{r_f + R_L} = \frac{50}{20 + 800} = 0.061 \,\text{A} = 61 \,\text{mA}$$

$$I_{dc} = I_m/\pi = 61/\pi = 19.4 \,\text{mA}$$

$$I_{rms} = I_m/2 = 61/2 = 30.5 \,\text{mA}$$

(ii)

a.c. power input =
$$(I_{rms})^2 \times (r_f + R_L) = \left(\frac{30.5}{1000}\right)^2 \times (20 + 800) = 0.763$$
 watt

d.c. power output =
$$I_{dc}^2 \times R_L = \left(\frac{19.4}{1000}\right)^2 \times 800 = 0.301$$
 watt

(iii)

d.c. output voltage =
$$I_{dc}R_L$$
 = 19.4 mA × 800 Ω = 15.52 volts

(iv)

Efficiency of rectification =
$$\frac{0.301}{0.763} \times 100 = 39.5\%$$

Q4. A half-wave rectifier is used to supply 50V d.c. to a resistive load of 800 Ω . The diode has a resistance of 25 Ω . Calculate a.c. voltage required.

Solution:

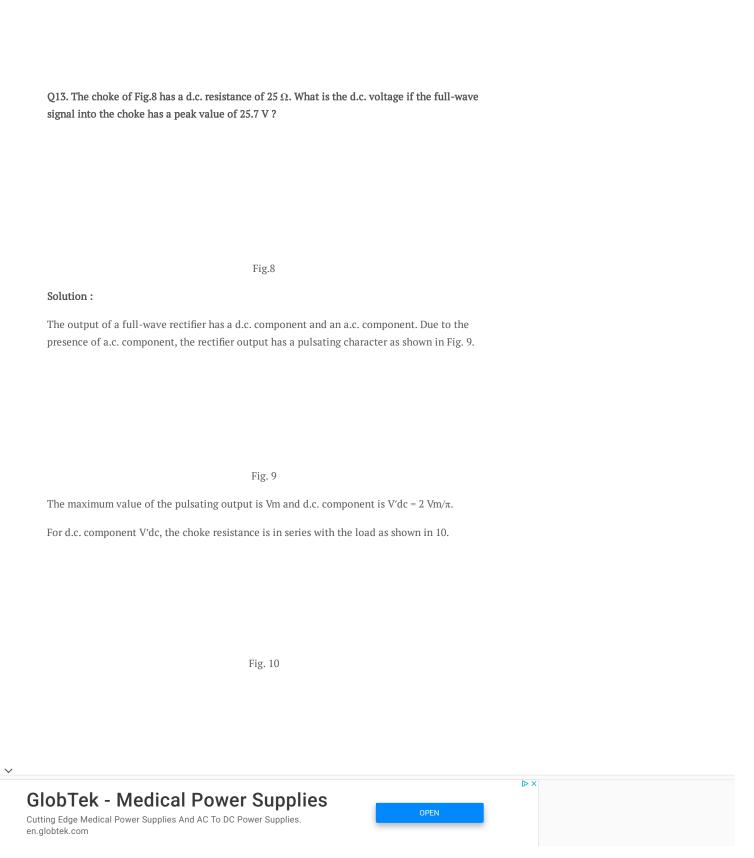
Q5. A full-wave rectifier uses two diodes, the internal resistance of each diode may be assumed constant at $20~\Omega$. The transformer r.m.s. secondary voltage from centre tap to each end of secondary is $50~V$ and load resistance is $980~\Omega$. Find : (i) the mean load current (ii) the r.m.s. value of load current.
Solution:
(i)
(ii)
Q6. In the centre-tap circuit shown in Fig. 2, the diodes are assumed to be ideal i.e. having zero internal resistance. Find :(i) d.c. output voltage(ii) peak inverse voltage (iii)
rectification efficiency.
Fig. 2
Solution:
Primary to secondary turns, $N1 / N2 = 5$
(i) Average current, Idc =
(ii) The peak inverse voltage is equal to the maximum secondary voltage, i.e
(iii)
D X

d.c. output voltage (ii) peak in secondary turns to be 4.				
	Fig.3			
olution :				
Q8. Fig. 4(i) and Fig. 4 (ii) sho	w the centre-tap and bridge type	circuits having the same	e load	
esistance and transformer tur	w the centre-tap and bridge type in ratio. The primary of each is co	onnected to 230V, 50 Hz		
resistance and transformer tur	n ratio. The primary of each is co e in each case. (ii) PIV for each c	onnected to 230V, 50 Hz		
resistance and transformer tur supply. (i) Find the d.c. voltag	n ratio. The primary of each is co e in each case. (ii) PIV for each c	onnected to 230V, 50 Hz		
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This shows that for the same secondary voltage, the d.c. output voltage of bridge circuit is twice that of the centre-tap circuit	
(ii) PIV for same d.c. output voltage :	
The d.c. output voltage of the two circuits will be the same if Vm (i.e. max. voltage utilized by each circuit for conversion into d.c.) is the same. For this to happen, the turn ratio of the transformers should be as shown in Fig. 5.	
Fig.5	
Centre-tap circuit:	
Bridge circuit :	
This shows that for the same d.c. output voltage, PIV of bridge circuit is half that of centre-tap circuit. This is a distinct advantage of bridge circuit.	
Q9. The four diodes used in a bridge rectifier circuit have forward resistances which may be considered constant at Ω and infinite reverse resistance. The alternating supply voltage is	
240 V r.m.s. and load resistance is 480 Ω . Calculate (i) mean load current and (ii) power dissipated in each diode.	
Solution:	
040 mt 1 11 - 70 - 1 - 1 mt 2 - 11 - 1 1 mt 1 / 1 - 7 - 7 - 7 - 7	

Bridge circuit :

Fig.6
Solution:
The conditions of the problem suggest that the a.c voltage across transformer secondary is 12V r.m.s.
Q11. A power supply A delivers 10 V dc with a ripple of 0.5 V r.m.s. while the power supply B delivers 25 V dc with a ripple of 1 mV r.m.s. Which is better power supply ?
Solution:
The lower the ripple factor of a power supply, the better it is.
For power supply A
For power supply B
Q12. For the circuit shown in Fig.7, find the output d.c. voltage.
Fig.7
Solution:
It can be proved that output d.c. voltage is given by :



Sasmita

Hi! I am Sasmita . At ElectronicsPost.com I pursue my love for teaching. I am an M.Tech in Electronics & Telecommunication Engineering. And, if you really want to know more about me, please visit my "About" Page.

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