ECE260B-W20 Lab 2. Synthesis

A530XXXXX Delvin Huynh (XXXXX@ucsd.edu)

Directory: /home/linux/ieng6/ee260bwi20/<username>/lab2 (Your simulation directory. Note: Do NOT delete log files after simulation)

Follow strictly with the tables, you can write your explanation and other answers below the table for each assignment.

Total cell area	
Total dynamic power	
Cell leakage power	
The worst slack	

	bc	tc	wc
Р			
V			
Т			
X8 FO4 delay			
X8 FO4 delay (avg. of rise & fall)			

	tc	bc
Total cell area		
Total dynamic power		
Cell leakage power		
The worst slack		

	Hierarchical	Flatten
Total cell area		
Total dynamic power		
Cell leakage power		
The worst slack		

	Physical-aware	Non-Physical aware
Total cell area		
Total dynamic power		
Cell leakage power		
The worst slack		