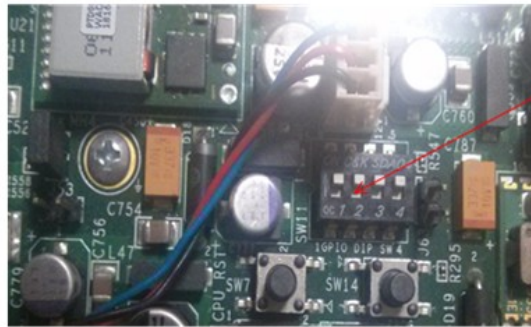


TEMAC configuration ON KC705

Binwei Deng
2014/10/4

Hardware configuration:

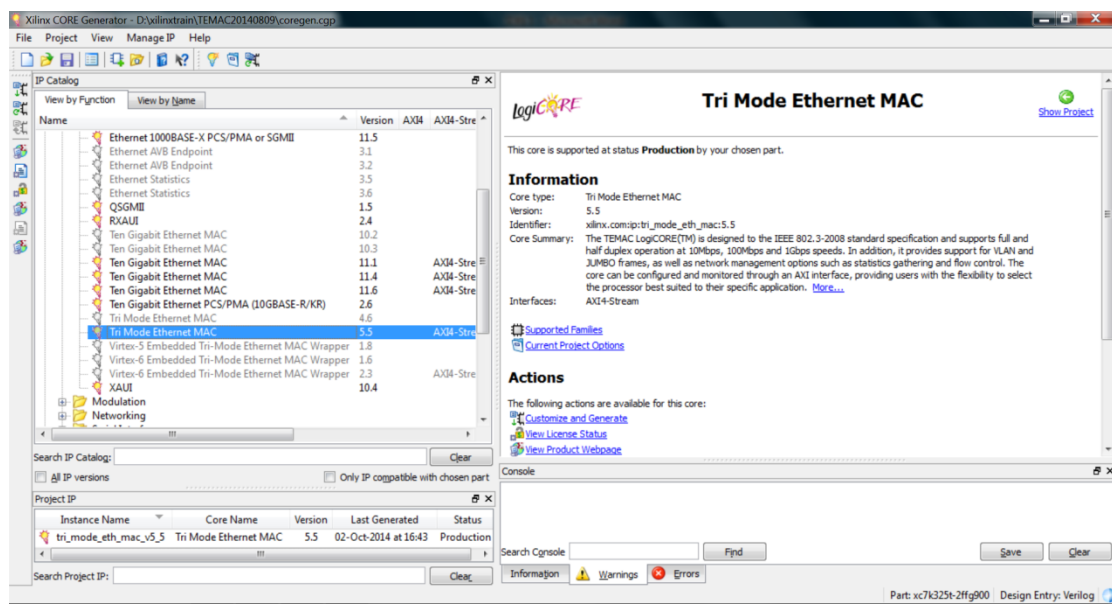


SW11

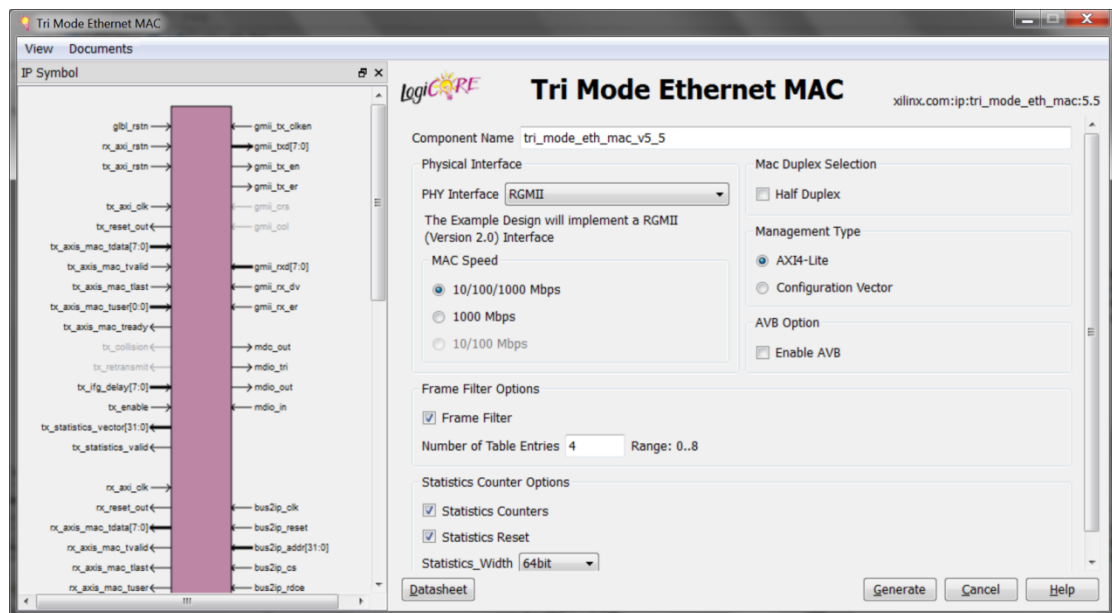


J64,J29,J30

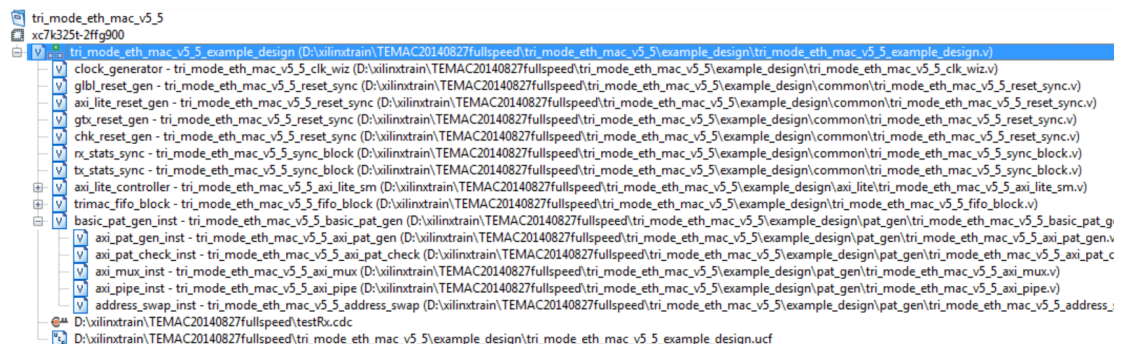
(1) ISE14.5 → IPCORE-→ Three mode Ethernet MAC 5.5



(2) TEMAC configuration



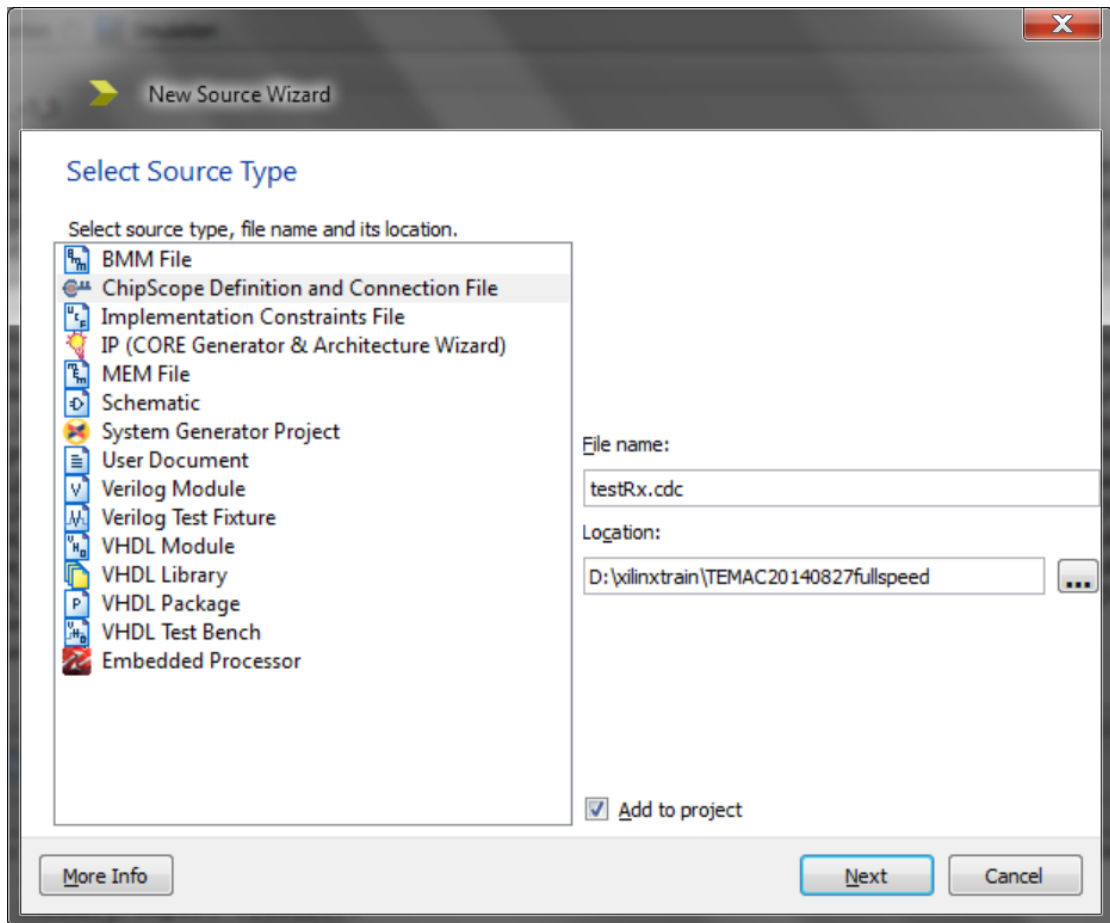
(3) add tri_mode_eth_mac_v5_5_example_design.v as top module and add others files



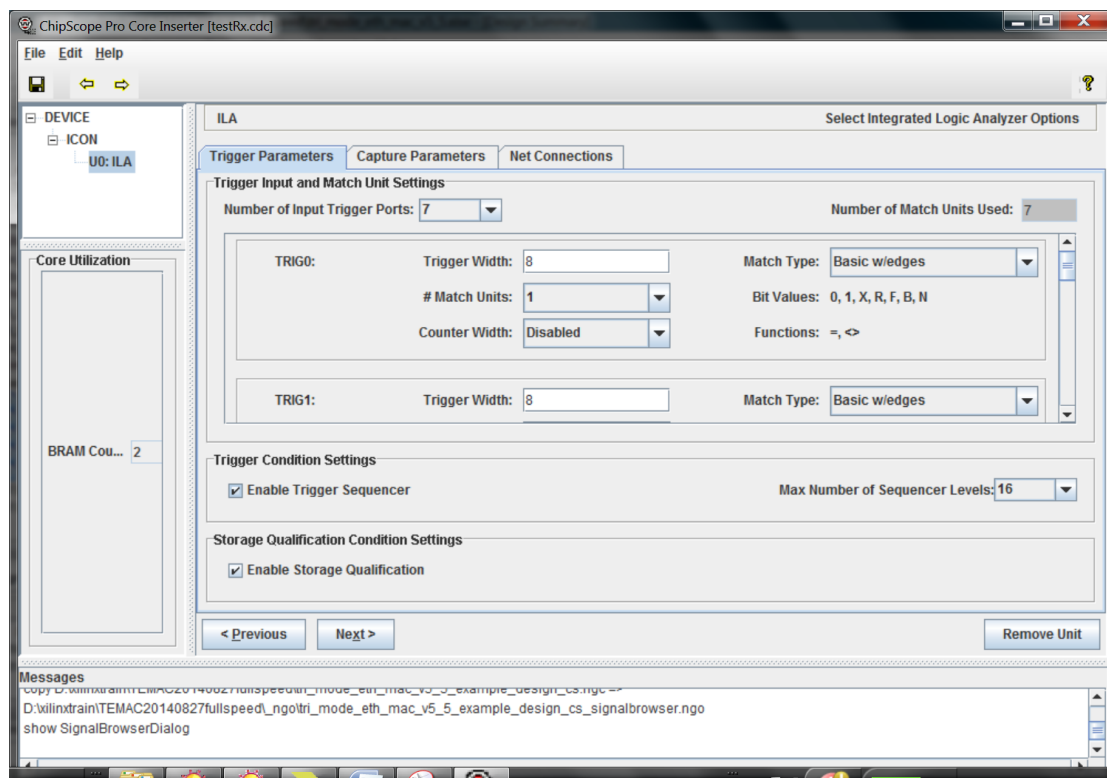
Above picture, testRx.cdc is used to test MAC RX.

(4) testRx.cdc configuration

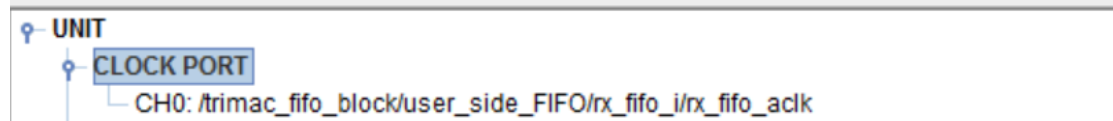
A:



B: Double *.cdc files icon→ ChipScope Pro Core Inserter[testRx.cdc]



C : ChipScope clk



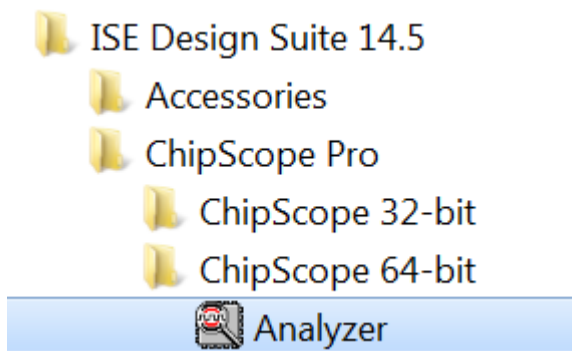
D: MAC RX data wave



E: MAC TX data wave



- (5) implement project
- (6) program
- (7) run Analyzer

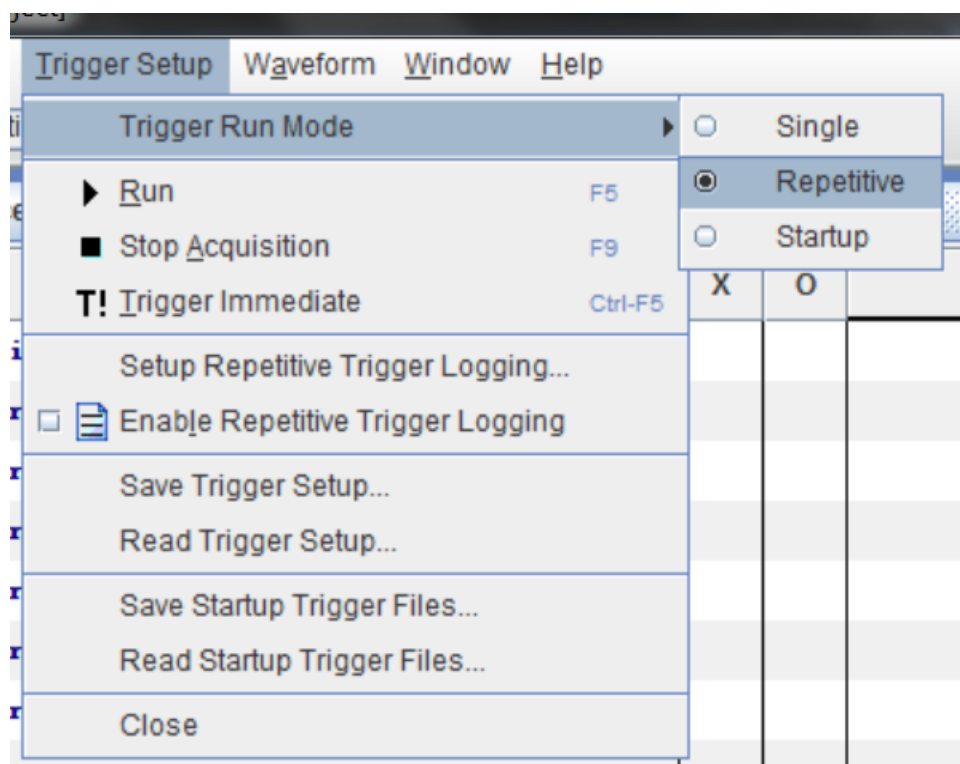


A: Set trigger value 01011010 to watch MAC Rx Because the format of packet which is omnipeek software sent is:

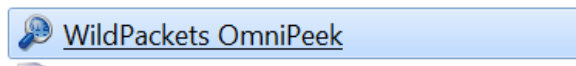
5a 01 02 03 04 05

Trigger Setup - DEV:0 MyDevice0 (XC7K325T) UNIT:0 MyILA0 (ILA)					
Match Unit	Function	Value	Radix	Counter	
M0:TriggerPort0	==	0101_1010	Bin	disabled	
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<0>		0			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<1>		1			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<2>		0			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<3>		1			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<4>		1			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<5>		0			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<6>		1			
/tramac_fifo_block/user_side_FIFO/rx_fifo_i/rx_axis_mac_tdata<7>		0			

B:

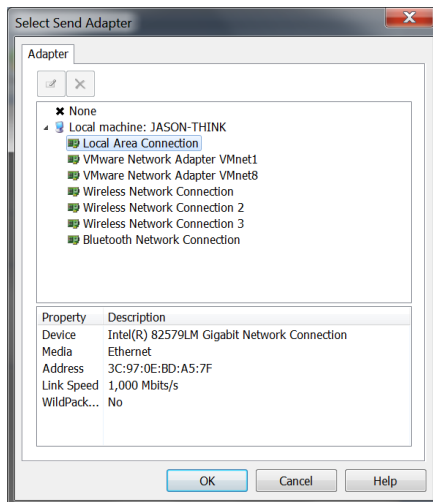


(8) OmniPeek software configuration to send packet

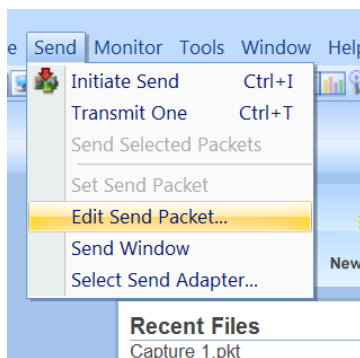


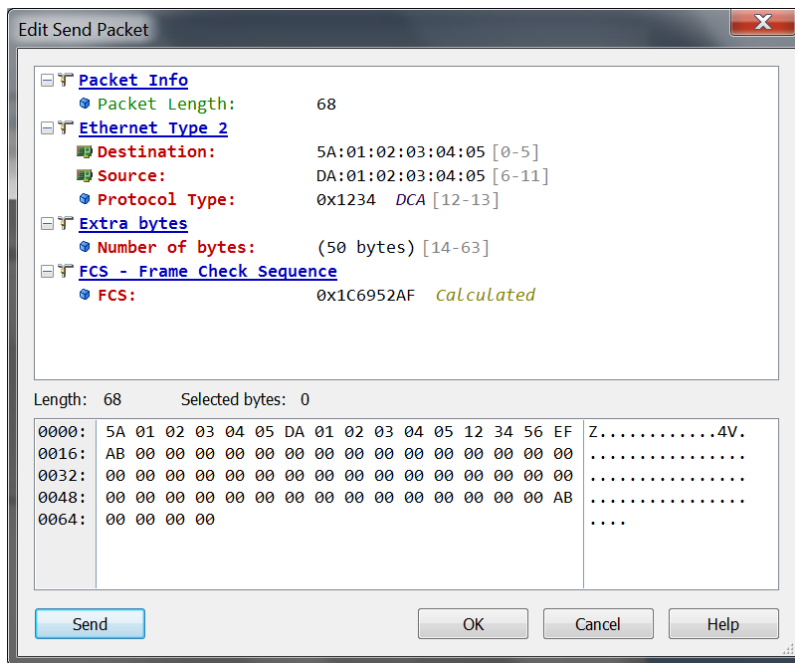
A: send configuration

I: select adapter

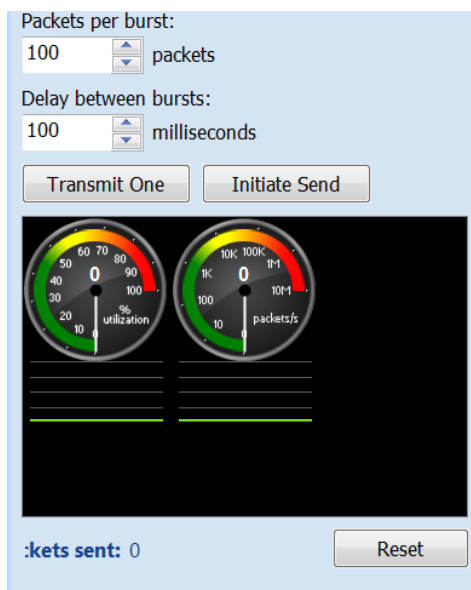


II:Edit Send Packet





III send packet



(9) Watch chipscope wave

