

에디로봇아카데미 임베디드 마스터 Lv1 과정

제 5기 2023. 05. 19 최성효

확인 사항



cltd, shr, lea 확인 필요

어셈블리어 분석(1)



ARM 어셈블리어

```
push
                             {r11, lr}
                             r11, sp, #4
                             sp, sp, #16
                             r3, #5
                             r3, [r11, #-8]
                             r3, #0
                             r3, [r11, #-16]
                             r3. #1
                             r3, [r11, #-12]
                             r3, #0
                             r3, r3, #1
                     rsblt r3, r3, #0
                             r3, #1
                             r3, r2, r3
                             r3, [r11, #-16]
 0x00010470 <+80>:
                             r3, r3, #1
                             r3, [r11, #-12]
                             r3, [r11, #-8]
                             0x10448 <main+40>
                             r0, [pc, #16] ; 0x104ac <main+140>
                             r3, #0
                             sp, r11, #4
-Type <RET> for more, q to quit, c to continue without paging--c
                             {r11, pc}
 0x000104ac <+140>: andeq r0, r1, r12, asr #10
```

코드

```
#include <stdio.h>
int main(void)
        int End=5;
        int sum=0;
        for (int i=1; i<=End; ++i){</pre>
                if(i%2==1){
                        sum+=i;
        printf("%d까지의 홀수의 합은 %d\n",End,sum);
        return 0;
```

어셈블리어 분석(2)

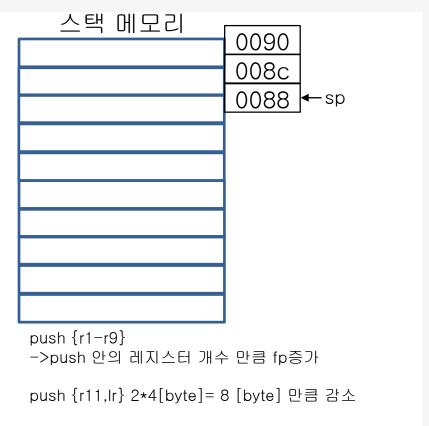


어셈블리어

```
{r11, lr}
                        push
) x 0 0 0 1 0 4 2 4 <+4>:
                        add
                                 rll, sp, #4
                                 sp, sp, #16
                        sub
                                 r3, #5
                        mov
                                 r3, [r11, #-8]
) x 0 0 0 1 0 4 3 0 <+16>:
                                 r3, #0
                                 r3, [r11, #-16]
                                 r3, #1
) x 0 0 0 1 0 4 3 c <+ 2 8 > :
                                 r3, [r11, #-12]
) x 0 0 0 1 0 4 4 8 < + 4 0 > :
                                 r3, #0
) x 0 0 0 1 0 4 4 c <+44>:
                        cmp
0x00010450 <+48>:
                                 r3, r3, #1
                       rsblt r3, r3, #0
                                 r3, #1
```

레지스터

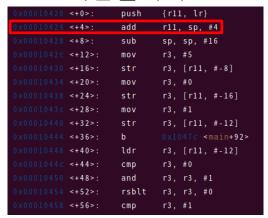
| r11 | 0 x 2 0 f 1 4 | 134932 |
|-----|---------------------|---------------------|
| r12 | 0x3ff97000 | 1073311744 |
| sp | 0x40800090 | 0x40800090 |
| | ↓ | |
| r11 | 0 x 2 0 f 1 4 | 134932 |
| r12 | 0x3ff97000 | 1073311744 |
| sn | 0 v 4 0 8 0 0 0 8 8 | 0 v 4 0 8 0 0 0 8 8 |



어셈블리어 분석(3)

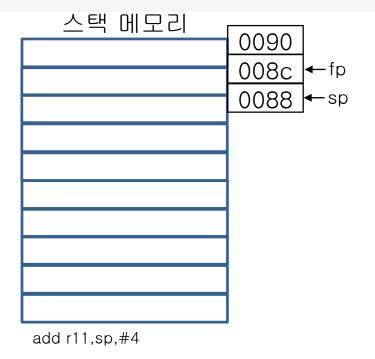


어셈블리어



레지스터

| r11 | 0x20f14 | 134932 |
|-----|------------|------------|
| r12 | 0x3ff97000 | 1073311744 |
| sp | 0x40800088 | 0x40800088 |
| | \ | |
| r11 | 0x4080008c | 1082130572 |
| r12 | 0x3ff97000 | 1073311744 |
| sp | 0x40800088 | 0x40800088 |



어셈블리어 분석(4)

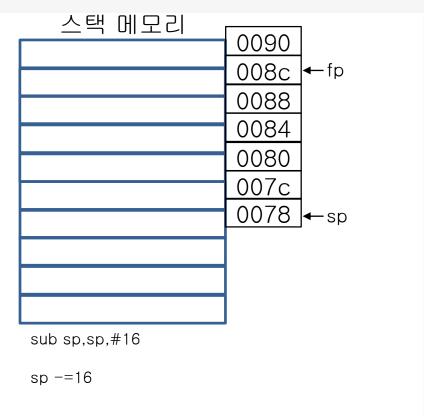


어셈블리어

```
push
                      add
                              r11, sp, #4
                              sp, sp, #16
                     sub
                              r3, #5
                      mov
                              r3, [r11, #-8]
                      mov
                              r3, #0
                              r3, [r11, #-16]
                              r3, #1
) x 0 0 0 1 0 4 3 c <+ 2 8 > :
                              r3, [r11, #-12]
)×00010448 <+40>:
                              r3, [r11, #-12]
                              r3, #0
) x 0 0 0 1 0 4 4 c <+44>:
                      cmp
)x00010450 <+48>:
                              r3, r3, #1
                     rsblt r3, r3, #0
                              r3, #1
```

레지스터

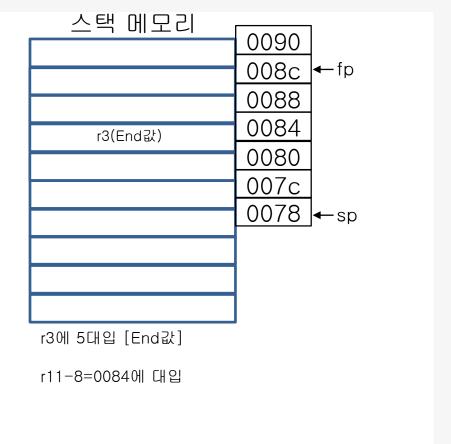




어셈블리어 분석(5)



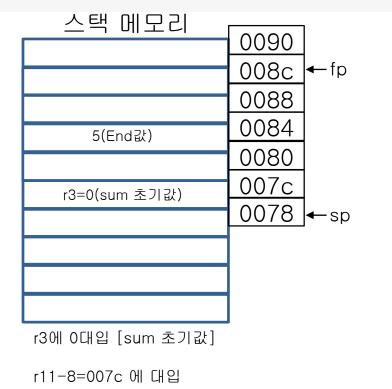
```
push
                      add
                               rl1, sp, #4
                      sub
x0001042c <+12>:
                              r3, #5
                      mov
                      mov
                               r3, #0
                              r3, [r11, #-16]
                              r3, #1
0x0001043c <+28>:
                              r3, [r11, #-12]
) x 0 0 0 1 0 4 4 8 < + 4 0 > :
                              r3, #0
) x 0 0 0 1 0 4 4 c <+44>:
                      cmp
0x00010450 <+48>:
                              r3, r3, #1
                     rsblt r3, r3, #0
                               r3, #1
```



어셈블리어 분석(6)



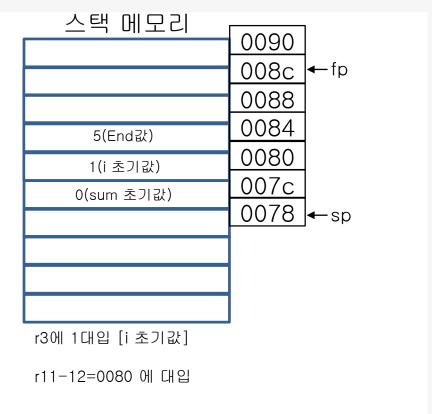
```
push
                     add
                             rl1, sp, #4
                             sp, sp, #16
                     sub
                             r3, #5
                     mov
                     mov
                             r3, #0
                             r3, [r11, #-16]
0x0001043c <+28>:
                             r3, #1
                             r3, [r11, #-12]
)×00010448 <+40>:
                             r3, [r11, #-12]
                             r3, #0
) x 0 0 0 1 0 4 4 c <+44>:
                     cmp
0x00010450 <+48>:
                             r3, r3, #1
                    rsblt r3, r3, #0
                             r3, #1
```



어셈블리어 분석(7)



```
push
                    add
                            r11, sp, #4
                            sp, sp, #16
                    sub
                            r3, #5
                    mov
                            r3, [r11, #-8]
                    mov
                            r3, #0
                            r3, [r11, #-16]
                            r3, #1
                    mov
                            r3, [r11, #-12]
                            0x1047c <main+92>
                    b
)×00010448 <+40>:
                            r3, #0
) x 0 0 0 1 0 4 4 c <+44>:
                    cmp
0x00010450 <+48>:
                            r3, r3, #1
                    rsblt r3, r3, #0
                            r3, #1
```



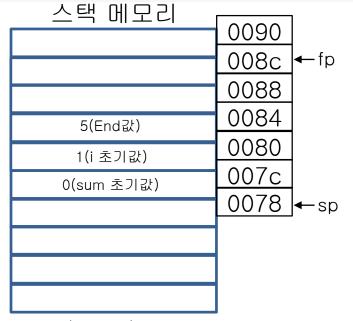
어셈블리어 분석(8)



어셈블리어

```
push
                               {r11, lr}
                       add
                               rl1, sp, #4
                               sp, sp, #16
                       sub
                               r3, #5
                       mov
                               r3, [r11, #-8]
 ) x 0 0 0 1 0 4 3 0 <+16>:
                               r3, #0
                       mov
                               r3, [r11, #-16]
                               r3, #1
 ) x 0 0 0 1 0 4 3 c <+ 2 8 > :
                               r3. [r11. #-12]
                               0x1047c <main+92>
                               r3, [r11, #-12]
 x00010448 <+40>:
                       ldr
                               r3, #0
 ) x 0 0 0 1 0 4 4 c <+44>:
                       cmp
 )x00010450 <+48>:
                               r3, r3, #1
                      rsblt r3, r3, #0
                               r3, #1
                       cmp
                                 r2, [r11, #-12]
x0001047c <+92>:
                       ldr
```

0x0001047c <+92>: ldr r2, [r11, #-12] 0x00010480 <+96>: ldr r3, [r11, #-8] 0x00010484 <+100>: cmp r2, r3 0x00010488 <+104>: ble 0x10448 <main+40>



r2에 1(i 초기값)대입 r3에 5(End값) 대입 r2, r3 비교 왼쪽(r2)가 오른쪽(r3)보다 작거나 같으면 점프

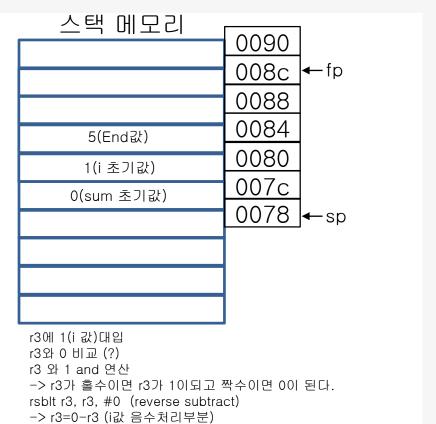
어셈블리어 분석(9)



어셈블리어

```
0x0001047c <+92>: ldr r2, [r11, #-12]
0x00010480 <+96>: ldr r3, [r11, #-8]
0x00010484 <+100>: cmp r2, r3
0x00010488 <+104>: ble 0x10448 <main+40>
```

```
x00010448 <+40>:
                      ldr
                               r3, [r11, #-12]
0x0001044c <+44>:
                               r3, #0
                      cmp
                               r3, r3, #1
0x00010450 <+48>:
                      and
                               r3, r3, #0
0x00010454 <+52>:
                      rsblt
0x00010458 <+56>:
                      cmp
                               r3, #1
0x0001045c <+60>:
                              0 x 1 0 4 7 0 < main + 8 0 >
                      bne
```



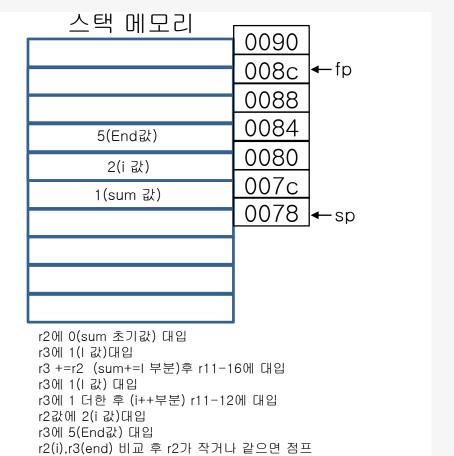
bne (r3와 1이 같지 않으면 점프)

-> i%2==1 부분 , r3가 1과 같음으로 계속 진행

어셈블리어 분석(10)



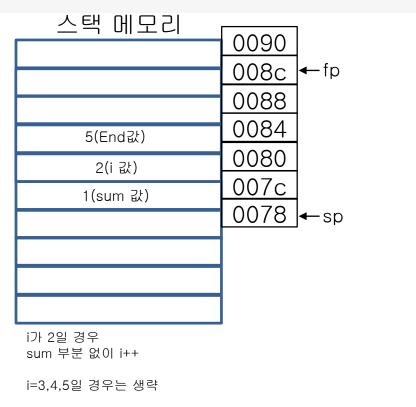
```
x00010448 <+40>:
                                                                                                                                                                                                                            ldr
                                                                                                                                                                                                                                                                                                               r3, [r11, #-12]
                                                                                                                                                                                                                                                                                                              r3, #0
     0x0001044c <+44>:
                                                                                                                                                                                                                            cmp
    0×00010450 <+48>:
                                                                                                                                                                                                                                                                                                              r3, r3, #1
                                                                                                                                                                                                                            and
                                                                                                                                                                                                                                                                                                            r3, r3, #0
    0×00010454 <+52>:
                                                                                                                                                                                                                          rsblt
     0x00010458 <+56>:
                                                                                                                                                                                                                                                                                                              r3, #1
                                                                                                                                                                                                                            cmp
                                                                                                                                                                                                                                                                                                           0x10470 <main+80>
  0x0001045c <+60>:
                                                                                                                                                                                                                         bne
 0 \times 0 \times 0 \times 10 \times 10^{-4} = 0 \times 10^{-4} = 0
                                                                                                                                                                                                                                                                                                              r2, [r11, #-16]
                                                                                                                                                                                                                            ldr
  ldr
                                                                                                                                                                                                                                                                                                              r3, [r11, #-12]
 0 \times 0 0 0 1 0 4 6 8 < +72 > :
                                                                                                                                                                                                                            add
                                                                                                                                                                                                                                                                                                              r3, r2, r3
                                                                                                                                                                                                                                                                                                            r3, [r11, #-16]
 0 \times 0001046c < +76>:
                                                                                                                                                                                                                            str
  0x00010470 <+80>:
                                                                                                                                                                                                                                                                                                              r3, [r11, #-12]
                                                                                                                                                                                                                            ldr
 0×00010474 <+84>:
                                                                                                                                                                                                                            add
                                                                                                                                                                                                                                                                                                              r3, r3, #1
 0x00010478 <+88>:
                                                                                                                                                                                                                                                                                                              r3, [r11, #-12]
                                                                                                                                                                                                                            str
 0 \times 0 \times 0 \times 10 \times 10^{-4} = 0 \times 10^{-4} \times 10^{-4} = 0
                                                                                                                                                                                                                                                                                                              r2, [r11, #-12]
                                                                                                                                                                                                                            ldr
0 \times 0 \times 0 \times 10 \times 480 < +96>:
                                                                                                                                                                                                                            ldr
                                                                                                                                                                                                                                                                                                              r3, [r11, #-8]
  cmp
                                                                                                                                                                                                                                                                                                              r2, r3
  0x00010488 <+104>:
                                                                                                                                                                                                                            ble
                                                                                                                                                                                                                                                                                                              0x10448 <main+40>
```



어셈블리어 분석(11)



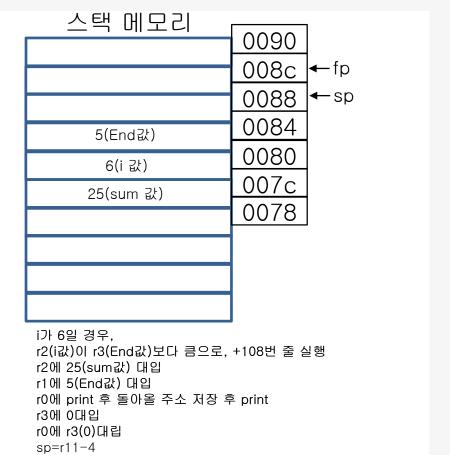
```
x00010448 <+40>:
                     ldr
                             r3, [r11, #-12]
0x0001044c <+44>:
                             r3, #0
                     cmp
0x00010450 <+48>:
                             r3, r3, #1
                     and
0x00010454 <+52>:
                     rsblt
                            r3, r3, #0
0x00010458 <+56>:
                             r3, #1
                     cmp
                            0x10470 <main+80>
0x0001045c <+60>:
                     bne
0x00010460 <+64>:
                     ldr
                             r2, [r11, #-16]
                             r3, [r11, #-12]
0x00010464 <+68>:
                     ldr
0×00010468 <+72>:
                     add
                             r3, r2, r3
0x0001046c <+76>:
                     str
                             r3, [r11, #-16]
0×00010470 <+80>:
                     ldr
                             r3, [r11, #-12]
0×00010474 <+84>:
                             r3, r3, #1
                     add
0x00010478 <+88>:
                             r3, [r11, #-12]
                     str
0x0001047c <+92>:
                             r2, [r11, #-12]
                     ldr
0x00010480 <+96>:
                     ldr
                             r3, [r11, #-8]
0x00010484 <+100>:
                     cmp
                             r2, r3
x00010488 <+104>:
                             0x10448 <main+40>
                     ble
```



어셈블리어 분석(12)



```
ldr
                           r2, [r11, #-16]
                          r3, [r11, #-12]
x00010464 <+68>:
                   ldr
                          r3, [r11, #-16]
                          r3, [r11, #-12]
x00010470 <+80>:
                   ldr
                   add
                          r3, r3, #1
                          r3, [r11, #-12]
                          r2, [r11, #-12]
                   ldr
                          r3, [r11, #-8]
                   ldr
                          r2, r3
x00010484 <+100>: cmp
                   ble
                          r2, [r11, #-16]
                   ldr
                          r1, [r11, #-8]
                   ldr
                          r0, [pc, #16] ; 0x104ac <main+140>
                  ldr
                          r3, #0
                   mov
                          sp, r11, #4
                  sub
)x000104a8 <+136>: pop
                         {r11, pc}
x000104ac <+140>: andeq r0, r1, r12, asr #10
```



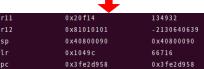
어셈블리어 분석(13)

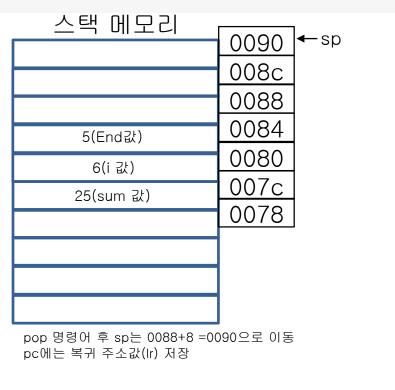


어셈블리어

```
ldr
                           r2, [r11, #-16]
                           r3, [r11, #-12]
x00010464 <+68>:
                   ldr
                   add
                           r3, [r11, #-16]
                           r3, [r11, #-12]
x00010470 <+80>:
                   ldr
x00010474 <+84>:
                   add
                           r3, r3, #1
x00010478 <+88>:
                           r3, [r11, #-12]
                           r2, [r11, #-12]
                   ldr
                   ldr
                           r3, [r11, #-8]
                   ble
                           0x10448 <main+40>
                   ldr
                           r2, [r11, #-16]
                   ldr
                           r1, [r11, #-8]
                           r0, [pc, #16] ; 0x104ac <main+140>
                   ldr
                   bl
                           r3, #0
                   mov
                           sp, r11, #4
                   sub
                           {r11, pc}
                   andeq r0, r1, r12, asr #10
```

r11 0x4080008c 1082130572 r12 0x81010101 -2130640639 sp 0x40800088 0x40800088 lr 0x1049c 66716 pc 0x104a8 0x104a8 <main+136>





andeq??????