UEFI & EDK II TRAINING
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SUGGESTED SCHEDULE

The next slides will have links to the appropriate slide show in the suggested order.

- **Unit 1:** Overview of EDK II, UEFI Shell Lab, and overview of UEFI aware OS
- **Unit 2:** More details of the EDK II infrastructure and Labs with writing UEFI Applications and Drivers
- **Unit 3:** Porting and Debugging using a Open Source example with EDK II
- **Unit 4:** Advanced topics for a shorter session after Units 1-3 completed

SUGGESTED SCHEDULE – UNIT 1

* Overview UEFI Boot Flow

* EDK II Build Process and Build Spec Files

* EDK II Open Source Platforms

* EDK II Platform Build LABs (Ovmf | Max HW setup | Max)

  OR EDK II Platform Build Windows (Nt32 | Max HW setup | Max)

* UEFI Shell Overview & Shell LAB Or Nt32 Shell Lab

* UEFI Aware OS , Secure Boot , Smm/MMI , coreboot

SUGGESTED SCHEDULE – UNIT 2

* EDK II Libraries and Modules and Drivers

* EDK II Platforms Configuration Database (PCD)

* UEFI Application Writers LAB – Win Lab

* UEFI Driver Model   - UEFI Driver Wizard and Porting Lab

    Windows   - UEFI Driver Wizard and Porting Lab

SUGGESTED SCHEDULE – UNIT 3

★ Porting an Existing Platform w/ EDK II example

★ Porting a New Board (example Apollo Lake)

★ Porting Beyond the Shell w/ EDK II

★ EDK II Debugging Presentation and Lab - Windows Lab

★ Source Level Debugging w/ Intel® UDK Debugger

SUGGESTED SCHEDULE – UNIT 4

🟢 UEFI / EDK II Network - EDK II Network Features , UEFI Protocols for EDK II , HTTP(s) Boot

🔵 UEFI / EDK II Platform Firmware Security

🟡 UEFI Capsule Update

🟣 UEFI / EDK II - HII Overview and Lab

Linux Lab Guide

Windows Lab Guide

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Questions?

SECTION EXAMPLE

This is an example of a section divider

SUMMARY

- Point 1
- Point 2
- Point 3
- Point 4