

# EE2301 - ELECTRONIC DEVICES AND CIRCUITS LAB FINAL PROJECT MICROPHONE AMPLIFIER WITH CLASS AB DRIVER

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## 1 Introduction

Audio amplification is a fundamental aspect of analog electronics design. This project aims to construct a functional audio chain that takes a low-level signal from a microphone (or AUX input), amplifies it through a pre-amplifier stage, and drives a load using a Class AB power stage. The design was improved step by step to solve problems such as overheating, distortion when the signal crosses zero, and unwanted noise. The final result is a reliable amplifier that can safely drive an 8-ohm speaker while keeping distortion under control.

## 2 Theory of Audio Amplification

Audio amplification is typically achieved in stages, each designed to solve a specific problem in the signal chain. A practical audio system almost always consists of a *pre-amplifier* followed by a *power amplifier*. This separation improves signal quality, efficiency, and reliability.

### 2.1 Audio Pre-Amplifier

An audio pre-amplifier is the first active stage of amplification. Its primary role is to amplify very small input signals, such as those from microphones or audio sources, to a usable voltage level without significantly increasing noise or distortion.

The pre-amplifier focuses on:

- High voltage gain
- High input impedance (to avoid loading the source)

- Low noise and faithful signal reproduction

At this stage, the signal is shaped, conditioned, and prepared for further amplification. However, a pre-amplifier cannot deliver sufficient current or power to drive a loudspeaker directly.

## 2.2 Power Amplifier Classes

Power amplifiers are designed to deliver sufficient current and power to low-impedance loads such as speakers. Based on the conduction angle of the output devices, power amplifiers are classified as Class A, Class B, and Class AB.

### 2.2.1 Class A Amplifier

In a Class A amplifier, the active device conducts for the entire  $360^\circ$  of the input signal cycle. This results in excellent linearity and minimal distortion.

However, Class A operation suffers from:

- Very low efficiency
- Continuous power dissipation
- Significant heat generation

As a result, Class A amplifiers are impractical for medium- and high-power audio applications.

### 2.2.2 Class B Amplifier

In a Class B amplifier, two complementary devices are used, each conducting for  $180^\circ$  of the signal cycle. This improves efficiency significantly compared to Class A operation.

The main drawback of Class B amplifiers is *crossover distortion*, which occurs near the zero-crossing point when neither device conducts adequately.

### 2.2.3 Class AB Amplifier

Class AB amplification combines the advantages of Class A and Class B operation. Each output device conducts for slightly more than  $180^\circ$ , ensuring a small overlap around the zero-crossing region.

This results in:

- Greatly reduced crossover distortion
- Improved efficiency compared to Class A
- Lower thermal stress compared to pure Class A operation

Because of this balance between efficiency and audio fidelity, Class AB amplifiers are widely preferred in audio power amplifier designs.

## 2.3 Why Both a Pre-Amplifier and Class AB Power Stage Are Needed

A single amplifier stage cannot efficiently satisfy all audio requirements. The pre-amplifier and Class AB power amplifier serve complementary roles:

- The pre-amplifier boosts weak audio signals and preserves signal quality.
- The Class AB power amplifier provides the necessary current and power to drive the speaker with low distortion.

Together, they ensure that the audio signal is amplified accurately, efficiently, and with sufficient power to produce clear and undistorted sound from the loudspeaker.

## 3 Circuit Description

The complete audio amplifier consists of two main sections: a transistor-based pre-amplifier and a Class AB power amplifier. Each section performs a distinct function in amplifying the input audio signal.

### 3.1 Pre-Amplifier Section

The pre-amplifier amplifies the low-level input signal before it is fed to the power stage. The input audio signal is first applied through a coupling capacitor ( $C_1$ ), which blocks any DC component from the source.

Transistor  $Q_1$  (BC547) operates as the initial amplification stage. Its biasing network sets a stable operating point, allowing small variations in the input signal to be amplified. The amplified signal is further processed by transistor  $Q_2$  (BC547), which provides additional voltage gain and improves signal strength.

Capacitors  $C_2$  and  $C_3$  are used for AC coupling between stages, ensuring that only the audio signal is transferred while DC biasing remains isolated. The output of this section, labeled  $V_{out\_preamp}$ , provides sufficient voltage gain to drive the Class AB stage.

### 3.2 Class AB Power Amplifier Section

The Class AB stage is responsible for delivering the required current to drive the loudspeaker. The input from the pre-amplifier is coupled through capacitor  $C_4$  to the driver transistor  $Q_3$  (2N3904), which prepares the signal for the output stage.

Diodes  $D_1$  and  $D_2$  provide the necessary biasing voltage between the output transistors, ensuring Class AB operation. This bias keeps both output transistors slightly conducting and minimizes crossover distortion.

The output stage consists of a complementary power-transistor pair:  $Q_4$  (TIP41C) for the positive half-cycle and  $Q_5$  (TIP42C) for the negative half-cycle. Emitter resistors ( $R_{10}$  and  $R_{11}$ ) improve thermal stability and limit excessive current.

### 3.3 Output Stage

The amplified signal is passed to the loudspeaker through the large coupling capacitor  $C_5$ . This capacitor blocks DC voltage from reaching the speaker while allowing the amplified AC audio signal to pass. The  $8\Omega$  speaker converts the electrical signal into sound.

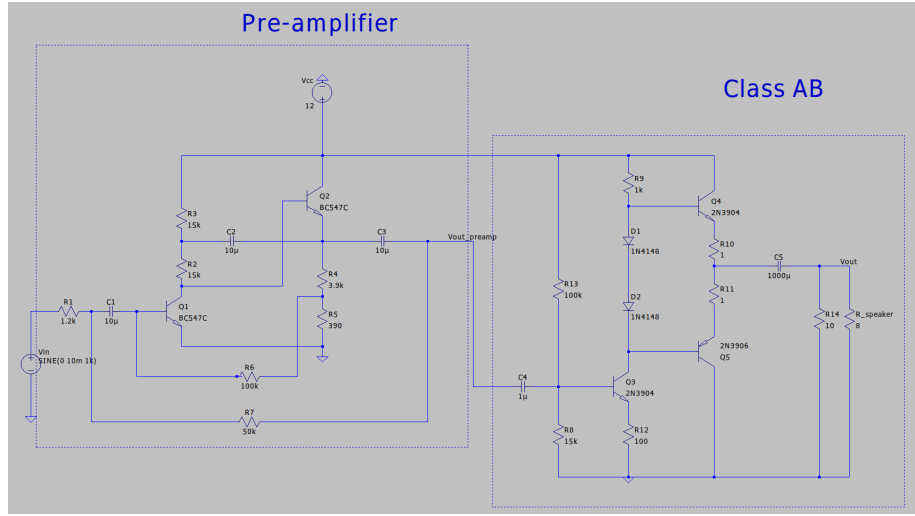


Figure 1: Circuit diagram

## 4 Output

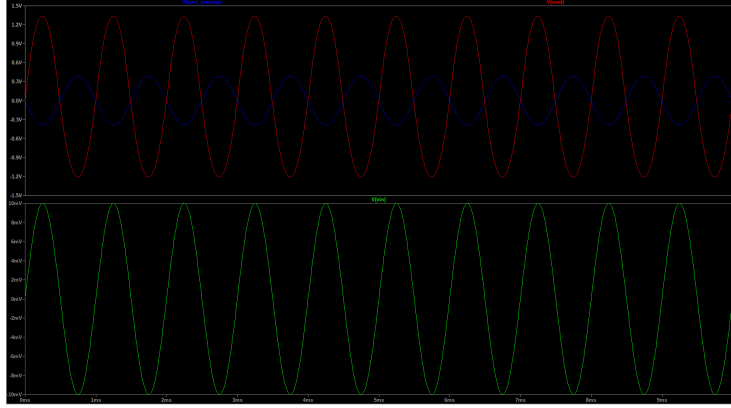


Figure 2: Simulated output diagram

## 5 Measured Parameters

### 5.1 Total Harmonic Distortion ( THD )

Total Harmonic Distortion (THD) is a measure of the distortion introduced by a system when amplifying a signal. It quantifies how much the output waveform deviates from an ideal sinusoid due to the presence of harmonic frequency components generated by non-linearities in the circuit.

When a pure sinusoidal signal of frequency  $f_1$  is applied to an amplifier, an ideal system would produce an output containing only the same frequency. In practical amplifiers, additional frequency components appear at integer multiples of the fundamental frequency, known as harmonics ( $2f_1, 3f_1, \dots$ ). THD expresses the combined effect of these harmonics relative to the fundamental component.

#### 5.1.1 Mathematical Definition

THD is defined as the ratio of the root-mean-square (RMS) value of all harmonic components to the RMS value of the fundamental frequency component:

$$\text{THD} = \frac{\sqrt{V_2^2 + V_3^2 + \dots + V_n^2}}{V_1},$$

where  $V_1$  is the RMS voltage of the fundamental component and  $V_2, V_3, \dots, V_n$  are the RMS voltages of the higher-order harmonics.

THD is commonly expressed as a percentage:

$$\text{THD}(\%) = \frac{\sqrt{V_2^2 + V_3^2 + \dots + V_n^2}}{V_1} \times 100.$$

### 5.1.2 Calculated THD

We apply the FFT option present in the oscilloscope to look at the harmonics, and use the cursors and measure options to measure their value. The calculated THD becomes,

$$\begin{aligned} THD &\approx \frac{V_2}{V_1} \\ &= \frac{80}{2300} = 0.034 \end{aligned}$$

## 5.2 Noise in Audio Amplifiers

Noise refers to unwanted random signals that are superimposed on the desired audio signal and degrade the overall sound quality. In audio amplifiers, noise limits the smallest signal that can be amplified clearly and directly affects the signal-to-noise ratio (SNR) of the system.

Common sources of noise in audio circuits include thermal noise generated by resistors, shot noise in semiconductor devices, power supply ripple, and external electromagnetic interference. These noise components are generally random in nature and spread over a wide range of frequencies.

### 5.2.1 Noise Measurement

Noise is typically quantified as the root-mean-square (RMS) value of the unwanted voltage or current measured at the output of the amplifier in the absence of an input signal. For voltage noise, it is expressed as:

$$V_{\text{noise}} = \sqrt{\frac{1}{T} \int_0^T v_n^2(t) dt},$$

where  $v_n(t)$  is the instantaneous noise voltage and  $T$  is the observation time.

From the diagram, the RMS value of noise is 34.8 mV .

### 5.2.2 Signal-to-Noise Ratio (SNR)

The effect of noise is commonly evaluated using the Signal-to-Noise Ratio (SNR), defined as the ratio of the RMS signal voltage to the RMS noise voltage:

$$\text{SNR} = \frac{V_{\text{signal}}}{V_{\text{noise}}}.$$

0.18

Tektronix

TBS 2000B SERIES DIGITAL OSCILLOSCOPE

Tek Stop

Cursor

Frequency

Magnitude

Screen

Link

On Off

Bring Cursor on Screen

Source: CH1  
Vertical Scale: 0.5V

2.30V

$\Delta$ 2.00V

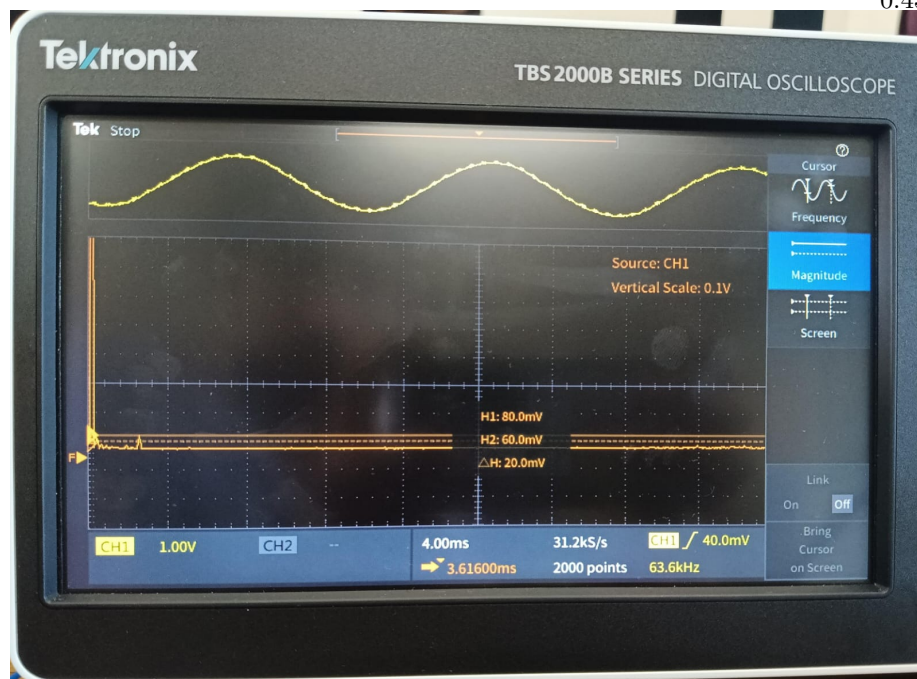
300mV

CH1 1.00V CH2 --

4.00ms 31.2KS/s CH1 / 40.0mV

3.61600ms 2000 points 36.3kHz

0.45



7

Figure 5: THD

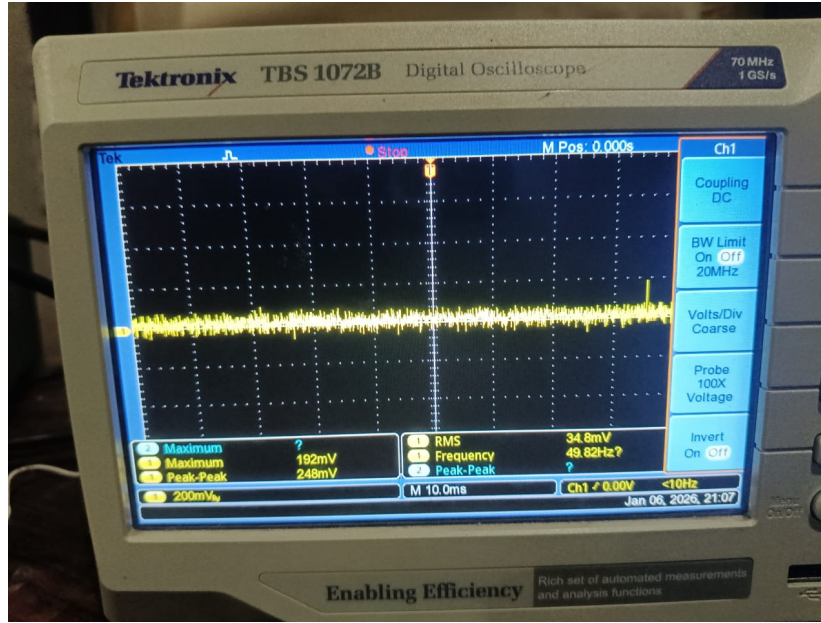


Figure 6: Measuring Noise

SNR is usually expressed in decibels (dB):

$$SNR_{dB} = 20 \log_{10} \left( \frac{V_{signal}}{V_{noise}} \right).$$

From the output diagram, it can be seen that

$$\begin{aligned} V_{signal} &= 1.5V \\ V_{noise} &= 34.8mV \\ \Rightarrow SNR &= 20 \log_{10} \left( \frac{V_{signal}}{V_{noise}} \right) \\ &= 20 \log_{10} \left( \frac{1500}{34.8} \right) \approx 33dB \end{aligned}$$



### 5.3 Power Supply Rejection Ratio (PSRR)

Power Supply Rejection Ratio (PSRR) indicates how effectively an amplifier suppresses variations or noise present on its power supply from appearing at the output.

#### 5.3.1 Measurement Method

To measure PSRR, the input of the amplifier is grounded and a small AC voltage is superimposed on the DC supply. Any resulting AC component at the output is due to power supply coupling.

#### 5.3.2 Calculation

PSRR is defined as the ratio of the applied supply ripple to the resulting output ripple:

$$\text{PSRR} = \frac{\Delta V_{\text{supply}}}{\Delta V_{\text{out}}}$$

and is usually expressed in decibels as:

$$\text{PSRR}_{\text{dB}} = 20 \log_{10} \left( \frac{\Delta V_{\text{supply}}}{\Delta V_{\text{out}}} \right).$$

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Figure 7: AC ripple injection network for PSRR measurement. The inductor isolates the DC source from AC components, while the capacitor blocks DC and injects AC ripple at the supply node.

A 10 mV AC ripple is given.

$$\begin{aligned} \text{PSRR} &= 20 \log_{10} \left( \frac{800}{10} \right) \\ &= 38 \text{ dB} \end{aligned}$$

### 5.4 Quiescent Current

Quiescent current ( $I_Q$ ) is the small current that flows through an amplifier when no input signal is applied. It sets the operating point of the active devices and ensures that the amplifier operates in its linear region.

In a Class AB amplifier, the quiescent current keeps both output transistors slightly conducting around the zero-crossing region. This reduces crossover distortion while maintaining much higher efficiency than Class A operation.

From the diagram, it can be seen that the Quiescent current,  $I_Q$  is 22 mA.

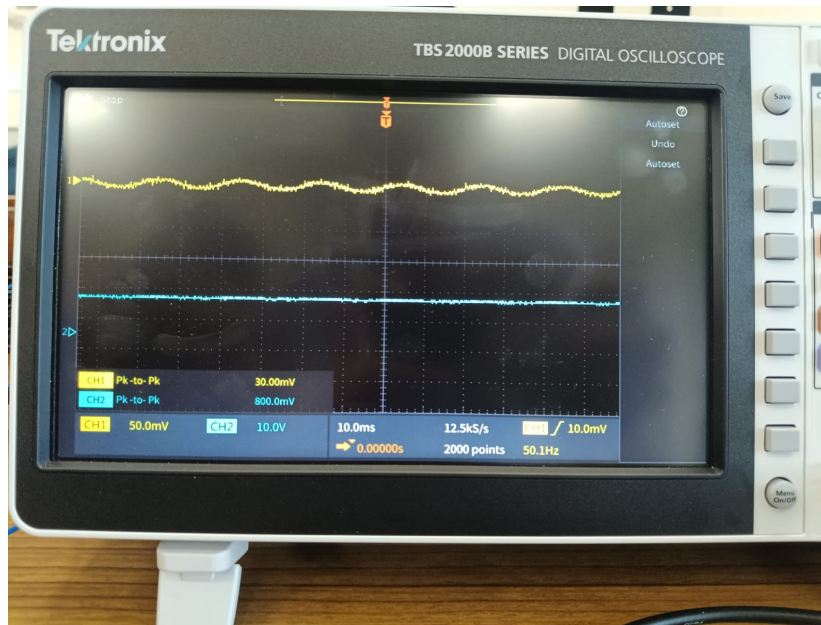


Figure 8: Calculated PSRR value



Figure 9: Calculation of Quiescent current

## 5.5 Gain

Gain represents the factor by which an amplifier increases the amplitude of an input signal. In audio amplifiers, voltage gain is of primary interest and is

defined as:

$$A_v = \frac{V_{out}}{V_{in}}$$

Gain is often expressed in decibels (dB):

$$A_v(\text{dB}) = 20 \log_{10} \left( \frac{V_{out}}{V_{in}} \right)$$

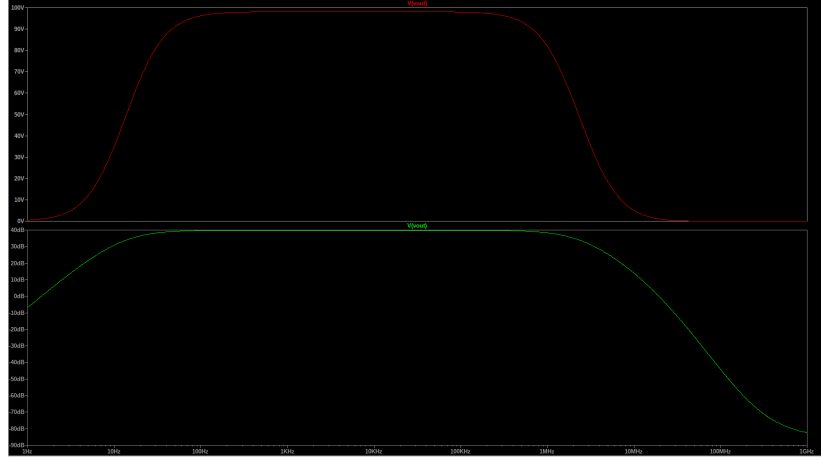


Figure 10: Bode plots to calculate gain and bandwidth

From the graph, the gain can be seen to be around 97-98 (  $\approx 40$  dB ) .

## 5.6 Bandwidth

Bandwidth is the range of frequencies over which the amplifier maintains an approximately constant gain. It is defined by the lower and upper cutoff frequencies,  $f_L$  and  $f_H$ , where the gain falls by 3 dB from its mid-band value.

$$\text{Bandwidth (BW)} = f_H - f_L$$

From the graph, it can be seen that the bandwidth is approximately 20 Hz to 1 MHz .

## 5.7 Output Swing and Output Power

Output swing and output power describe the maximum signal level an amplifier can deliver to a load without distortion or clipping.

Output swing is the maximum peak-to-peak voltage that appears at the amplifier output while remaining within the linear operating region. It is limited by the supply voltage and the voltage drops across the output devices.

Output power is the average power delivered to the load .

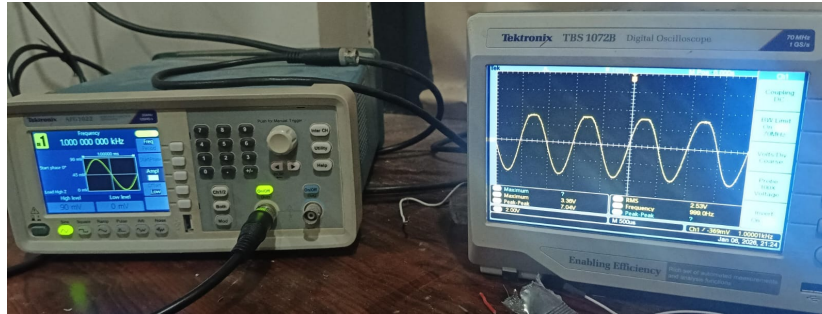


Figure 11: Calculating output swing and output power - It can be seen that the output waveform gets clipped

From the diagram it can be seen that the output swing is 90 mV. The output voltage corresponding to the output swing is  $\approx 7V_{pp}$ . Hence the output power is  $\frac{7*7}{8*8} \approx 3.06W$

## 6 Conclusion

A microphone amplifier using a voltage pre-amplifier and a Class AB power stage was successfully designed and analyzed. The circuit amplifies a low-level microphone signal and drives an  $8\Omega$  load with stable operation and controlled distortion. The required parameters are measured, and compared with those of commercial amplifiers.

## 7 Circuit diagram credits

- Pre-amplifier - <https://sound-au.com/project13.htm>
- Power amplifier - <https://www.electronics-tutorials.ws/amplifier/class-ab-amplifier.html>

## 8 Demonstration

The video demonstration of the project and the files related to the project can be found on the following github page  
<https://github.com/EE24BTECH11012/Mic-Amplifier>